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How hard is it to verify flat affine counter systems with the finite monoid property?

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Abstract. We study several decision problems for counter systems with guards defined by convex polyhedra and updates defined by affine transformations. In general, the reachability problem is undecidable for such systems. Decidability can be achieved by imposing two restrictions: (1) the control structure of the counter system is flat, meaning that nested loops are forbidden, and (2) the multiplicative monoid generated by the affine update matrices present in the system is finite. We provide complexity bounds for several decision problems of such systems, by proving that reachability and model checking for Past Linear Temporal Logic stands in the second level of the polynomial hierarchy $\Sigma^P_2$, while model checking for First Order Logic is PSPACE-complete.

1 Introduction

Counter systems are finite state automata extended with integer variables, also known as counter automata or counter machines. These are Turing-complete models of computation, often used to describe the behavior of complex real-life systems, such as embedded/control hardware and/or software systems. Because many verification problems, of rather complex systems, can be reduced to decision problems for counter systems, it is important to understand the difficulties faced by potential verification algorithms designed to work with the latter.

Due to their succinctness and expressive power, most decision problems, such as reachability, termination and temporal logic model-checking, are undecidable for counter systems, even when the operations on the counters are restricted to increment, decrement and zero-test \cite{24}. This early negative result motivated the search for subclasses with decidable decision problems. Such classes include one-counter systems \cite{14}, vector addition systems with states \cite{22}, reversal-bounded counter machines \cite{15} and flat counter systems \cite{4,12}.

Flat counter systems are defined by a natural syntactic restriction, which requires that no state occurs in more than one simple cycle in the control flow graph of the system. Decidability results on the verification of reachability problems for flat counter systems have been obtained by proving that, under certain restrictions on the logic that defines the transition rules, the set of reachable configurations is semilinear and effectively definable in Presburger arithmetic \cite{4,12,24}. Even though flatness is an important restriction (few counter systems
modeling real-life hardware and software artifacts are actually flat), this class provides the grounds for a useful method that under-approximates the set of behaviors of a non-flat counter system by larger and larger sets of paths described by flat counter systems. This method is currently used by model checking tools, such as FAST [2] and Flata [19], and has been applied to improve the results of static analysis [13], as well as the convergence of counterexample-driven abstraction refinement algorithms [17]. Moreover, several works define classes of flatable counter systems, for which there exist flat unfoldings of the system with identical reachability sets. Such is the case of timed automata [7] and 2-dimensional vector addition systems with states [21,3]. For these systems, the method of under-approximations by flat unfoldings is guaranteed to terminate.

In general, the flatness restriction is shown to reduce the computational complexity of several decision problems, such as reachability or temporal logic model checking. For instance, in the case of Kripke structures, flatness reduces the complexity of the model-checking of Linear Temporal Logic (LTL) from PSPACE to NP [20]. When considering flat counter systems whose updates are described by translations, the complexity of these problems drops from undecidable to NP-complete [10], while model checking for First Order Logic (FO) is coined to be PSPACE-complete [8]. For branching time temporal logics, flatness yields decidable problems, but with less remarkable complexity bounds [9].

In this work, we focus on the model of affine counter systems, in which each transition is labeled with (i) a guard defined by (a disjunction of) convex polyhedra, i.e. linear systems of inequalities of the form $C \cdot x \leq d$, and (ii) a deterministic update defined by an affine transformations $f(x) = A \cdot x + b$ where $A, C \in \mathbb{Z}^{n \times n}$ are square matrices with integer entries, $b, d \in \mathbb{Z}^n$ are vectors of integer constants and $x = [x_1, \ldots, x_n]$ is a vector of counters. For such systems, the set of reachable configurations is semilinear (thus reachability is decidable), provided that the multiplicative monoid generated by the matrices used in update functions is finite. This condition is also known as the finite monoid property [4,12]. Moreover, it has been shown that the model-checking of such systems, for an extended version of the branching time logic CTL* is decidable, also by reduction to the satisfiability of a Presburger formula, of size exponential in the size of the counter system [11].

In this work, we show that for flat affine counter systems with the finite monoid property, reachability and model checking for Past LTL are $\Sigma^P_2$, whereas model checking for FO is PSPACE-complete. Our result generalizes the results for flat counter systems with translations [8,10], since these systems are a strict subclass of flat affine counter systems with the finite monoid property. For instance, a transfer of values between different counters can be done in one step with an affine counter system, whereas a translating counter system would need a cycle to implement such operations. Our proof technique is based on an analysis of the behavior of the sequence of matrix powers in a finite multiplicative monoid, and adapts several techniques for translating counter systems to this more general case.

Due to lack of space, omitted proofs can be found in [18].
2 Counter Systems and their Decision Problems

We denote by \( \mathbb{N} \) and \( \mathbb{Z} \) the sets of natural and integer numbers, respectively. We write \([\ell, u]\) for the integer interval \( \{\ell, \ell + 1, \ldots, u\} \), where \( \ell \leq u \), and \( \text{abs}(n) \) for the absolute value of the integer \( n \in \mathbb{Z} \). The cardinality of a finite set \( S \) is denoted by \( \|S\| \).

We denote by \( \mathbb{Z}^{n \times m} \) the set of matrices with \( n \) rows and \( m \) columns, where \( A[i] \) is the \( i \)-th column and \( A[i][j] \) is the entry on the \( i \)-th row and \( j \)-th column of \( A \in \mathbb{Z}^{n \times m} \), for each \( i \in [1, n] \) and \( j \in [1, m] \). If \( n = m \), we call this number the dimension of \( A \), and we denote by \( I_n \) the identity matrix in \( \mathbb{Z}^{n \times n} \). For \( A \in \mathbb{Z}^{n \times m} \) and \( B \in \mathbb{Z}^{m \times p} \), we denote by \( A \cdot B \in \mathbb{Z}^{n \times p} \) the matrix product of \( A \) and \( B \).

For a matrix \( A \in \mathbb{Z}^{n \times n} \), we define \( A^0 = I_n \) and \( A^i = A^{-1} \cdot A \), for all \( i > 0 \).

We write \( \mathbb{Z}^n \) for \( \mathbb{Z}^{n \times 1} \) in the following. Each \( v \in \mathbb{Z}^n \) is a column vector, where \( v[i] \) is the entry on its \( i \)-th row. For a vector \( x \) of variables of length \( n \) and a matrix \( A \in \mathbb{Z}^{m \times n} \), the product \( A \cdot x \) is the vector of terms \( (A \cdot x)[i] = \sum_{j=1}^{m} A[i][j] \cdot x[j] \), for all \( i \in [1, m] \). A row vector is denoted by \( v = [v_1, \ldots, v_n] \in \mathbb{Z}^{1 \times n} \). For a row vector \( v \), we denote its transpose by \( v^\top \).

For a vector \( v \in \mathbb{Z}^n \), we consider the standard infinity \( \|v\|_{\infty} = \max_{i=1}^{n} \text{abs}(v[i]) \) norm. Given \( A \in \mathbb{Z}^{m \times n} \), consider the induced \( \|A\|_{\infty} = \max_{i=1}^{m} \sum_{j=1}^{n} \text{abs}(A[i][j]) \), and the maximum \( \|A\|_{\max} = \max_{i=1}^{m} \max_{j=1}^{n} \text{abs}(A[i][j]) \) norms. The size of a matrix is \( \text{size}(A) = \sum_{i=1}^{m} \sum_{j=1}^{n} \log_2(A[i][j] + 1) \), with integers encoded in binary.

2.1 Counter systems

Let \( X_n = \{x_1, x_2, \ldots, x_n\} \) be a finite set of integer variables, called counters, \( x \) be the vector such that \( x[i] = x_i \), for all \( i \in [1, n] \), and \( \text{AP} = \{a, b, c, \ldots\} \) be a countable set of boolean atomic propositions. A guard is either true, denoted by \( \top \), or a disjunction of systems of inequalities, denoted by \( \bigvee_{i=1}^{k} C_i \cdot x \leq d_i \), where \( C_i \in \mathbb{Z}^{m \times n} \) and \( d_i \in \mathbb{Z}^m \) for all \( i \in [1, k] \). A guard is said to be without disjunction if it is either true or it consists of a single system of inequalities.

An integer vector \( v \in \mathbb{Z}^n \) satisfies the guard \( g \), written \( v \models g \), if either (i) \( g := \top \), or (ii) \( g := \bigvee_{i=1}^{k} C_i \cdot x \leq d_i \) and \( v \) is a solution of a system \( C_i \cdot x \leq d_i \), for some \( i \in [1, k] \). The set of guards using \( X_n \) is denoted by \( \text{CG}(X_n) \).

An affine function \( f : \mathbb{Z}^n \to \mathbb{Z}^n \) is a pair \( (A, b) \in \mathbb{Z}^{n \times n} \times \mathbb{Z}^n \). Given a vector \( v \in \mathbb{Z}^n \), the result of the function \( f = (A, b) \) applied to \( v \) is \( f(v) = A \cdot v + b \).

We denote by \( \text{Aff}_n \) the set of affine functions over \( \mathbb{Z}^n \). An affine function \( (A, b) \) where \( A = I_n \) is called a translation.

**Definition 1.** [Affine Counter System] For an integer \( n \geq 0 \), an affine counter system of dimension \( n \) (shortly a counter system) is a tuple \( S = (Q, X_n, \Delta, A) \), where: (i) \( Q \) is a finite set of control states, (ii) \( A : Q \to 2^{\text{AP}} \) is a labeling function, and (iii) \( \Delta \subseteq Q \times \text{CG}(X_n) \times \text{Aff}_n \times Q \) is a finite set of transition rules labeled by guards and affine functions (updates).

A counter system is said to be disjunction free if all its guards are without disjunction. For a transition rule \( \delta = (q, g, f, q') \in \Delta \), we use the notations...
source(δ) = q, guard(δ) = g, update(δ) = f and target(δ) = q’. A path π of S is a non-empty sequence of transition rules δ₁...δₘ such that source(δₙ₊₁) = target(δₙ) for all i ∈ [1, m – 1]. The path π is a simple cycle if δ₁...δₘ are pairwise distinct and source(δ₁) = target(δₘ). In this case, we denote source(π) = target(π) = source(δ₁). A counter system S is flat if for each control state q ∈ Q there exists at most one simple cycle π such that source(π) = q. In such a system any path leaving a simple cycle cannot revisit it.

Example 1. Figure 1 shows a flat counter system whose control states q₀, q₁, q₂, q₃ are labeled by the atomic propositions a, b, c, d, respectively. From the initial state q₀ with all counters equal to 0, this system begins with incrementing x₁ a certain number of times by a transition δ₀ then, with δ₁, it transfers the value of the counter x₁ to x₃ and resets x₁; the loop labeled by δ₂ increments both x₁ and x₂ until they both reach the value of x₃ and finally the loop labeled by δ₄ is used to decrement x₂ and increment x₁ until the value of x₁ is twice the value of x₃. As a consequence, when the system reaches q₃ the value of x₁ is twice the value of x₃ and the value of x₂ is equal to 0. Hence, any run reaching q₃ visits the state q₁ exactly the same number of times as the state q₂.

![Fig. 1. A flat affine counter system](image)

The size of a counter system S is size(S) = Σₜ∈Δ size(δ) + Σₗ∈Q ||A(l)||, where size(δ) = 1 + size(guard(δ)) + size(update(δ)), for a guard g := ∨ₖ Cᵦ, x ≤ d, we have size(g) = ∑ₖ size(Cᵦ) + size(d), and for an update f = (A, b), size(f) = size(A) + size(b).

A counter system of dimension n = 0 is called a Kripke structure. We denote by KS and KSF the sets of Kripke structures and flat Kripke structures, respectively. A counter system of dimension n ≥ 1 is translating if all updates labeling the transition rules are pairs (Iᵦ, b). Let TS and TSF denote the sets of translating and flat translating counter systems of any dimension n ≥ 1.

For a counter system S of dimension n ≥ 1, we consider Mₛ ⊆ Zⁿˣⁿ to be the smallest set of matrices, closed under product, which contains Iᵦ, and each matrix A occurring in an update (A, b) of a transition rule in S. Clearly, Mₛ forms a monoid with the matrix product and identity Iᵦ. We say that S has the
finite monoid property if the set $\mathcal{M}_S$ is finite. Let $\mathcal{AS}_{\mathit{fin}}$ be the set of flat counter systems with the finite monoid property and $\mathcal{AS}^\mathit{df}_{\mathit{fin}}$ its restriction to disjunction free systems. These latter classes are the main focus of this paper.

A configuration of the counter system $S = (Q, X, \Delta, A)$ is a pair $(q, v) \in Q \times \mathbb{Z}^n$, where $q$ is the current control state and $v[i]$ is the value of the counter $x_i$, for all $i \in [1, n]$. Given two configurations $\gamma = (q, v)$ and $\gamma' = (q', v')$ and a transition rule $\delta$, we write $\gamma \xrightarrow{\delta} \gamma'$ iff $q = \mathit{source}(\delta)$, $q' = \mathit{target}(\delta)$, $v \models \mathit{guard}(\delta)$ and $v' = \mathit{update}(\delta)(v)$. We use the notation $\gamma \rightarrow \gamma'$ when there exists a transition rule $\delta$ such that $\gamma \xrightarrow{\delta} \gamma'$. A run of $S$ is then an infinite sequence of the form $\rho : \gamma_0 \xrightarrow{s_0} \gamma_1 \xrightarrow{s_1} \gamma_2 \xrightarrow{s_2} \ldots$. We say that such a run starts at configuration $\gamma_0$, furthermore we denote by $\mathit{trans}(\rho) = s_0s_1s_2\ldots$ the infinite sequence of transition rules seen during $\rho$. Without loss of generality we consider deadlock-free counter systems only, where for each configuration $\gamma \in Q \times \mathbb{Z}^n$, there exists a configuration $\gamma'$ such that $\gamma \rightarrow \gamma'$.

Example 2. The sequence below is a run of the counter system from Figure 1.

$$
\begin{align*}
(q_0, \begin{bmatrix} 0 \\ 0 \\ 0 \end{bmatrix}) & \xrightarrow{s_0} (q_0, \begin{bmatrix} 1 \\ 0 \\ 0 \end{bmatrix}) & \xrightarrow{s_1} (q_1, \begin{bmatrix} 0 \\ 0 \\ 1 \end{bmatrix}) & \xrightarrow{s_2} (q_1, \begin{bmatrix} 1 \\ 1 \\ 0 \end{bmatrix}) & \xrightarrow{s_3} (q_2, \begin{bmatrix} 1 \\ 1 \end{bmatrix}) \\
& \xrightarrow{s_4} (q_2, \begin{bmatrix} 2 \\ 0 \\ 1 \end{bmatrix}) & \xrightarrow{s_5} (q_3, \begin{bmatrix} 2 \\ 0 \\ 1 \end{bmatrix}) & \xrightarrow{s_6} (q_3, \begin{bmatrix} 2 \\ 0 \end{bmatrix}) & \xrightarrow{s_7} \ldots
\end{align*}
$$

2.2 Decision Problems

The reachability problem for a class of counter systems $\mathcal{C}$, denoted by $\mathit{REACH}(\mathcal{C})$, can then be stated as follows: given a counter system $S \in \mathcal{C}$, an initial configuration $\gamma_0$, and a control state $q_\mathit{t}$, does $S$ have a run starting in $\gamma_0$ and containing a configuration $(q_\mathit{t}, v)$, for some $v \in \mathbb{Z}^n$? It is well known that $\mathit{REACH}(\mathit{TS})$ is undecidable for non-flat counter systems, even for only 2 counters with zero test guards, and increment/decrement updates [24].

In this work we also consider model checking problems for two specification logics, namely Past Linear Temporal Logic (PLTL) and First Order Logic (FO). The formulae of PLTL are defined by the grammar: $\phi ::= p \mid \neg \phi \mid \phi \land \phi \mid X\phi \mid \phi U \phi \mid \phi S \phi$, where $p \in \mathit{AP}$. As usual, we consider the derived modal operators $F\phi ::= \top U \phi$ and $G\phi ::= \neg F\neg \phi$. Given a run $\rho : \gamma_0 \xrightarrow{s_0} \gamma_1 \xrightarrow{s_1} \gamma_2 \xrightarrow{s_2} \ldots$ of a counter system $S$ and a PLTL formula $\phi$, the semantics of PLTL is defined by an inductive forcing relation $\rho, i \models_{\mathit{pTL}} \phi$, where for all $i \geq 0$: $\rho, i \models_{\mathit{pTL}} p \iff$

---

3 We ensure deadlock-freedom by adding a sink state $s$ to $S$, with a self-loop $s \rightarrow s$, and a transition $q \rightarrow s$ from each state $q \in Q$. 

---

5
The satisfiability problem for Presburger arithmetic where the built formula isReach complexity for these problems is known. We only can deduce from the proofs inMCknow that
checking of an extension of the branching time logicReach of formula which characterizes the reachability set, which entails the decidabilityfinite monoid property, it has been shown that one can compute a Presburgertranslating counter systems. For what concerns flat counter systems with the
the complexity of these two last problems does not change if one considers flatPspaceis
|ρ,j|0 and
¬and
ρ,k=|ρ,k|
ρ,1=|ρ,1|
ρ,i=|ρ,i|
ρ,j=|ρ,j|
i>0 and ρ,i−1=|ρ,i−1|
ρ,i=|ρ,i|
ρ,k=|ρ,k|
for all i≤k<j; ρ,i=|ρ,i|
X−1φ⇒i>0 and ρ,i−1=|ρ,i−1|
φUψ⇒ρ,j=|ρ,j|
ψ for some 0≤j≤i
and
ρ,k=|ρ,k|
for all j<k≤i. The semantics of the boolean connectives ∧ and ¬ is the usual one. We write ρ|=|ρ|ψ for ρ,0|=|ρ|ψ. For instance, each
run of the counter system from Figure 1satisfies G((b∧Xb∧Fd)→F(c∧Xc)), because each run visiting q3 sees the same number of b’s and c’s.

The formulae of FO are defined by the grammar: φ::=p(z) | z<x’ | ¬φ | φ∧φ | ∃z.φ, where p ∈ AP and z belongs to a countable set of logical variablesVar. The semantics is given by a forcing relation ρ|=|ρ|φ between runs ρ of S and
closed formulae φ, with no free variables, which interprets the quantified variables
z ∈ Var as positive integers denoting positions in the run. With this convention,
the semantics of FO is standard. For instance, each run of the counter system
from Figure 1satisfies the FO property: ∀x∀x’.(x<x’∧b(x)∧b(x’)∧∃z.d(z))→∃y∃y’. c(y)∧c(y’), which differs from the previous PLTL formula only in that x and x’ (y and y’) are not necessarily consecutive moments in time. For both of these
logics, we consider the size of a formula as its number of subformulae.

The model-checking problem for counter systems in a class C with specification
language L (in this work either PLTL or FO), denoted by MC_C(C), is
defined as follows: given a counter system S in C, an initial configuration γ_0, and
a formula φ of L, does there exist a run ρ of S starting in γ_0 such that ρ|=|C|φ?

<table>
<thead>
<tr>
<th>Table 1. Known results</th>
</tr>
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<tbody>
<tr>
<td>KS</td>
</tr>
<tr>
<td>REACH</td>
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<tr>
<td>MC_{PLTL}</td>
</tr>
<tr>
<td>MC_{FO}</td>
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</tbody>
</table>

Table 1 gives an overview of the known complexity bounds for the previously
mentioned decision problems looking at different classes of counter systems.
For flat Kripke structures, it is proved in [10,20] that MC_{PLTL}(KS_i) is NP-
complete and in [8] that MC_{FO}(KS) is PSPACE-complete, whereas MC_{PLTL}(KS)
is PSPACE-complete and MC_{FO}(KS) is non-elementary. As explained in [5,11,10],
the complexity of these two last problems does not change if one considers flat
translating counter systems. For what concerns flat counter systems with the
finite monoid property, it has been shown that one can compute a Presburger
formula which characterizes the reachability set, which entails the decidability of
REACH(AS_{lm})[12]. Later on, in [11], the authors have shown that the model-
checking of an extension of the branching time logic CTL* is decidable. Hence we
know that MC_{PLTL}(AS_{lm}) and MC_{FO}(AS_{lm}) are decidable, however no precise
complexity for these problems is known. We only can deduce from the proofs in
[12,15,11] that for REACH(AS_{lm}) and MC_{PLTL}(AS_{lm}) there exists a reduction to
the satisfiability problem for Presburger arithmetic where the built formula is
exponentially bigger than the size of the model, this leads to an upper bound in 4EXPTIME (the satisfiability problem for Presburger arithmetic can in fact be solved in 3EXPTIME, see e.g. [16]).

In this work, we aim at improving the complexity for the problems related to affine counter systems with the finite monoid property. Note that for the presented results, the counter systems were manipulating natural numbers instead of integers, but considering the latter option does not change the stated results.

3 A Hardness Result

In this section we prove that the reachability problem for flat affine counter systems with the finite monoid property is $\Sigma^P_2$-hard, by reduction from the validity problem for the $\exists^*\forall^*$ fragment of quantified boolean formulae ($\Sigma^P_2$-QBF), which is a well-known $\Sigma^P_2$-complete problem [1, §5.2]. Let us consider a formula $\Phi := \exists y_1 \ldots \exists y_p \forall z_1 \ldots \forall z_q \Psi(y, z)$, where $y = \{y_1, \ldots, y_p\}$ and $z = \{z_1, \ldots, z_q\}$ are non-empty sets of boolean variables, and $\Psi$ is a quantifier-free boolean formula. We shall build, in polynomial time, a flat counter system $S_\Phi$, with the finite monoid property, such that $\Phi$ is valid if and only if $S_\Phi$ has a run reaching $q_f$ which starts in $(q_0, v_0)$ for a certain valuation $v_0$ of its counters.

Let $\pi_n$ denote the $n$-th prime number, i.e. $\pi_1 = 2, \pi_2 = 3, \pi_3 = 5$, etc. Formally, $S_\Phi = (Q, \Sigma_N, \Delta, \Lambda)$, where $Q = \{q_0, \ldots, q_p, q, q_f\}$, $N = p + \sum_{n=1}^q \pi_n$, and $\Lambda$ is the function associating to each state an empty set of propositions. We recall that $\pi_n$ is a polynomial in the size of $n$, hence $N$ is as well polynomial in the size of $n$. The transition rules $\Delta$ are depicted in Figure 3. Intuitively, each existentially quantified boolean variable $y_i$ of $\Phi$ is modeled by the counter $x_i$ in $S_\Phi$, each universally quantified variable $z_j$ of $\Phi$ is modeled by the counter $x_{p+\sum_{n=1}^q \pi_n}$, and the rest are working counters. All counters range over the set $\{0, 1\}$, with the obvious meaning (0 stands for false and 1 for true).

The counter system $S_\Phi$ works in two phases. The first phase, corresponding to transitions $q_0 \rightarrow \ldots \rightarrow q_p$, initializes the counters $x_1, \ldots, x_p$ to some values from the set $\{0, 1\}$, thus mimicking a choice of boolean values for the existentially quantified variables $y_1, \ldots, y_p$ from $\Phi$. Here $I_N \in \mathbb{Z}^{N \times N}$ is the identity matrix, and $e_i \in \{0, 1\}^N$ is the unit vector such that $e_i[j] = 0$ if $j \neq i$ and $e_i[i] = 1$.

The second phase checks that $\Phi$ is valid for each choice of $z_1, \ldots, z_q$. This is done by the cycle $q \rightarrow q$, which explores all combinations of 0’s and 1’s for
the counters $x_{p+\sum_{n=1}^{\pi} n}$, corresponding to $z_j$, for all $j \in [1, q]$. To this end, we use the permutation matrix $M$, which consists of $I_p$ and $q$ rotation blocks $M_{\pi_j} \in \{0, 1\}^{\pi_j \times \pi_j}$ (Figure 3). The valuation $v_0$ ensures that the initial value of $x_{p+\sum_{n=1}^{\pi} n}$ is 1, for all $j \in [1, q]$, the other counters being 0 initially (Figure 3).

Intuitively, after $n$ iterations of the affine function $(M, 0)$, labeling the cycle $q \rightarrow q$ in $S_{\Phi}$, we have $x_{p+\sum_{n=1}^{\pi} n} = 1$ iff $n$ is a multiple of $\pi_j$. This fact guarantees that all combinations of 0’s and 1’s for $z_1, \ldots, z_q$ have been visited in $\Pi_{j=1}^{q} \pi_j$ iterations of the cycle. The guard $g_1$, labeling the cycle, tests that, at each iteration, the formula $\Psi$ is satisfied, using a standard encoding of the formula $\Psi$. Namely, each variable $y_i$ is encoded as the term $x_i \geq 1$ and each $z_j$ is encoded as $x_{p+\sum_{n=1}^{\pi} n} \geq 1$.

For instance, the formula $y_1 \lor \neg z_2$ is encoded as $x_1 \geq 1 \lor \neg (x_{p+\pi_1+\pi_2} \geq 1)$ which is equivalent to $x_1 \geq 1 \lor x_{p+\pi_1+\pi_2} < 1$. Finally, the guard $g_2$ simply checks that $x_{\pi_1} = \ldots = x_{\pi_q} < 1$, ensuring that the loop has been iterated sufficiently many times. This allows us to deduce the following result.

**Lemma 1.** Reach$(\mathcal{AS}_{\text{df}})$ is $\Sigma_2^P$-hard.

4 **Bounding the Number of Cycle Iterations**

In this section we prove a crucial property of counter systems from the $\mathcal{AS}_{\text{df}}$ class, namely that there exists a polynomial function $\text{Poly}(x)$ such that, for each run $\rho$ starting at $\gamma_0$ of the considered counter system, there exists another run $\rho'$ starting at $\gamma_0$, using the same transition rules as $\rho$, in exactly the same order, and which iterates each simple cycle at most $2^{\text{Poly}(\text{size}(S) + \text{size}(\gamma_0))}$ times.

In the rest of this section, we fix a flat disjunction free affine counter system $S = (Q, X_n, \Delta, A)$ with the finite monoid property. We recall here that the set of
runs of a flat counter system can be captured by a finite (though exponential) number of path schemas \[ \text{3}\] Formally, a path schema is a non-empty finite sequence \( P := u_1 \ldots u_N \), where \( u_i \) is either a transition rule from \( \Delta \) or a simple cycle, such that (i) \( u_1, \ldots, u_N \) are pairwise distinct, (ii) \( u_N \) is a simple cycle, called terminal, and (iii) \( \text{target}(u_i) = \text{source}(u_{i+1}) \), for all \( i \in [1, N-1] \). All simple cycles on \( P \), except for \( u_N \), are called nonterminal. We use then the following notations: \( \text{len}(P) \) for \( N \), \( P[i] \) for \( u_i \) with \( i \in [1, N] \), and \( \text{size}(P) \) is the sum of the sizes of all transition rules occurring in \( P \).

Intuitively a path schema \( P \) represents a set of infinite paths obtained by iterating the non-terminal cycles a certain number of times. We can hence represent such a path by an associated path schema and an iteration vector. Formally, an iterated path schema is a pair \((P, m)\), such that \( P \) is a path schema, and \( m \in \mathbb{N}^{\text{len}(P) - 1} \) is a vector, where for all \( i \in [1, \text{len}(P) - 1] \), \( m[i] \geq 1 \) and \( m[i] > 1 \) implies that \( P[i] \) is a cycle. An iterated path schema defines a unique infinite word over \( \Delta \), denoted by \( \text{trans}(P, m) \) for all \( i \) of \( P, m \in \text{trans}(P) \) for all possible iterations of \( P \), \( m \) and \( \Delta \).

\[ \text{Lemma 2.} \quad \text{Let } S \text{ be a flat affine counter system. Then:} \]
\[ \text{1. the length and the size of a path schema of } S \text{ are polynomial in } \text{size}(S); \]
\[ \text{2. for any run } \rho \text{ of } S, \text{ there exists an iterated path schema } (P, m) \text{ such that } \text{trans}(\rho) = \text{trans}(P, m). \]

For a run \( \rho \), we consider the set \( \text{ips}(\rho) = \{ (P, m) \mid \text{trans}(\rho) = \text{trans}(P, m) \} \). Observe that \( \text{ips}(\rho) \neq \emptyset \) for any run \( \rho \) of \( S \), due to Lemma 2. Moreover, as a consequence of Lemma 2, the number of path schemas is bounded by a simple exponential in the size of \( S \). Note that \( \text{ips}(\rho) \) is not necessarily a singleton: if a run enters and exits a loop in different states then, in the path schema, the loop may begin either from the entering state or from the exiting state.

We fix a run \( \rho \) of \( S \) starting at \( \gamma_0 \), and \( (P, m) \in \text{ips}(\rho) \) an iterated path schema corresponding to \( \rho \). We consider a simple cycle \( c = \delta_0 \ldots \delta_{k-1} \) of \( P \), whose transition rules are \( \delta_i = (q_i, C_i \cdot x \leq d_i, (A_i, b_i), q_{i+1}) \), for all \( i \in [0, k-1] \), and \( q_k = q_0 \). Let \( f_c = (A_c, b_c) \) be the update of the entire cycle \( c \), where \( A_c = A_{k-1} \cdot A_{k} \cdot A_0 \), denoted \( \prod_{i=k-1}^{0} A_i \), and \( b_c = \sum_{i=0}^{k-1} \prod_{j=i}^{k-1} A_j \cdot b_i \). Since \( S \) has the finite monoid property, the set \( \mathcal{M}_c = \{ A_c^i \mid i \in \mathbb{N} \} \) is finite. Then there exist two integer constants \( \alpha, \beta \in \mathbb{N} \), such that \( 0 \leq \alpha + \beta \leq |\mathcal{M}_c| + 1 \), and \( A_c^\alpha = A_c^\beta \). Observe that, in this case, we have \( \mathcal{M}_c = \{ A_c^0, \ldots, A_c^\alpha, \ldots, A_c^{\alpha + \beta - 1} \} \).

Our goal is to exhibit another run \( \rho' \) of \( S \) and an iterated path schema \( (P, m') \in \text{ips}(\rho') \), such that \( \| m' \|_\infty \leq 2^{\text{Poly}(\text{size}(S) + \text{size}(\gamma_0))} \), for a polynomial function \( \text{Poly}(x) \). Because \( c = \delta_0 \ldots \delta_{k-1} \) is a simple cycle of \( P \) and \( (P, m) \in \text{ips}(\rho) \), there exists a (possibly infinite) contiguous subsequence of \( \rho \), let us call it \( \theta = (q_0, v_0) \xrightarrow{\infty} (q_1, v_1) \xrightarrow{\infty} \ldots \) that iterates \( c \), i.e. \( \tau_i = \delta_i \text{ (mod } k) \), for all \( i \geq 0 \). If the following, we call any subsequence of a run an execution.

The main intuition now is that \( \theta \) can be decomposed into a prefix of length \((\alpha + \beta)k\) and \( k \) infinite sequences of translations along some effectively computable vectors \( w_0, \ldots, w_{k-1} \). More precisely, all valuations \( v_i \) of \( \theta \), for \( i \geq
(α + β)k, that are situated at distance βk one from another, differ by exactly the same vector. We refer to Figure 4 for an illustration of this idea.

![Diagram](image)

Fig. 4. Behavior of an execution which iterates α + 3β times the cycle c = δ₀...δₖ₋₁

**Lemma 3.** Given an execution \((q₀, v₀) \overset{δ₀}{\longrightarrow} \cdots \overset{δₖ₋₁}{\longrightarrow} (qₖ, vₖ) \overset{δₖ}{\longrightarrow} \cdots\) of \(S\) that iterates a simple cycle \(c = δ₀\ldots δₖ₋₁\), there exist \(w₀, \ldots, wₖ₋₁ \in \mathbb{Z}^n\), such that

\[v_{(α + βr)k + q} = v_{(αr)k + q} + p \cdot w_q,\]

for all \(p ≥ 0, r ∈ [0, β − 1]\) and \(q ∈ [0, k − 1]\), where \(f_c = (A_c, b_c)\) is the update of \(c\) and \(α, β ≥ 0\) are such that \(A_c^α = A_c^α + β\).

We distinguish now the case when \(c\) is a nonterminal cycle of \(P\), iterated finitely many times, from the case when \(c\) is terminal, thus iterated ad infinitum. We consider first the case when \(c\) is a nonterminal cycle, taken a finite number of times. Viewing the sequence of counter valuations, that occur during the unfolding of a simple loop, as a set of translations by vectors \(w₀, \ldots, wₖ₋₁\), prefixed by an initial sequence, allows us to reduce the problem of checking the validity of the guards along this sequence to checking the guards only in the beginning and in the end of each translation by \(w_q\), for \(q ∈ [0, k − 1]\). This is possible because the counter systems is disjunction free and hence each guard in the loop is defined by a convex vector set \(\{v ∈ \mathbb{Z}^n \mid C \cdot v ≤ d\}\), for a matrix \(C ∈ \mathbb{Z}^{m × n}\) and a vector \(d ∈ \mathbb{Z}^m\), thus a sequence of vectors produced by a
translation cannot exit and then re-enter the same guard, later on. This crucial observation, needed to prove the upper bound, is formalized below.

We consider the relaxed transition relation \( \sim \subseteq (Q \times \mathbb{Z}^n) \times \Delta \times (Q \times \mathbb{Z}^n) \), defined as \( (q, v) \sim (q', v') \) iff source(\( \delta \)) = q, \( v' = update(\delta)(v) \), and target(\( \delta \)) = \( q' \). Hence, \( \sim \) allows to move from one configuration to another as in \( \rightarrow \), but without testing the guards. In the following, we fix a sequence of configurations \( \theta' = (q_0, v_0) \sim^0 (q_1, v_1) \sim^1 \ldots \) called a pseudo-execution. We assume, moreover, that \( \theta' \) iterates the simple cycle \( c = \delta_0, \ldots, \delta_{k-1} \) a finite number of times, i.e., \( \tau_i := \delta_i \mod k \), for all \( i \geq 0 \). To check whether \( \theta' \) is a real execution, it is enough to check the guards in the first \( \alpha + \beta + 1 \) and the last \( \beta \) iterations of the cycle, as shown by the following lemma:

**Lemma 4.** For any \( m > (\alpha + \beta + 1)k \), given a finite pseudo-execution \( (q_0, v_0) \sim^0 \ldots \sim^0 (q_m, v_m) \) of \( S \), that iterates a nonterminal simple cycle \( c = \delta_0, \ldots, \delta_{k-1} \), \( (q_0, v_0) \sim^0 \ldots \sim^0 (q_m, v_m) \) is an execution of \( S \) iff \( v_i \models guard(\tau_i) \), for all \( i \in [0, (\alpha + \beta + 1)k - 1] \cup [m - \beta k, m - 1] \).

The next step is to show that if a cycle is iterated \( \ell \) times with \( \ell = \alpha + \beta + p\beta + r \) for some \( p > 0 \) and \( r \in [0, \beta - 1] \), starting with values \( v \in \mathbb{Z}^n \), then \( [v][1], \ldots, [v][n], p^\top \) is the solution of a system of inequalities \( M_c \cdot [y; z]^\top \leq n_c \), where \( [y; z] = [y_1, \ldots, y_n, z] \) is a vector of \( n + 1 \) variables. The bound on the number of iterations follows from the theorem below, by proving that the sizes of the entries of \( M_c \) and \( n_c \) (in binary) are bounded by a polynomial in \( size(S) \).

**Theorem 1.** Given \( A \in \mathbb{Z}^{m \times n} \) and \( b \in \mathbb{Z}^m \), for \( n \geq 2 \), the system \( A \cdot x \leq b \) has a solution in \( \mathbb{N}^n \) iff it has a solution such that \( \|x\|_\infty \leq m^{2n} \cdot \|A\|_{\infty \rightarrow \infty} \cdot \|b\|_\infty \).

We recall that \( c = \delta_0, \ldots, \delta_{k-1} \), where guard(\( \delta_i \)) = \( C_i \cdot x \leq d_i \), update(\( \delta_i \)) := \( (A_i, b_i) \), and that \( f_c = (A_c, b_c) \) is the affine function defining the update of the entire cycle. For any \( j > 0 \), we define \( b_i^\ell = \Sigma_{i=0}^j A_i^\ell \cdot b_i \), hence \( f_c^\ell = (A_c^\ell, b_c^\ell) \) is the update corresponding to \( \ell \) iterations of the cycle for a fixed integer constant \( \ell > 0 \). The following set of inequalities expresses the fact that all guards are satisfied within the \( \ell \)-th iteration of the cycle starting at \( v \in \mathbb{Z}^n \):

\[
C_{p} \cdot (\prod_{i=p-1}^{0} A_i \cdot (A_{p}^{\ell-1} \cdot v + b_{p}^{\ell-1}) + \sum_{i=0}^{p-1} A_{i+1} \cdot \cdots A_{i+1} \cdot b_i) \leq d_p , \text{ for all } p = 0, \ldots, k-1
\]

In the sequel, we define \( M_{\ell} \) as the matrix obtained by vertically stacking the matrices \( C_j \cdot \prod_{i=j+1}^{k-1} A_i \cdot A_{j+1}^{\ell-1} \) for \( j = 0, \ldots, k-1 \), with \( C_0 \cdot A_{k}^{\ell-1} \) on top. Also, \( n_\ell \) is the column vector with rows \( n_\ell[j] = d_j - (C_j \cdot \prod_{i=j+1}^{k-1} A_i \cdot b_{j}^{\ell-1} + C_j \cdot (\sum_{i=0}^{j-1} A_{j-1} \cdot \cdots A_{j+1} \cdot b_i)), \) for \( j = 0, \ldots, k-1 \). For technical reasons that will be made clear next, we do not need to consider the case when the loop is iterated less than \( \alpha + 2\beta + 1 \) times. We know, from Lemma 4 that checking whether a given cycle \( c \) can be iterated \( \ell > \alpha + 2\beta + 1 \) times from \( v \), reduces to checking
the validity of the guards during the first $\alpha + \beta + 1$ and the last $\beta$ iterations only. This condition is encoded by the union of the linear inequality systems below:

$$
\begin{bmatrix}
M_1 \\
M_{\alpha + \beta + 1}
\end{bmatrix}v \leq \begin{bmatrix}
n_1 \\
\vdots \\
n_{\alpha + \beta + 1}
\end{bmatrix}
\quad \begin{bmatrix}
M_1 \\
\vdots \\
M_{\beta}
\end{bmatrix}f_c^{\ell - \beta}(v) \leq \begin{bmatrix}
n_1 \\
\vdots \\
n_{\beta}
\end{bmatrix}
$$

Since we assumed that $\ell > \alpha + 2\beta + 1$, it follows that $\ell - \beta = \alpha + p\beta + r$ for some $p > 0$ and $r \in [0, \beta - 1]$, thus $f_c^{\ell - \beta}(v) = f_c^{\alpha + r}(v) + p\cdot w_0 = A_c^{\alpha + r}v + b_c^{\alpha + r} + p\cdot w_0$, by Lemma 3. Then, for any finite execution starting with $v$, and consisting of $\alpha + p\beta + r$ iterations of $c$, we have that the column vector $[v[1], \ldots, v[n], p]^T$ is a solution of the linear system $M_{c,r} \cdot [y; z]^T \leq n_{c,r}$, where:

$$
M_{c,r} = \begin{bmatrix}
M_1 & \cdots & 0 \\
M_{\alpha + \beta + 1} & \cdots & 0 \\
M_{\beta} \cdot A_c^{\alpha + r} & M_1 \cdot w_0 \\
M_{\beta} \cdot A_c^{\alpha + r} & \cdots & M_\beta \cdot w_0
\end{bmatrix}
\quad n_{c,r} = \begin{bmatrix}
n_1 \\
\vdots \\
n_{\alpha + \beta + 1} \\
n_{\beta} - M_\beta \cdot b_c^{\alpha + r}
\end{bmatrix}
$$

We now consider the case when the simple cycle $c = \delta_0 \ldots \delta_{k-1}$ is terminal and let $w_0, \ldots, w_{k-1} \in \mathbb{Z}^n$ be the vectors from Lemma 3. We say that $c$ is infinitely iterable iff for all $i \in [0, k-1]$, we have $C_i \cdot w_i \leq 0$. Since $w_0, \ldots, w_{k-1}$ are effectively computable vectors, this condition is effective. The next lemma reduces the existence of an infinite iteration of the cycle to the existence of an integer solution of a linear inequation system.

**Lemma 5.** Given an infinite pseudo-execution $(q_0, v_0) \xrightarrow{\gamma_0} (q_1, v_1) \xrightarrow{\gamma_1} \ldots$ of $S$, that iterates a terminal simple cycle $c = \delta_0 \ldots \delta_{k-1}$, $(q_0, v_0) \xrightarrow{\gamma_0} (q_1, v_1) \xrightarrow{\gamma_1} \ldots$ is an infinite execution of $S$ if $c$ is infinitely iterable and $v_i \models \text{guard}(\tau_i)$, for all $i \in [0, (\alpha + \beta + 1)k - 1]$.

As a consequence, for an infinitely iterable cycle $c$, the existence of an execution that iterates $c$ infinitely often is captured by the linear system $M_{c,\omega} \cdot y \leq n_{c,\omega}$, where $M_{c,\omega}$ and $n_{c,\omega}$ are obtained by stacking the matrices $M_1, \ldots, M_{\alpha + \beta + 1}$ and vectors $n_1, \ldots, n_{\alpha + \beta + 1}$, respectively.

We have now all the ingredients needed to bound the number of cycle iterations within the runs of a flat disjunction free affine counter system having the finite monoid property. The argument used in the proof relies on the result of Theorem 1, namely that the size of a minimal solution of a linear system of inequalities is polynomially bounded in the maximum absolute value of its coefficients, and the number of rows, and exponentially bounded in the number of columns. Since the number of rows depends on the maximum size of the monoids of the update matrices in the counter system, we use the result from Lemma 13, §B.1, namely that the size of a finite monoid of a square matrix is simply exponential in the dimension of that matrix.

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4 They are defined in the proof of Lemma 3.
Theorem 2. Given a flat disjunction free affine counter system $S = \langle Q, X_n, \Delta, \Lambda \rangle$, with the finite monoid property, for any run $\rho$ of $S$, starting in $(q_0, v_0)$, and any iterated path schema $\langle P, m \rangle \in \text{ips}(\rho)$, there exists a run $\rho'$, starting in $(q_0, v_0)$, and an iterated path schema $\langle P, m' \rangle \in \text{ips}(\rho')$, such that $\|m'\|_\infty \leq 2^{\text{Poly}(\text{size}(S) + \text{size}(v_0))}$, for a polynomial function $\text{Poly}(x)$.

5 The Complexities of Decision Problems for $\mathsf{AS}_{\text{df}}^{\text{fm}}$

In this section, we will prove that the previous reasoning on iterated path schemas allows us to deduce complexity bounds of the reachability problems and of model-checking with PLTL and FO formulae for disjunction free flat counter systems with the finite monoid property.

5.1 Reachability is $\Sigma_P^P$

In this section we give the first upper bound, for the reachability problem and show that $\text{Reach}(\mathsf{AS}_{\text{df}}^{\text{fm}})$ is $\Sigma_P^P$. Even if this upper bound holds only for disjunction free counter system, we believe we could extend it to all the class $\mathsf{AS}_{\text{fm}}$ by adapting the method presented in [10] to eliminate the disjunctions. This would allow us to match the lower bound from Section 3. However we did not wish to enter into the heavy details of eliminating disjunctions in this work, in order to focus more on the specific aspects of affine counter systems. Anyway the provided result improves the $4\text{EXPTIME}$ upper bound from Table 1. The crux of the proof is based on the result provided by Theorem 2 and it follows the following reasoning: we use a polynomial-time bounded nondeterministic Turing machine that guesses an iterated path schema and then a NP oracle to check whether a guard has been violated. This gives us an NP algorithm for $\text{Reach}(\mathsf{AS}_{\text{df}}^{\text{fm}})$, which then lies in $\Sigma_P^P$. Theorem 2 ensures us the soundness of the Algorithm and the correctness is provided by the fact that if, in an iterated path schema, no guard is violated then it corresponds necessarily to a run.

Let us now explain how our NP oracle works. The next lemma is based on the fact that any power $A^k$ of a finite monoid matrix $A$ can be computed in time polynomial in $\text{size}(A)$ and $\log_2 k$, using matrix exponentiation by squaring. The reason is that the value of an entry of any power of a finite monoid matrix $A$ is bounded by an exponential in $\text{size}(A)$, thus the size of its binary representation is polynomially bounded by $\text{size}(A)$, and each step of the squaring algorithm takes polynomial time [18, Lemma 14, §B.1].

Lemma 6. Given an iterated path schema $\langle P, m \rangle$ of a counter system with the finite monoid property $S$ and an initial configuration $\gamma_0$, checking whether there is no run $\rho$ starting at $\gamma_0$ such that $\langle P, m \rangle \in \text{ips}(\rho)$ is in NP.

The next theorem gives the main result of this section.

Theorem 3. $\text{Reach}(\mathsf{AS}_{\text{fm}}^{\text{df}})$ is $\Sigma_P^P$. 

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5.2 PLTL Model Checking is \( \Sigma_2^{P} \)

For a PLTL formula \( \phi \), its temporal depth \( td(\phi) \) is defined as the maximal nesting depth of temporal operators in \( \phi \), and the size of \( \phi \) is its number of subformulae. In [10] Theorem 4.1, the authors have proved a stuttering theorem for PLTL stating that if an \( \omega \)-word \( w = w_1w_2^My_1 \) over the alphabet \( 2^\mathbb{N} \) with \( w_2 \neq \epsilon \) satisfies a PLTL formula \( \phi \) (i.e. \( w, 0 \models_{rtn} \phi \)) and if \( M \geq 2td(\phi) + 5 \) then all \( \omega \)-words \( w' = w_1w_2^{M'}y_1 \) with \( M' \geq 2td(\phi) + 5 \) are such that \( w', 0 \models_{rtn} \phi \). In other words, to verify if an \( \omega \)-word with some repeated infix words satisfies a PLTL formula it is enough to verify the property for the \( \omega \)-words where each infix is repeated at most \( 2td(\phi) + 5 \) times. This allows to deduce that the model-checking of PLTL for flat translating counter systems is NP-complete. We rewrite now in our terminology the main proposition which leads to this result.

In the sequel we consider a flat disjunction free counter system \( S = (Q, X_n, \Delta, \Lambda) \) with the finite monoid property. For a finite sequence of transitions \( \delta_1 \ldots \delta_k \), we denote by \( A(\delta_1 \ldots \delta_k) = A(source(\delta_1)) \ldots A(source(\delta_k)) \) the finite word labeling the sequence with sets of atomic propositions. We lift this definition to iterated path schemas \( \langle P, m \rangle = A(P[1])m[1]A(P[2])m[2] \ldots A(P[len(P) - 1])m[len(P) - 1]A(P[len(P)]) \). Observe that, for a run \( \rho \) of a counter system, if \( \langle P, m \rangle \in \text{ips}(\rho) \) is an iterated path schema, we have by definition of the semantics of PLTL that \( \rho \models_{rtn} \phi \) iff \( A(P, m), 0 \models_{rtn} \phi \) for all PLTL formulae \( \phi \). Moreover, for each \( m \in \mathbb{N} \), we define the function \( \xi_m \) mapping each vector \( v \in \mathbb{N}^k \) to \( \xi_m(v) \in \mathbb{N}^k \), where, for all \( i \in [1, k] \), \( \xi_m(v)[i] = v[i] \) if \( v[i] < m \) and \( \xi_m(v)[i] = m \) otherwise. Let us now recall the main technical propositions established in [11], which are a consequence of the stuttering theorem for PLTL and of the result on the complexity of model-checking ultimately periodic path with PLTL given in [23].

**Lemma 7.** Let \( \langle P, m \rangle \) be an iterated path schema and \( \phi \) a PLTL formula, then:

1. [11] Proposition 5.1 \( A(P, m), 0 \models_{rtn} \phi \) iff \( A(P, \xi_{2td(\phi) + 5}(m)), 0 \models_{rtn} \phi \).
2. [23] Theorem 3.2] Given finite words \( u \) and \( v \), checking \( uv^\omega \), \( 0 \models_{rtn} \phi \) can be done in time polynomial in the sizes of \( uv \) and \( \phi \).

We need furthermore a version of Theorem 2 above, which ensures that given an iterated path schema and a PLTL formula \( \phi \), we do not change the number of times a loop is iterated if this one is less than \( 2td(\phi) + 5 \). The proof of the next result can in fact be deduced by adapting the proof of Theorem 2 by unfolding the loop which are iterated less than \( 2td(\phi) + 5 \) for a given formula \( \phi \). As a consequence of Lemma 7, the new run \( \rho' \), obtained in the next lemma, is such that \( \rho \models_{rtn} \phi \) iff \( \rho' \models_{rtn} \phi \) for the considered PLTL formula \( \phi \).

**Lemma 8.** For a run \( \rho \) of \( S \) starting in \( (q_0, v_0) \), an iterated path schema \( \langle P, m \rangle \in \text{ips}(\rho) \) and a PLTL formula \( \phi \), there exists a run \( \rho' \) starting in \( (q_0, v_0) \), and an iterated path schema \( \langle P, m' \rangle \in \text{ips}(\rho') \), such that \( \|m'\|_\infty \leq 2^{\text{Poly}(\text{size}(S) + \text{size}(v_0) + td(\phi))} \) for a polynomial \( \text{Poly}(x) \) and \( \xi_{2td(\phi) + 5}(m') = \xi_{2td(\phi) + 5}(m) \).

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5 We take here the classical semantics of PLTL over infinite words.
We can now explain why the model-checking of flat counter systems with the finite monoid property with PLTL formulae is in $\Sigma^P_2$. Given a flat counter system $S$ with the finite monoid property, an initial configuration $\gamma_0$, and a PLTL formula $\phi$, we guess an iterated path schema $\langle P, m \rangle$ of polynomial size in the size of $S$, $\gamma_0$ and $\phi$, and we check whether $A(P, \xi_{2\text{st}}(\phi)+5(m)), 0 \models_{\text{PLTL}} \phi$. This check can be done in polynomial time in the size of $P$ and $\phi$ thanks to Lemma 7. Finally, we use the NP algorithm of Lemma 6 to verify that there exists a run $\rho$ starting at $\gamma_0$, such that $\langle P, m \rangle \in \text{ips}(\rho)$. This gives us a $\Sigma^P_2$ algorithm whose correctness is ensured by Lemma 8 and Lemma 2.

Theorem 4. $\text{MC}_{\text{PLTL}}(\text{AS}^\text{df}_{\text{fm}})$ is $\Sigma^P_2$.

5.3 FO Model Checking is PSPACE-complete

For a FO formula $\phi$, its quantifier height $qh(\phi)$ is the maximal nesting depth of its quantifiers, and the size of $\phi$ is its number of subformulae. Similarly, as for the PLTL case, in [8, Theorem 6], a stuttering theorem for FO is provided, which says that that two $\omega$-words $w = w_1 w_2^M w_3$ and $w = w_1 w_2^{M'} w_3$ with $w \neq \epsilon$ are indistinguishable by a FO formula $\phi$ if $M$ and $M'$ are strictly bigger than $2^{qh(\phi)+2}$. The main difference with PLTL is that this provides an exponential bound in the maximum number of times an infix of an $\omega$-word needs to be repeated to satisfy a FO formula. In the sequel we consider a flat counter system $S = \langle Q, X_n, \Delta, l \rangle$ with the finite monoid property and we reuse the notations introduced in the previous section. The results of [8] can be restated as follows.

Lemma 9. Given an iterated path schema $\langle P, m \rangle$ and a FO formula $\phi$, then:
1. [8, Lemma 7] $A(P, m) \models_{\text{ro}} \phi$ iff $A(P, \xi_{2\text{st}}(\phi)+z(m)), 0 \models_{\text{ro}} \phi$.
2. [8, Theorem 9] Checking $A(P, m), 0 \models_{\text{ro}} \phi$ can be done in space polynomial in the sizes of $\langle P, m \rangle$ and $\phi$.

As for the PLTL case, this allows us to deduce a NPSPACE algorithm for the model-checking problem of flat counter system with the finite monoid property with FO formulae. Since the problem is already PSPACE-hard for flat translating counter systems [8, Theorem 9], we conclude by the following theorem.

Theorem 5. $\text{MC}_{\text{FO}}(\text{AS}_{\text{fm}})$ is PSPACE-complete.

References