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# Stability Improvement of the Interconnection of Weak AC Zones by Synchronverter-based HVDC link

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**Abstract--** The interconnection of weak electric power grids opens new issues into power system stability and control. This paper proposes a control strategy of HVDC transmission yielding increased power transfer capacity and enhanced transient stability of weak interconnected systems. The proposed control is a recently developed synchronverter-based control and emulation of the HVDC link (SHVDC). Methodologically, the transient stability of the neighbouring zone is a priori taken into account at the design level of the control. The parameters of the SHVDC regulators are tuned based on a specific residues method. The performances of the control strategy are analyzed by the power transfer limit  $P_{limit}$  when the interconnection is a pure DC link, and by both the  $P_{limit}$  and the Critical Clearing Time (CCT) when the interconnection is a hybrid DC/AC link. The study further investigates the impact of the inertia emulation that the synchronverter provides, and of the tuned control parameters on  $P_{limit}$ . The proposed control is tested in comparison to the standard vector control. The simulation results indicate that the synchronverter based control improves both the dynamic transfer capacity and the transient stability of weak interconnected power systems.

**Keywords--** HVDC, synchronverter, stability limits, regulator parameters tuning.

## **Nomenclature**

$V, E$ : Stator and grid voltages.

$i_s, i_g$ : Stator and grid currents.

$e$  : Back emf.

$P, Q$ : VSC active and reactive powers outputs.

$L_s, R_s$ : VSC inductance and resistance.

$L_g, R_g$ : Grid inductance and resistance.

$Z_g$ : Grid impedance.

$C_f$ : Filter capacitor.

$T_m, T_e$ : Mechanical and electromagnetic torques.

$J$ : Inertia moment.

$\theta$  : Rotor angle.

$M$ : Field excitation.

$\omega$  : Grid angular frequency.

$|V|$  : Voltage Magnitude.

$S_{ac}$ : Grid short circuit power.

$P_{dN}$ : rated DC power.

$D_p, D_q$ : Frequency and Voltage droops.

$s$ : Laplace operator.

## **Subscript**

$G, M$ : SG and SM indices.

### **1. Introduction**

The interconnection of AC electrical power grids have historically presented technical challenges ranging from frequency regulation, coordination of operations, and transient

stability [1]. These challenges have gained higher interest on one hand due to the incorporation of power electronic converters in the transmission links and on the other hand, due to the integration of weak, rather than strong power systems. In a weak system a small disturbance can cause large deviations in the voltage and other variables in the network. The short circuit level at a bus is commonly used as a measure of the system strength at that particular point. Technically, a weak AC system could be evaluated following several considerations: low ratio of inductance over resistance, high impedance and low inertia [2]. Examples of weak networks include isolated microgrids with renewable energy (wind power, photovoltaic...). Due to network weakness, dynamics of voltage and frequency are coupled, making it difficult to simultaneously guarantee voltage stability and frequency synchronization [2-4].

In response to these challenges, transmission in HVDC has been developed to mitigate these issues. The HVDC technology is able to provide to the transmission system advantages such as transfer capacity enhancement and power flow control [5]. Indeed, HVDC applications have widely increased, presenting diverse configurations: pure DC links or hybrid configurations where AC and DC circuits are parallelized. The strength of the AC system with respect to the power rating of the HVDC link is described by the Short-Circuit Ratio (SCR) [2-4]. The interconnected networks may be electrically strong AC systems with an SRC greater than 3, or weak networks suffering from low inertia and low SCR. Indeed, the HVDC links are increasingly used inside the same AC network in order to enhance the power transmission capacity between some zones of the grid, such as the integration of renewable energy distributed generators.

Many authors have investigated the impact of HVDC applications on stability limits of interconnected power systems [6-17]. For transient stability, the criteria used are the

*Critical Clearing Time (CCT)* and the *power transfer limit*  $P_{\text{limit}}$ , called also *the dynamic transmission capacity* in [18]. For pure HVDC links, only the  $P_{\text{limit}}$  criteria is used.

For parallel AC/DC lines, both the CCT and the  $P_{\text{limit}}$  are used as transient stability criteria: the CCT of a three-phase fault on the AC line, in addition to the total power transfer limit  $P_{\text{limit}}$ . The authors in [6-17] have shown that the strategy used to control the HVDC converters can impact the stability of the system in which the link is embedded. HVDC lines in parallel with HVAC lines are shown to improve the transient stability of power system quantified by the CCT. In a hybrid AC/DC link, increasing the DC power, improves the transient stability [17].

One of the main trends in control techniques for VSC-HVDC links is based on the well-known vector control scheme [9-15]. Despite the effectiveness of this control in many power converter applications, the use of the VSC-HVDC based on vector-current control in weak AC connections are claimed to exhibit low performances [19-23]. Reference [19] shows that using vector current control, the power limit  $P_{\text{limit}}$  of a VSC-HVDC link to an AC system with SCR = 1.0 is limited to only 0.4 p.u. Recently in [24], the synchronverter control has been adapted to the converters of a VSC-HVDC link. The concept of the synchronverter control is to mimic the behavior of a synchronous generator (SG) along with its voltage and frequency regulations [25]. The sending-end rectifier emulates a synchronous motor (SM) and the receiving end inverter emulates a synchronous generator (SG). The resulting Synchronverter based HVDC was called SHVDC [24].

This paper proposes a control strategy of HVDC transmission to increase power transfer capacity and enhance transient stability of weak interconnected systems. The proposed control is a synchronverter-based control and emulation of the HVDC link. Methodologically, the transient stability of the neighbouring zone is a priori taken into

account at the design level of the control. The parameters of the SHVDC regulators are tuned by the sensitivity of the poles of the neighbouring zones of the HVDC to control parameters, and by the method of residues. The performances of the control strategy are analyzed by the power transfer limit  $P_{\text{limit}}$  when the interconnection is a pure DC link, and by both the  $P_{\text{limit}}$  and the Critical Clearing Time (CCT) when the interconnection is a hybrid DC/AC link. The impact of the control parameters and the inertia emulation on the dynamic transmission capacity  $P_{\text{limit}}$  of an HVDC connection of two weak AC zones is firstly analyzed in a two weak terminals HVDC line. Second, we test the influence of the HVDC relative capacity on the stability limits in the case of a hybrid DC/AC interconnection. The proposed control is tested in comparison to the standard vector control. The simulation results indicate that the synchronverter based control improved both the dynamic transfer capacity and the transient stability of weak interconnected power systems.

The rest of the paper is organized as follows: in Section 2, the SHVDC structure is recalled. In Section 3, we present the criteria for transient stability. The tuning method of the proposed controller is developed in Section 4. Sections 4 and 5 present the simulation results.

## **2. Synchronverter-based HVDC connecting two weak AC systems**

### **2.1 Characteristics of weak AC systems**

The characteristics of AC systems have a significant impact on the operation of HVDC systems. The strength of an AC/DC system can be measured by its short circuit ratio (SCR) which is the ratio of the AC system short-circuit capacity to the rated power of the HVDC system. This is expressed in equation (1) [2]

$$SCR = \frac{S_{ac}}{P_{dN}}, \quad (1)$$

The short-circuit capacity of the grid at the PCC bus in Fig.1 is given by:

$$S_{ac} = \frac{|V|^2}{Z_g} , \quad (2)$$

The strength of an AC system may be quantified based on the classification given in [2-4]:

- Strong system, if the SCR of the AC system is greater than 3.0.
- Weak system, if the SCR of the AC system is between 2.0 and 3.0.
- Very weak system, if the SCR of the AC system is lower than 2.0.

Moreover, HVDC links connected to weak AC systems with low SCR have negative impacts on the AC system performances such as: power transfer limitations, voltage instability and high dynamic over-voltages.

## 2.2 Synchronverter based HVDC

In this Section, the synchronverter-based emulation of HVDC link (SHVDC) is briefly recalled [24]. The SHVDC system consists of two synchronverters connected with a DC line as shown in Fig.1. The sending-end rectifier emulates a synchronous motor (SM) and the receiving end inverter emulates a synchronous generator (SG) [24]. Fig.1 depicts the power part of the SHVDC. It consists of an inverter/rectifier plus an LC filter. The basic equations of this circuit are expressed by (3) and (4).

$$V_G = -R_s i_{sG} - L_s \frac{di_{sG}}{dt} + e_G , \quad (3)$$

$$V_M = R_s i_{sM} + L_s \frac{di_{sM}}{dt} + e_M . \quad (4)$$

Each VSC converter is connected to the AC system (Fig.1) via a grid impedance representing the short-circuit power at the point of common coupling (PCC). The output current and voltage may be given by:

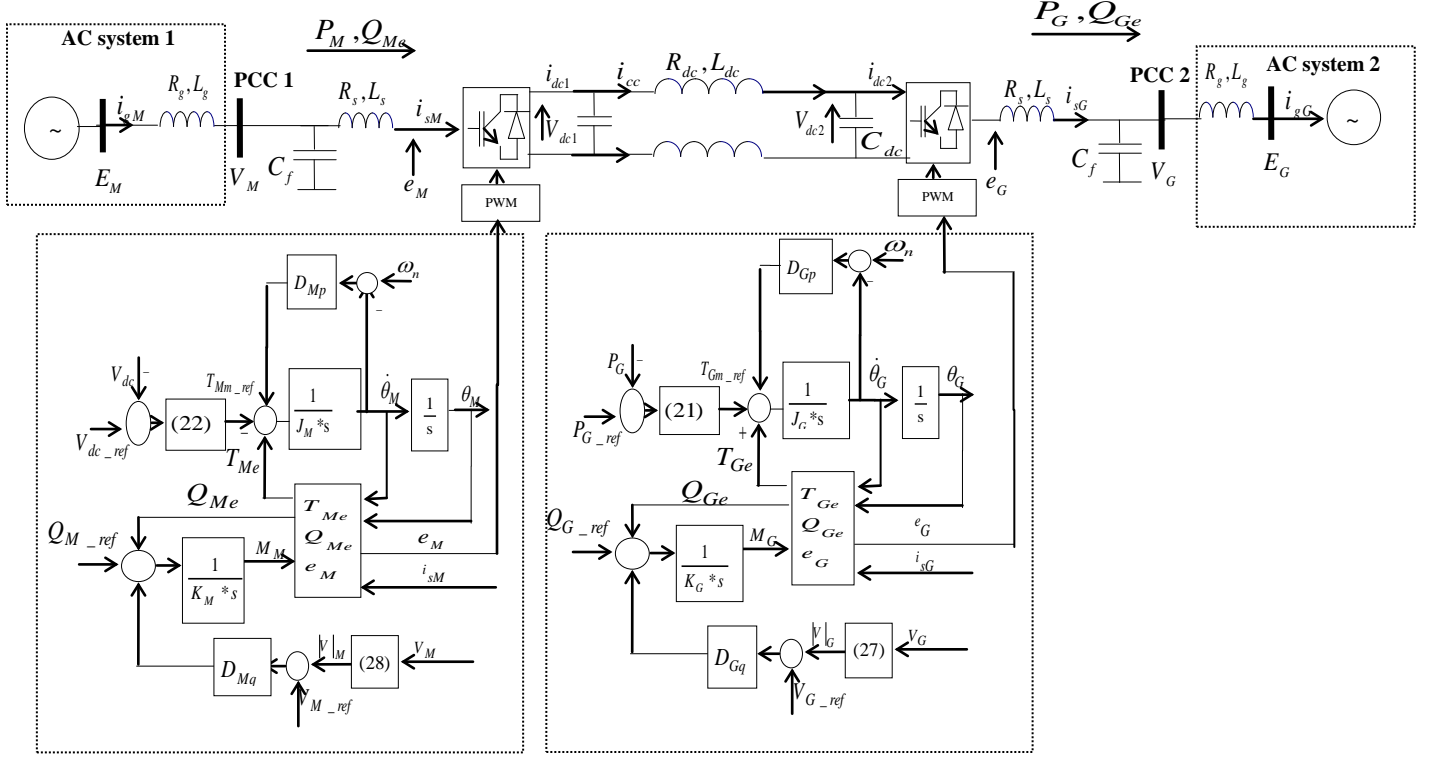


Fig. 1. Synchronverter-based HVDC link

$$V_G = \frac{1}{C_f s} (i_{sG} - i_{gG}), \quad (5)$$

$$i_{sG} = \frac{1}{(R_g + L_g s)} (V_G - E_G), \quad (6)$$

$$V_M = \frac{1}{C_f s} (i_{gM} - i_{sM}), \quad (7)$$

$$i_M = \frac{1}{(R_g + L_g s)} (E_M - V_M). \quad (8)$$

As shown in Fig. 1, controllers include the mathematical model of a three-phase round-rotor synchronous machine described by [24]

$$\ddot{\theta}_G = \frac{1}{J_G} (T_{Gm} - T_{Ge} - D_{Gp} s \theta_G), \quad (9)$$

$$\ddot{\theta}_M = \frac{1}{J_M} (T_{Me} - T_{Mm} - D_{Mp} s \theta_M), \quad (10)$$



$$T_{Ge} = M_G \langle i_{sG}, \tilde{\sin} \theta_G \rangle, \quad (11)$$

$$T_{Me} = M_M \langle i_{sM}, \tilde{\sin} \theta_M \rangle, \quad (12)$$

$$e_G = M_G s \theta_G \tilde{\sin} \theta_G, \quad (13)$$

$$e_{M\_abc} = M_M s \theta_M \tilde{\sin} \theta_M, \quad (14)$$

$$P_G = M_G s \theta_G \langle i_{sG}, \tilde{\sin} \theta_G \rangle, \quad (15)$$

$$Q_{Ge} = -M_G s \theta_G \langle i_{sG}, \tilde{\cos} \theta_G \rangle, \quad (16)$$

$$Q_{Me} = M_M s \theta_M \langle i_{sM}, \tilde{\cos} \theta_M \rangle, \quad (17)$$

$$P_M = M_M s \theta_M \langle i_{sM}, \tilde{\sin} \theta_M \rangle, \quad (18)$$

where  $\tilde{\sin} \theta$  and  $\tilde{\cos} \theta$  are [24]

$$\tilde{\sin} \theta = \begin{bmatrix} \sin \theta & \sin(\theta - \frac{2\pi}{3}) & \sin(\theta + \frac{2\pi}{3}) \end{bmatrix}^T, \quad \tilde{\cos} \theta = \begin{bmatrix} \cos \theta & \cos(\theta - \frac{2\pi}{3}) & \cos(\theta + \frac{2\pi}{3}) \end{bmatrix}^T.$$

The operator  $\langle \dots \rangle$  denotes the conventional inner product in  $\mathbb{R}^3$ .

The frequency droop control loop of the SHVDC link is given by [24]

$$T_{Gm} = T_{Gm\_ref} + D_{Gp} (\omega_n - s \theta_G), \quad (19)$$

$$T_{Mm} = T_{Mm\_ref} + D_{Mp} (\omega_n - s \theta_M). \quad (20)$$

where  $T_{gm\_ref}$  is the mechanical torque applied to the SG rotor, and it is generated by a PI controller to regulate the real power output  $P_g$  (Fig. 1). It is expressed as

$$T_{Gm\_ref} = (k_{p\_Pg} + \frac{k_{i\_Pg}}{s})(P_G - P_{G\_ref}), \quad (21)$$

In the SM case, the reference torque  $T_{mm\_ref}$  is generated by a DC voltage controller for power balance [24].

$$T_{Mm\_ref} = (k_{p\_vdc} + \frac{k_{i\_vdc}}{s})(V_{dc\_ref} - V_{dc1}). \quad (22)$$

The reactive power  $Q_{gm}$  (respectively  $Q_{mm}$ ) is controlled by a voltage droop control loop in order to regulate the field excitation  $M_g$  (respectively  $M_m$ ) [24].

$$M_M = \frac{1}{k_G s} (Q_{Gm} - Q_{Ge}), \quad (23)$$

$$M_M = \frac{-1}{k_M s} (Q_{Mm} - Q_{Me}), \quad (24)$$

$$Q_{Gm} = Q_{G\_ref} + D_{Gq} (V_{G\_ref} - |V|_G) \quad (25)$$

$$Q_{Mm} = Q_{M\_ref} + D_{Mq} (V_{M\_ref} - |V|_M), \quad (26)$$

The output voltages amplitudes are computed by [24]

$$|V|_G = \frac{2}{\sqrt{3(V_{Ga}V_{Gb} + V_{Ga}V_{Gc} + V_{Gb}V_{Gc})}}, \quad (27)$$

$$|V|_M = \frac{2}{\sqrt{3(V_{Ma}V_{Mb} + V_{Ma}V_{Mc} + V_{Mb}V_{Mc})}}. \quad (28)$$

Applying the current Kirchhoff law to each capacitor terminal, the circuit equations of the DC line (Fig. 1) are

$$V_{dc1} = \frac{1}{C_{dc} s} (i_{dc1} - i_{cc}), \quad (29)$$

$$V_{dc2} = \frac{1}{C_{dc} s} (i_{cc} - i_{dc2}), \quad (30)$$

Interconnected relationship between the two converters station is given by

$$V_{dc1} = V_{dc2} + R_{dc} i_{cc} + L_{dc} \frac{di_{cc}}{dt}. \quad (31)$$

The AC and DC circuit equations have to be coupled. The coupling equations follow from the active power balance between AC and DC sides of each converter.

$$P_{dc1} = V_{dc1} i_{dc1}, \quad (32)$$

$$P_{dc2} = V_{dc2} i_{dc2}, \quad (33)$$

$$P_{dc1} = P_M , \quad (34)$$

$$P_{dc2} = P_G . \quad (35)$$

Equation (31) could be expressed by

$$V_{dc1} i_{cc} = V_{dc2} i_{cc} + R_{dc} i_{cc}^2 + \frac{L_{dc}}{2} \frac{di_{cc}^2}{dt} \quad (36)$$

Combining the latter equation with (29), (30), the active power flow satisfies the equation :

$$V_{dc1} i_{dc1} = V_{dc2} i_{dc2} + (R_{dc} i_{cc}^2 + \frac{L_{dc}}{2} \frac{di_{cc}^2}{dt} + \frac{C_{dc}}{2} \frac{dV_{dc1}^2}{dt} + \frac{C_{dc}}{2} \frac{dV_{dc2}^2}{dt}) \quad (37)$$

From (37), the flow of active power in the HVDC transmission system is balanced, which means that the active power  $P_M$  entering the HVDC system matches the active power  $P_G$  leaving it, plus the losses in the DC transmission system. Thus, (38) expresses this power balance:

$$P_M = P_G + losses . \quad (38)$$

### 2.3 Structural analysis of the SHVDC system

The scope of this Section is to analyze the SHVDC structure based on the algebraic approach [37] issued from dynamic systems theory. The latter consists in an analytic check of the degrees of freedom of the closed-loop (system along with regulations). The model of the SHVDC given by equations (3)-(35) is a general Differential Algebraic Equations (DAE) form. The approach introduced in [37] and related references can be directly used. In this latter, a linear system  $\Sigma$  is a module  $M$  defined by a matrix equation

$$S(s)w = 0 , \quad (39)$$

where  $S(s)$  is a matrix which elements are polynomials in the Laplace/derivation operator  $s$ .

For the SHVDC system,  $S(s)$  results from the linear approximation of equations (3)-(35) and  $w$  is given by (40). The system in (39) is written in order to put into evidence the inputs of the controlled system, i.e., the variables which can be imposed by the controls.

$$\begin{aligned}
w = & [i_{sG}, V_G, e_{Gc}, i_{gG}, i_{sM}, V_M, e_M, i_{gM}, P_G, \\
& T_{Ge}, T_{Gm}, T_{Gm\_ref}, \theta_G, P_M, T_{Me}, T_{Mm}, T_{Mm\_ref}, \theta_M, Q_{Ge}, Q_{Gm}, M_G, |V|_G, \\
& V_{dc1}, i_{dc1}, P_{dc1}, Q_{Me}, Q_{Mm}, M_M, |V|_M, V_{dc2}, i_{dc2}, i_{cc}, P_{dc2}, \\
& \dot{\theta}_{M\_ref}, \dot{\theta}_{G\_ref}, P_{G\_ref}, V_{dc\_ref}, V_{M\_ref}, V_{G\_ref}, Q_{M\_ref}, Q_{G\_ref}]^T
\end{aligned} \quad (40)$$

Let  $k = \text{length}(w) = 40$  and  $r = \text{rank}(S(s)) = 32$ . From [37], the integer  $m = k - r$  is the rank of the module  $M$ , which coincides with the rank of the linear system  $\Sigma$ . The number of the independent inputs  $u = \{u_1, \dots, u_m\}$  of the system  $M$  given by (39) is the rank of the module which defines the system  $m = \text{rank}(M)$ . For the SHVDC case, the rank of the module  $M$  defined by the SHVDC is equal to 8, which means that the number of the independent inputs is 8. The latter coincides with the references of the schemes in Fig. 1. More precisely, with given initial conditions, trajectories for the reference variables can be imposed by the controls.

*Remark:* In the SHVDC system in Fig.1, the power balance has been verified with simulation: a step change of +0.3 p.u is applied to the reference power  $P_{G\_ref}$ . Fig.2 shows that the power  $P_M$  tracked the power  $P_G$ , the responses of both powers tracked the set-point  $P_{G\_ref}$  (modulo the losses). Thus, the line power is not changed in steady-state. From eq. (37) and (38), the transient difference between  $P_M$  and  $P_G$  is stored in the  $C_{dc}$  capacitor of the SHVDC line. More precisely, during the transient, the power balance is achieved using energy storage system in the HVDC link.

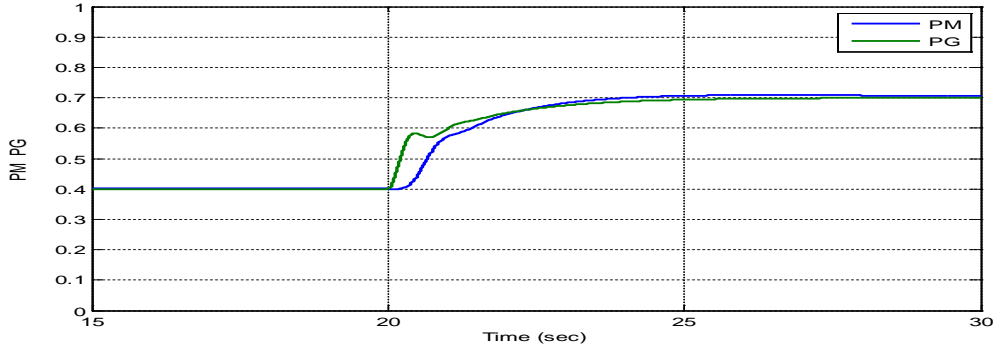


Fig.. 2 Responses of  $P_M$  and  $P_G$  to a +0.3 step in  $P_{g\_ref}=0.4p.u$

### 3. Transient stability criteria

Transient stability of a power system is defined as its the ability to maintain synchronism when subjected to a severe transient disturbance. In conventional transient stability studies, analysis aims at evaluating contingency severity in terms of stability limits. The stability limits of current concern are the power transfer limit  $P_{limit}$  for a given clearing time and the CCT for given pre-fault operating conditions [18, 29].

For a fault applied under given operating conditions, the CCT is defined as the maximal fault duration for which the system remains transiently stable [29]. The instability is then manifested by the loss of synchronism of a group of machines.

The power transfer limit  $P_{limit}$  considered here refers to the maximum flow of power that can be transmitted through a given flow gate (like, e.g., a line) for which the system remains dynamically stable after being subjected to a disturbance. The evaluation of the  $P_{limit}$  depends on the initial operating condition as well as on the nature of the disturbance.

Small disturbances are in the form of small load, generation or regulation set points variations and the system must be able to successfully transmit the power transfer limit noted  $P_{limit}^{small}$  under these disturbances [29]. The latter is the small signal stability margin of the system. Severe transient disturbances usually considered are short-circuits on buses or transmission lines. In this case, the power transfer limit is lower. We denote this limit

$P_{\text{limit}}^{\text{transient}}$  and it is a transient stability margin of the system which depends on the duration of the considered short-circuit. Obviously, the latter should be below the CCT [18].

The applications of HVDC transmission have considerable impact on stability limits of interconnected power systems [6-17]. For pure HVDC links as the example in Fig. 1, only the  $P_{\text{limit}}$  criteria is used. For parallel AC/DC lines (Fig. 13), the CCT of a three-phase fault on the AC line, measures the transient stability margin of the interconnected power systems, in addition to the total power limit  $P_{\text{limit}}$ . For both cases, the HVDC link is based on the synchronverter concept presented in Section 2. From [24] the standard parameters of the SG cannot be directly used for this structure. Thus, a specific tuning method of these parameters to enhance the stability limits and to satisfy the usual HVDC control requirements is discussed in the next section.

#### **4. Control model description**

The tuning methodology of the SHVDC parameters developed in [24] is briefly recalled. In the present work, the aforementioned tuning method is adopted for the interconnection of two weak AC systems having equal SRC=1 by VSC-HVDC link.

##### **4.1 HVDC Control Specifications**

The tuning procedure starts by defining the full set of control specifications for a VSC-based HVDC. In order to guarantee the set-points of the transmitted active power, the reactive power and the voltage at the points of coupling, their transient behaviour are tracked with the following transient performance criteria [27]:

- the response time of the active/reactive power is normally in the range of 50 ms to 150 ms;
- the response time for voltage is about 100 ms to 500 ms.

## 4.2 Control objectives

In [24], a tuning methodology of the SHVDC parameters has been developed. The synthesis of the SHVDC parameters takes into account poorly damped oscillation modes of the neighbouring AC zone of the HVDC link. As a consequence, the stability limit in terms of the CCT of the neighbouring zone was improved in addition to the local performances presented above. The local performances for active and reactive power tracking are ensured [24]. More precisely, the synthesis of the SHVDC parameters is done such that the stability performances is ensured for several cases of fault (i.e, the enhancement of the stability limits based on the power limit  $P_{\text{limit}}$  and the CCT) along with the local ones. In this case, the impact of the SHVDC control parameters and the inertia emulation on the power limit  $P_{\text{limit}}$  is analyzed. The latter is computed for small and transient signals disturbances. A hybrid HVAC/HVDC line test power system will be used in Section 5.

## 4.3 Control Structure

In order to satisfy the cited control objectives, the SHVDC parameters should be tuned simultaneously and in a coordinated way. For this tuning, a feedback control presented in Fig.3 is applied on the system in Fig.1 where  $\tilde{\Sigma}$  is modeled by (3)–(35) with the exception of (19), (20), (21), (22), (23), and (24).

All control parameters are grouped in the following diagonal matrix:

$$K(s,q) = \text{diag}(D_{gp}, D_{mp}, K_{p\_Vdc}, K_{i\_Vdc}, D_{gq}, D_{mq}, K_{p\_Pg}, K_{i\_Pg}) \quad (41)$$

where  $D_{gp}$ ,  $D_{mp}$  are, respectively, the static frequency droop coefficients of the SG and the SM;  $D_{gq}$ ,  $D_{mq}$  are, respectively, the voltage droop coefficient of the SG and of the SM;

$K_{p\_Vdc}$ ,  $K_{i\_Vdc}$  are the DC voltage PI control parameters; and  $K_{p\_Pg}$ ,  $K_{i\_Pg}$  are the active power PI control parameters.

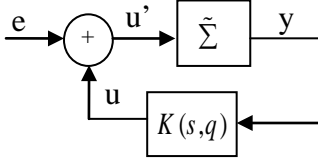


Fig. 3. Feedback system

Note that all elements of the matrix  $K(s, q)$  are tuned via the pole placement presented in the following Section to meet HVDC performance specifications given in Section 4.1.

The inputs  $u$  and the outputs  $y$  are

$$\mathbf{u} = [\mathbf{T}_{mm}, \mathbf{Q}_{mm}, \mathbf{T}_{mm-ref\_kp}, \mathbf{T}_{mm-ref\_ki}, \mathbf{T}_{gm}, \mathbf{Q}_{gm}, \mathbf{T}_{gm-ref\_kp}, \mathbf{T}_{gm-ref\_ki}]^T$$

$$\mathbf{y} = [\omega_n - s\theta_m, \mathbf{V}_{m\_ref} - \mathbf{V}_m, \mathbf{V}_{dc\_ref} - \mathbf{V}_{dc}, \frac{\mathbf{V}_{dc\_ref} - \mathbf{V}_{dc}}{s}, \omega_n - s\theta_g, \mathbf{V}_{g\_ref} - \mathbf{V}_g, \mathbf{P}_{g\_ref} - \mathbf{P}_g, \frac{\mathbf{P}_{g\_ref} - \mathbf{P}_g}{s}]^T.$$

#### 4.4 Parameters and residues of the Regulators

The tuning of the control parameters is based on the poles sensitivity to the regulators parameters. The tuning design of the SHVDC controller is developed using the linear approximation of the feedback system in Fig.3 (HVDC-VSC and its control system) around an equilibrium point. The latter is a steady state operating point of the system in Fig.1 satisfying the usual linearization conditions which avoid variable limitations (like saturations...) and singular operating conditions. Let  $H(s)$  be the transfer matrix of a linear approximation of  $\tilde{\Sigma}$  and consider each closed-loop of the feedback system which corresponds only to input  $u_i$  and output  $y_i$  (see Fig.3). More specifically,  $H_{ii}(s)$  and  $K_{ii}(s)$  are the (i,i) transfer functions of  $H(s)$ ,  $K(s, q)$ , respectively. The sensitivity of a pole  $\lambda$  of the closed-loop with respect to a parameter  $q$  of the regulator  $K_{ii}$  is

$$\frac{\partial \lambda}{\partial q} = r_\lambda \frac{\partial K_{ii}(s, q)}{\partial q} \quad (42)$$



where  $r_\lambda$  is the residue of  $H_{ii}(s)$  at pole  $\lambda$ . Note that, for our case. (41),  $\left. \frac{\partial K_{ii}(s, q)}{\partial q} \right|_{s=\lambda} = 1$

#### 4.5 Coordinated Tuning of SHVDC Parameters

First, desired locations  $\lambda_i^*$  can be computed for each pole  $\lambda_i$  starting from the control specifications given in Section 4.1. Table 1 summarizes the dynamics of interest and their associated modes. In fact, in Table 1; column 1 gives the dynamics of interest (for  $P_m, Q_m, P_g, Q_g$ ). Column 2 presents the initial mode associated with  $\lambda_i^0$  of each dynamic of interest. Column 3 gives the desired mode associated with  $\lambda_i^*$  of each dynamic of interest. The criteria used to evaluate the dynamics of interest are based on the participation factors given in Table 2. The latter presents only the highest participation factor for each dynamics of interest in the associated mode. Therefore each dynamic is associated to only one pole for the computation of desired poles  $\lambda_i^*$ . From the same column of Table 2; several poles have also significant participations to the same dynamic which led us to compute the gains  $K$  in a coordinated way. More specifically, if  $\Lambda$  denotes the set indices  $j$  from 1 to 8 for which  $H_{jj}(s)$  has  $\lambda_i$  as pole, the contribution of each control gain in the shift of the pole is

$$\lambda_i = \lambda_i^0 + \sum_{j \in \Lambda} r_{ij} K_j, \quad (43)$$

where  $\lambda_i^0$  is the initial (open-loop) location of the pole  $\lambda_i$  and  $r_{ij}$  is the residue of  $H_{jj}(s)$  in  $\lambda_i$ . Finally, the pole placement is the solution of the following optimization problem

$$\{K_j^*, j = 1 \dots 8\} = \arg \min_{K_j} \sum_i \|\lambda_i^* - \lambda_i\|^2, \quad (44)$$

Table 1 Desired modes meeting the HVDC specifications Case 1: h=3.2 s

Dynamics of interest	$\lambda_i^0$	$\lambda_i^*$
Voltage $v_m$	-5.2	-10
Voltage $v_g$	4.2	-10
Active Power $P_m$	2.8± 5.3i	-21±21.4i
Active Power $P_g$	-2.8±5.3	-21±21.4i
Reactive Power $Q_m$	-5.2	-10
Reactive Power $Q_g$	4.2	-10

Table 2 Participation factors Case 1: h=3.2 s

%	$P_{a\_V_m/Q_m}$	$P_{a\_P_m}$	$P_{a\_V_g/Q_g}$	$P_{a\_P_g}$
$\lambda_{v_m Q_m}^0$	41.29	34.1	0.34	0.16
$\lambda_{p_m}^0$	29	66.66	0.15	0.22
$\lambda_{v_g Q_g}^0$	0.16	0.2	31.06	42.57
$\lambda_{p_g}^0$	0.25	0.2	34.9	57.35

where  $\lambda_i$  is given by (43). The used optimization tool is based on the constrained nonlinear solver *fmincon* of Matlab Optimization Toolbox. The flow chart of the tuning parameters approach for the SHVDC converters is given in Fig. 4.

#### 4.6 Simulations Results

In this Section, we consider is a pure VSC-HVDC link interconnection of two very weak AC systems, having SCR=1. The simulation method of the VSC converters is based on the average value model (AVM). The AVM approximates system dynamics by neglecting switching details [30]. The AVM of the VSCs consists of equivalent voltage sources generating the AC voltage averaged over one cycle of the switching frequency [30]-[32]. The average-value VSC-HVDC model used in the simulations is rated at ±100 kV, 200 MVA. The detailed system data is given in the appendix.

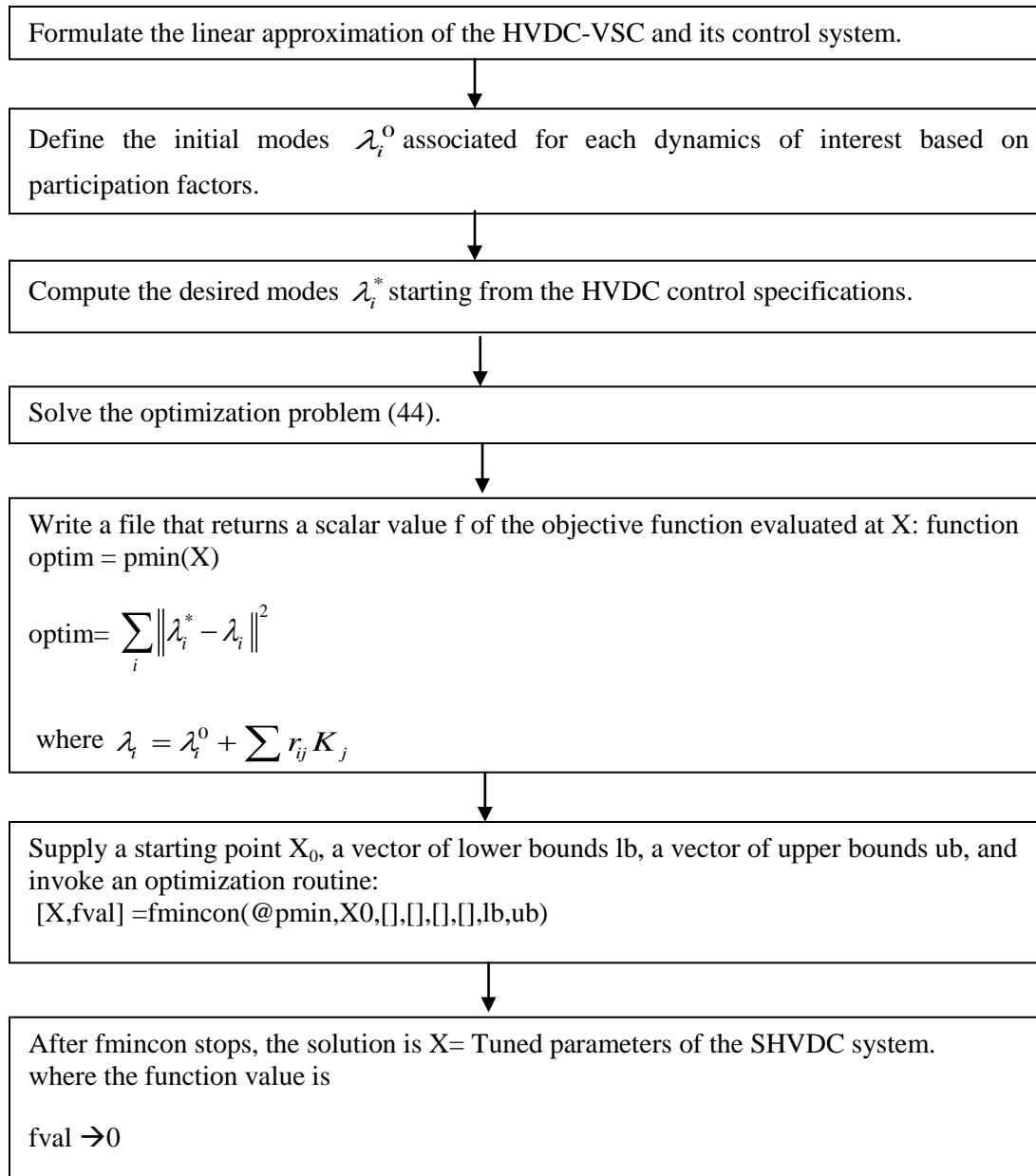


Fig. 4. Flow chart of the tuning SHVDC parameters

The optimal SHVDC parameters in the second column of Table 3 were obtained with (41) solved for the desired locations in Table 1. The new controller is compared with the standard vector controller. The latter has a cascade structure with an inner loop faster than the outer one. The proportional and integral gains of this control are tuned to satisfy the same performance specifications given in Section 4.1.

Table 3 SHVDC parameters

$K$	SHVDC optimal parameters for case 1	SHVDC optimal parameters for case 2
$D_{mp}$	15.06	10.38
$D_{mq}$	84.12	84.07
$K_{p\_V_{dc}}$	0.38	0.41
$K_{i\_V_{dc}}$	0.87	0.81
$D_{gp}$	200.8	250.52
$D_{gq}$	375	400
$K_{p\_P_g}$	32.33	30.27
$K_{i\_P_g}$	20.45	24.35

For the two controllers, small-signal and the transient stability margins, respectively  $P_{lim}^{small}$  and  $P_{lim}^{transient}$ , are evaluated using Matlab/Simulink toolbox. In the simulations, the limitations in DC current as well as DC voltages are taken into account, by saturations introduced in the PI controllers. In our case, the rated DC current and DC voltage are, respectively, 2 kA and  $\pm 100$  kV. The maximum DC current is chosen 3.5 kA, so that the VSC valves currents will not exceed their acceptable current limits [33, 34]. The maximum limit of the DC voltage is chosen 1.5 p.u of the rated voltage as in [35-36].

#### A) Small-signal stability margin

A small step change is applied to different reference levels  $P_{g\_ref}$  of the active power transmitted through the HVDC link (see (21)). The  $P_{limit}^{small}$  is the maximum value of  $P_{g\_ref}$  for which the system remains dynamically stable after a small step change. Fig.5 shows the responses of the HVDC link of Fig.1 with the standard vector control. Fig.5.a presents steady-state responses of the system and instability after + 0.05 p.u step change to the active power reference  $P_{g\_ref} = 0.4$  p.u in 20 s. Similar tests shown in Fig.6 with lower values for  $P_{g\_ref}$  led to stable responses. Thus  $P_{limit}^{small} = 0.39$  p.u when the standard vector control is used.

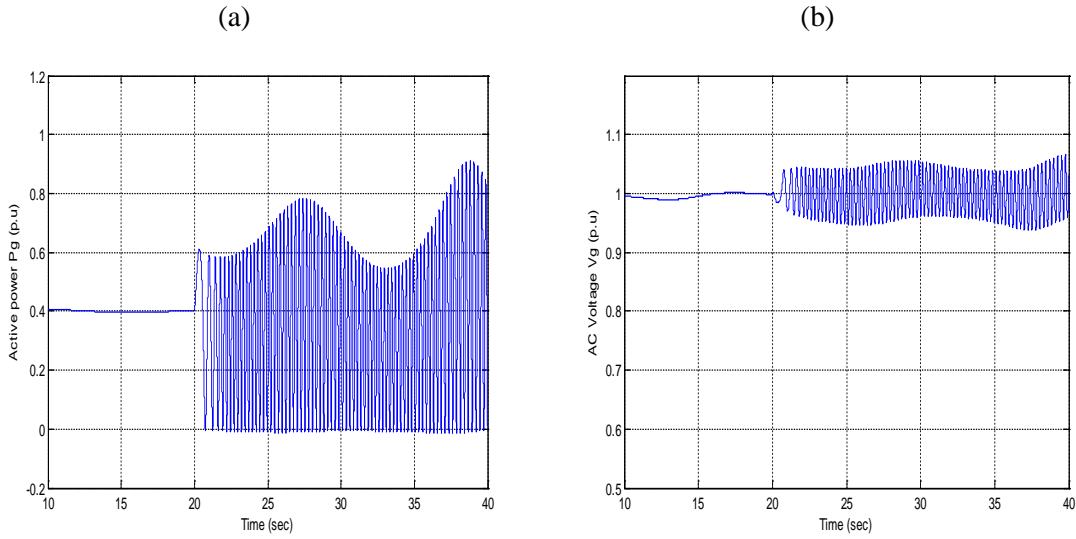


Fig. 5 Responses with vector control to a +0.05 step in  $P_{g\_ref}=0.4$ p.u (p.u). (a) Response of  $P_g$  ; (b) Response of  $V_g$  .

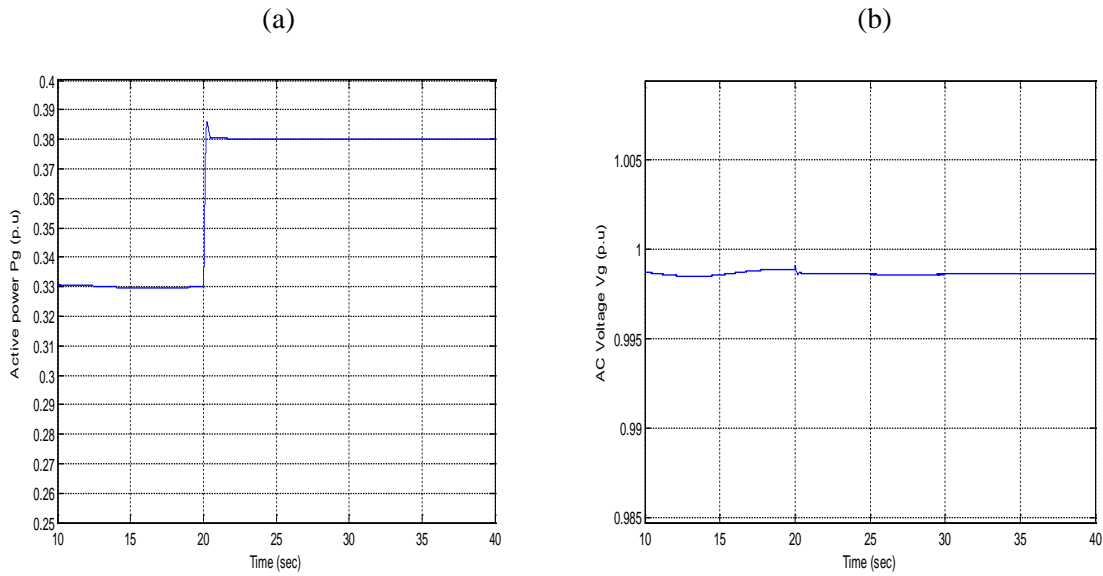


Fig. 6 Responses with vector control to a +0.05 step in  $P_{g\_ref} = 0.33$  (p.u) (a) Response of  $P_g$  to a +0.05 step in  $P_{g\_ref}$  (p.u) ; (b) Response of  $V_g$  .

Second, responses of the SHVC link of Fig.1 with the new tuned parameters are presented in Fig.7. The latter shows steady-state responses of the system and instability after + 0.05 p.u step increase in the active power reference  $P_{g\_ref} = 0.78$  p.u. Stable responses of the SHVDC are illustrated in Fig.8.

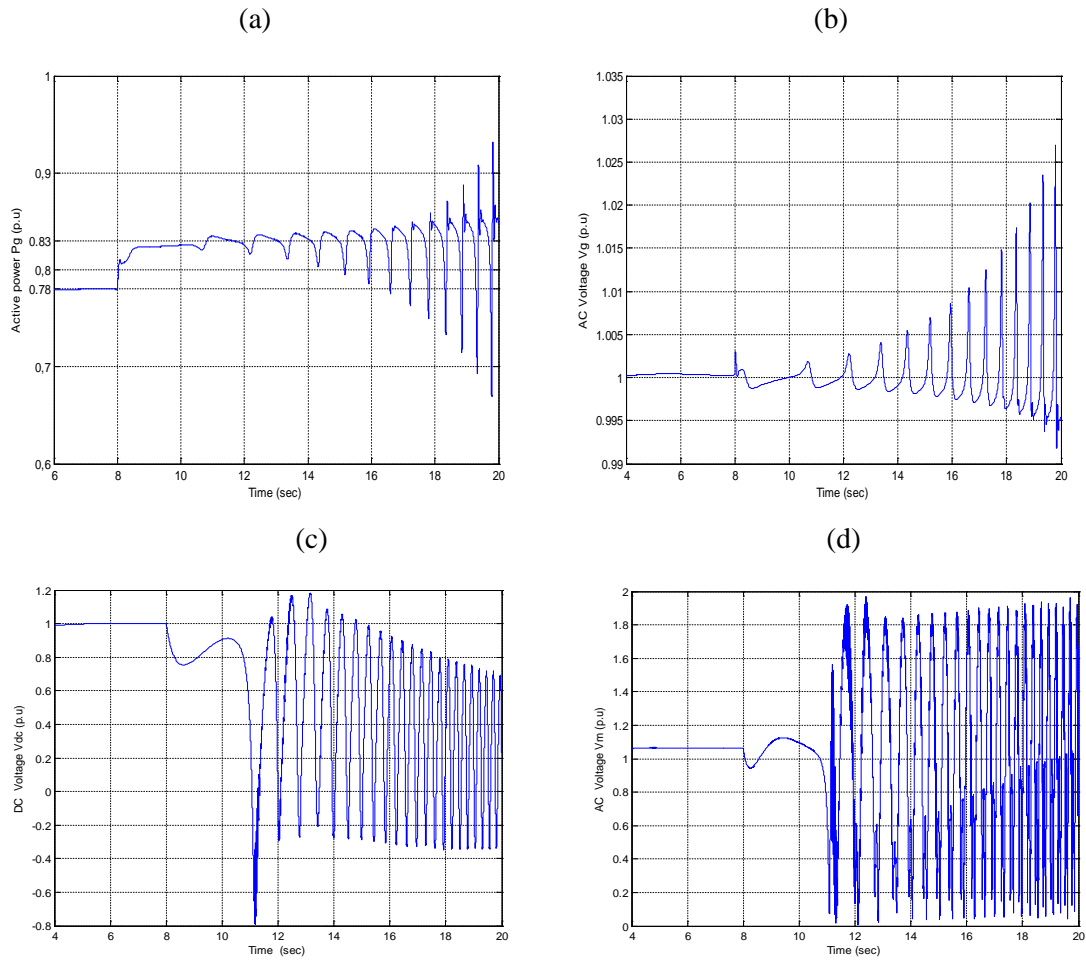


Fig. 7 Responses of SHVDC to a +0.05 step in  $P_{g\_ref}$  for the operating point of  $P_g = 0.78$  p.u and  $H = 3.2$  s. (a) Response of  $P_g$ ; (b) Response of  $V_g$ ; (c) Response of  $V_{dc}$ ; (d) Response of  $V_m$ .

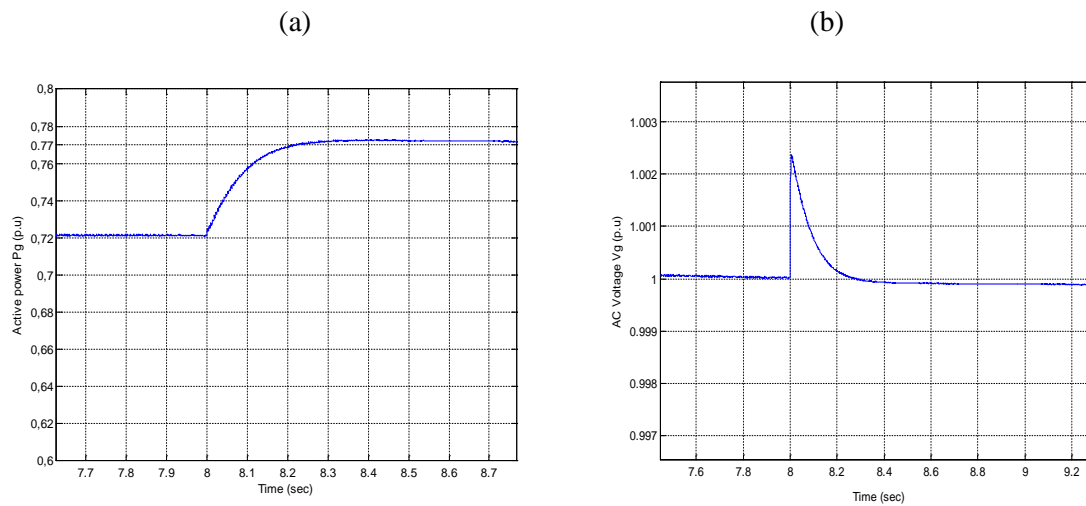


Fig. 8 Responses of SHVDC to a +0.05 step in  $P_{g\_ref}$  for the operating point of  $P_g = 0.72$  p.u and  $H = 3.2$  s. (a) Response of  $P_g$ ; (b) Response of  $V_g$ .

The same rationale as above proves that the SHVDC structure enables a power transfer limit  $P_{\text{limit}}^{\text{small}}$  of approximately 0.77 p.u. The latter is greater than the one obtained with the vector control. As a consequence, the SHVDC control improves the dynamic power transmission capacity of an HVDC link which connects two weak AC systems.

#### B) Transient signal margin

The total fault clearing time consists of the relay time and the breaker-circuit interrupting time [29]. On high voltage (HV) transmissions systems, the normal relay times range from 35 to 70 ms and the breaker-circuit interrupting times range from 70 to 130 ms.

In this study, a three phase short-circuit is applied at the point of common coupling, labeled PCC2 in Fig.1. The  $P_{\text{limit}}^{\text{transient}}$  is the maximum power through the DC link for which the system remains transiently stable after the clearing of the short-circuit. A 200 ms duration of the short-circuit was chosen for the tests. First, when the VSC-HVDC link uses vector control, the  $P_{\text{limit}}^{\text{transient}}$  is only 0.3 p.u (Fig.9.a), and the system is unstable when operating at a higher power than 0.3 p.u (Fig.9.b).

Second, the responses of the SHVDC system are presented in Fig. 10. The latter shows stable responses to a 200 ms short circuit with a power transfer of  $P_g = 0.64$  p.u. However, Figs. 11 show unstable responses of the SHVDC system with a power transfer of 0.65 p.u to the same fault. Therefore, the power transfer limit  $P_{\text{limit}}^{\text{transient}}$  is 0.64 p.u. The latter is greater than the one obtained with the vector control. This confirms the previous result on small signals margin stability of the system in Fig.1. Note also, that for the same controller, the  $P_{\text{limit}}^{\text{transient}}$  is smaller than the  $P_{\text{limit}}^{\text{small}}$ . This is because the power transfer limit depends on the type of fault.

(a)

(b)

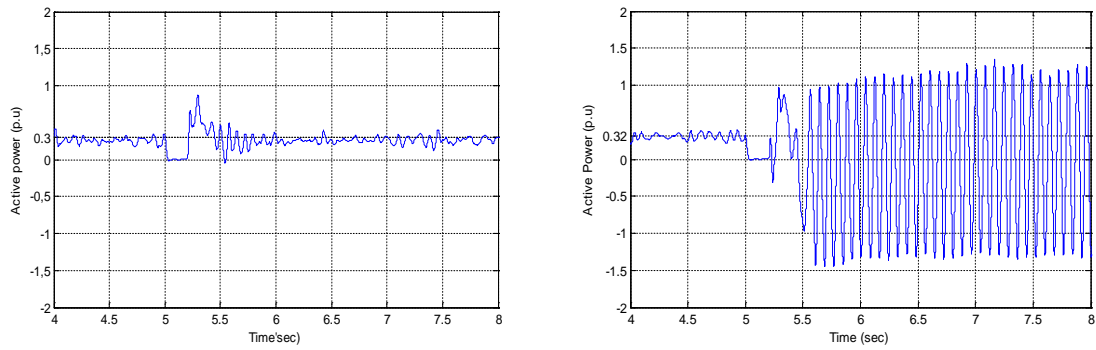
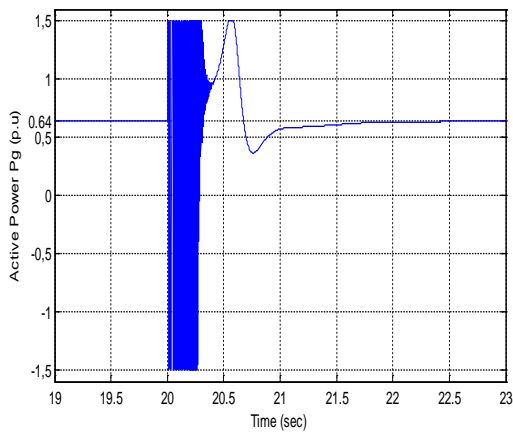


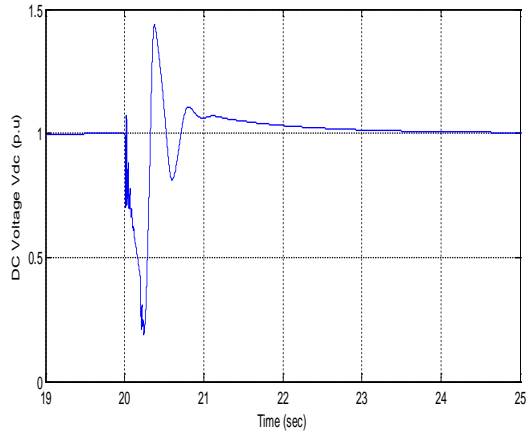
Fig. 9 Active power responses with the vector control to a 200 ms short-circuit and SCR = 1.0. (a) Response of  $P_g$  for  $P_{g\_ref} = 0.3 pu$  ; (b) Response of  $P_g$  for  $P_{g\_ref} = 0.32 pu$  .

(a)

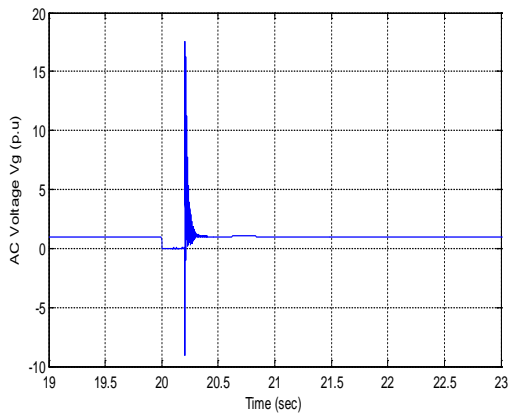
(b)



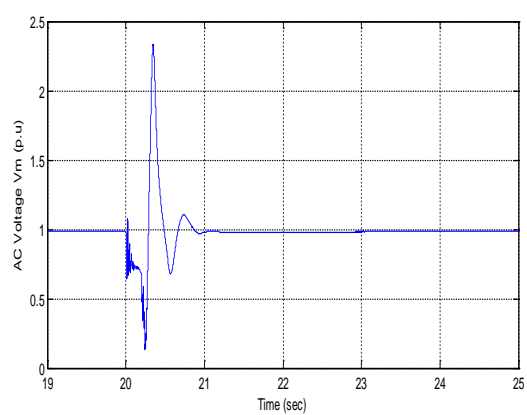
(c)



(d)



(a)



(b)

Fig.10 Responses of SHVDC to a 200 ms short circuit for the operating point of  $P_g = 0.64 p.u.$  (a) Response of  $P_g$  ; (b) Response of  $V_{dc}$  ; (c) Response of  $V_g$ ; (d) Response of  $V_m$ .



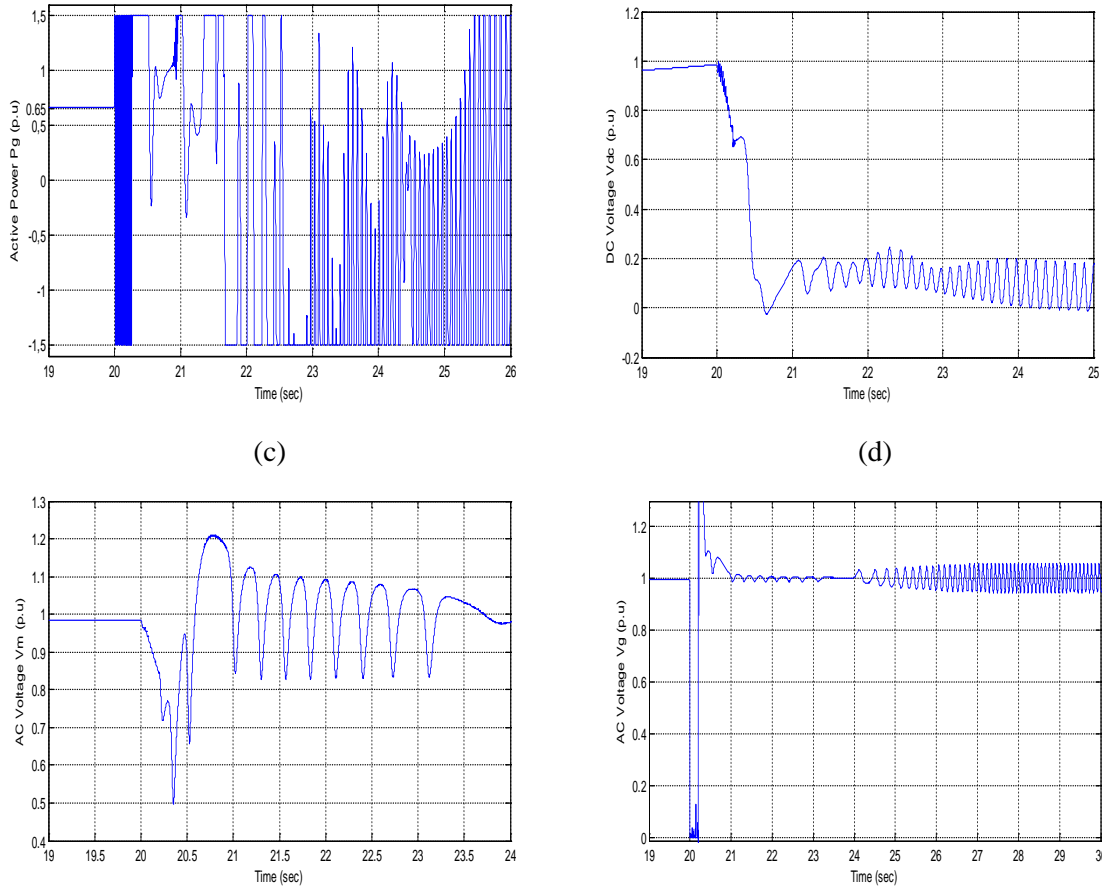


Fig. 11 Responses of SHVDC to a 200 ms short circuit for the operating point of  $P_g = 0.65$  p.u. (a) Response of  $P_g$ ; (b) Response of  $V_{dc}$ ; (c) Response of  $V_g$ ; (d) Response of  $V_m$ .

### C) Effect of the inertia emulation

The advantage of the SHVDC structure is to mimic the closed-loop of a standard synchronous generator. Thus inertia emulation is provided, and it is well known that the inertia has a positive impact on the stability of AC systems [30]. For this reason, we have analyzed the SHVDC structure for two different inertia constants:

- case 1:  $H=3.2$  s (case in Section 4.2).
- case 2:  $H= 5$ s.

For the two cases, we have placed the poles of the closed-loop at the same desired locations of dynamics of interest (Table 1). For case 2, the obtained tuned SHVDC parameters K are given in the third column of Table 3.

Table 4  
Desired modes meeting the HVDC specifications Case 2: h=5s

Dynamics of interest	$\lambda_i^0$	$\lambda_i^*$	$r_{\lambda_i^0}$
Voltage $v_m$	-4.2	-10	-0.16
Voltage $v_g$	3	-10	-0.3
Active Power $P_m$	$5.1 \pm 8.3i$	$-21 \pm 21.4i$	$-0.03 \pm 0.1i$
Active Power $P_g$	$-1.8 \pm 8.3$	$-21 \pm 21.4i$	$-0.01 \pm 0.1i$
Reactive Power $Q_m$	2.6	-10	-0.5
Reactive Power $Q_g$	1.36	-10	-0.2

Fig.12 shows that the system SHVDC of case 2 is unstable for  $P_{g-ref}$  magnitude just greater than 0.78 p.u. As a consequence, the  $P_{limit}$  for case 2 is equal also to 0.77 p.u as for case 1. We can thus conclude that the improvement of the power transfer limit  $P_{limit}$  is due to the control manner (the way the closed-loop poles are placed) and not to the inertia emulation provided by the SHVDC structure.

## 5. Hybrid HVAC-SHVDC link test power system

In this Section, the specific tuning of the SHVDC parameters presented in Section 4.1 is tested on the hybrid HVAC-SHVDC interconnection shown in Fig. 13. As for the case of the two-bus system in Fig.1, the two AC zones are very weak, having equal SCR=1.

The two systems are connected with an AC line in parallel with one HVDC link. The 100 km HVDC cable link has a rated power of 200 MW and a DC voltage rating of  $\pm 100$  kV. Each area consists of two identical generating units of 400 MVA/20 kV rating. Each of the units is connected through transformers to the 100 kV transmission line. There is a power transfer of 400 MW from Area 1 to Area 2. The detailed system data is given in the appendix. The control SHVDC approach is assessed for stability limits based on the CCT and the  $P_{limit}$ , defined in Section 3.

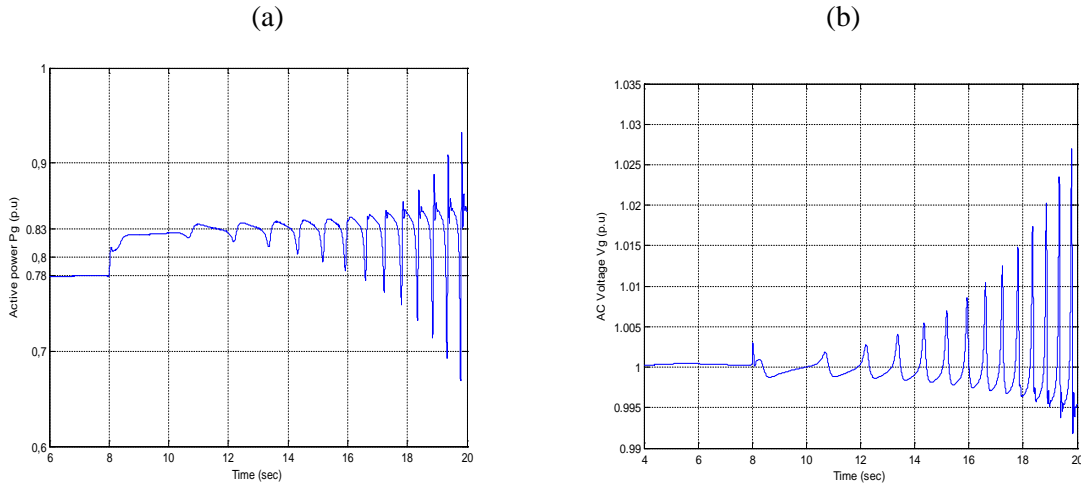


Fig. 12 Responses of SHVDC to a +0.05 step in  $P_{g\_ref}$  for the operating point of  $P_g = 0.78$  p.u and  $H= 5$  s. (a) Response of  $P_g$ ; (b) Response of  $V_g$ .

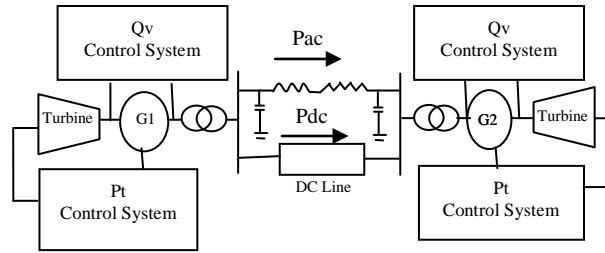


Fig 13. Hybrid HVAC-SHVDC interconnection

In this Section, only the transient stability margin  $P_{limit}^{transient}$  of the interconnected system is investigated. In the following, the influence of the relative capacity of the HVDC and the parallel AC transmission is investigated. The ratio between the active power transmitted and the continuous power to HVDC line is defined by

$$\rho = \frac{P_{ac}}{P_{dc}}, \quad (45)$$

where  $P_{ac}$  is the per unit active power transmitted by the AC line and  $P_{dc}$  is the continuous per unit power of the DC line. According to [17], the strength of an AC line may be quantified by the value of  $\rho$ :

- $\rho \geq 1.5$ : the AC line is strong
- $\rho \leq 0.7$ : the AC line is weak

The objective of this study is to investigate the impact of the ratio  $\rho$  on the stability limits in the case of hybrid HVAC-SHVDC interconnection.

### 5.1 Results of the CCT with different $\rho$

The reference per unit power of the DC line  $P_{g\_ref}$  presented in equation (21) is varied ( $P_{g\_ref} = 0.2, 0.4, 0.6, 0.8, 1, 1.2$  and  $1.3$ ) keeping constant the total transmitted power:

$$P_T = P_{dc} + P_{ac} = \text{constant} . \quad (46)$$

The obtained CCTs for a three-phase short-circuit fault in the mid-point of the AC transmission line are presented in Table 5 for different DC power levels. From Table 5, the increase of the DC power reduces the AC power transfer capability and improves therefore the CCT. We can see from Table 5 that the SHVDC control with new tuned parameters improves the transient stability of the system and thus augments the transient stability margins of the neighbouring AC zone when the AC line becomes weaker. This is due to the fact that the DC power is increased, and that the dynamics of the neighbour zone are taken into account at the synthesis stage via the oscillatory modes in Table 2. The gains of the controllers are computed not only for the local HVDC dynamics, but also to damp oscillatory modes and diminish the general swing of the zone. Moreover, it is noted that for each value of  $P_{g\_ref}$ , the gains  $K$  of the SHVDC controller are not recomputed and are thus the same as in the appendix. This result confirms the robustness test in [29] of the performances of the proposed controller against variation of operating conditions.

### 5.2 Results of the $P_{lim}^{transient}$ with different $\rho$

In this Section, the total power limit  $P_{lim}^{transient}$  given in (46) is evaluated for different per unit DC powers  $P_{g\_ref}$  ( $P_{g\_ref} = 0.2, 0.4, 0.6, 0.8, 0.85, 0.9, 1, 1.2$  and  $1.3$ ).

Table 5 CCT with different DC power

DC Power $P_{g\_ref}$ (p.u)	AC power $P_{ac}$ (p.u)	$\rho = \frac{P_{ac}}{P_{dc}}$	CCT (ms)
0.2	1.32	6.6	210
0.4	1.12	2.8	240
0.6	0.92	1.53	260
0.8	0.72	0.9	290
1	0.52	0.52	310
1.2	0.32	0.23	340
1.3	0.22	0.17	355

To evaluate the power transfer limit  $P_{limit}^{transient}$  of the interconnection, a short-circuit of 200 ms duration (which is below to the CCTs computed for all DC power levels (see Table 5)) is applied in the middle of the AC line. It is noted that the power limit  $P_{limit}^{transient}$  is the maximum power through the two parallel lines for which the system remains stable after the short-circuit. The results in Table 6 and Fig. 14 show that  $P_{limit}^{transient}$  is proportional to the DC power. However, this latter reaches a maximum value called *the optimal power limit*  $P_{limit}^{optim}$ . We can see that the transient stability margin of the parallel HVAC/HVDC line is reduced when the power limit is greater than the  $P_{limit}^{optim}$ . As a result, for the hybrid HVAC/HVDC transmission lines, the increase of the DC power of the HVDC line above this threshold could not improve any longer the power limit of the interconnection, whereas the increase of the DC power improves the transient stability of the interconnected power system without any constraint. From Table 5, we can note the *optimal DC power* transferred by the HVDC  $P_{dc}^{optim} = 0.85$  p.u which corresponds to the power limit  $P_{limit}^{optim} = 1.8$  p.u.

Table 6 The power limit levels  $P_{lim}^{transient}$  with different DC power levels

DC Power $P_{g\_ref}$ (p.u)	AC power $P_{ac}$ (p.u)	$\rho = \frac{P_{ac}}{P_{dc}}$	$P_{lim}^{transient}$
0.2	1.31	6.55	1.48
0.4	1.11	2.77	1.65
0.6	0.91	1.51	1.72
0.8	0.71	0.88	1.79
0.85	0.66	0.78	1.8
0.9	0.61	0.67	1.78
1	0.51	0.51	1.75
1.2	0.31	0.25	1.55
1.3	0.22	0.09	1.42

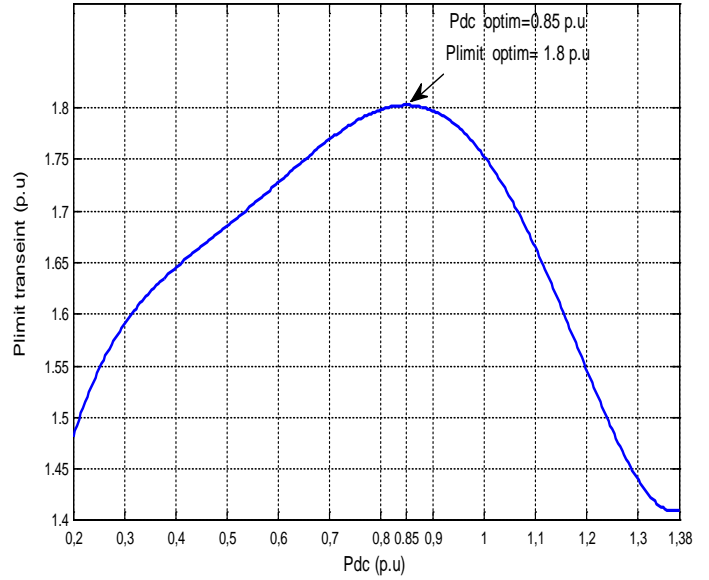


Fig.14 The power limit  $P_{lim}^{transient}$  with different DC power levels

## 6. Conclusion

This paper proposed a control strategy of HVDC transmission yielding increased power transfer capacity and enhanced transient stability of weak interconnected systems. The proposed control is based on the synchronverter control and emulation of the HVDC link (SHVDC).

The transient stability of the neighbouring zone is a priori taken into account at the design level of the control. The parameters of the SHVDC regulators are tuned based on a specific residues method. The control strategy has been tested for two configurations of interconnection of two very weak AC zones: an HVDC line and a hybrid HVAC-HVDC interconnection. In the former, the performance of the control strategy is analyzed by the power limit  $P_{limit}$ . In the latter, both the  $P_{limit}$  and the Critical Clearing Time (CCT) are used. For these configurations, better results than with the vector current control were obtained:

- (i) Better dynamic performances, since this approach allows one to analytically take into account dynamic specifications at the tuning stage.

(ii) Better dynamic transmission capacity  $P_{\text{limit}}$  of the HVDC connection of two weak AC zones in the sense that the link capacity under stability constraints is higher than the one obtained when using the standard vector control. This improvement of the dynamic transmission power is due to the way by which the SHVDC parameters are tuned and not to the inertia emulation provided by the SHVDC structure.

(iii) Better stability margins have been obtained for both interconnection cases: hybrid HVAC/SHVDC or only SHVDC. Swing information is directly taken into account at the synthesis stage in terms of the less damped modes of the neighbouring zone and not only for the local HVDC dynamics as it is the case for the standard VSC control.

The exploitation of the proposed control in weak interconnected systems may be more advantageous in low-inertia systems which have limited number of rotating machines, or no machines at all. Examples of such applications can be found when an HVDC link is powering island applications or if it is connected to renewable generators.

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## 8. Appendix

Network data (Fig. 1)

Bus system 1 & 2: line voltage=230 kV, frequency=50 Hz, Short circuit power=200 MVA,  $R_s=0.75\Omega$ ,  $L_s=0.2$  H. Generators G1, G2: Rated 200 MVA, 100/230 kV

DC system: voltage=  $\pm 100$  kV, rated DC power=200 MW,

Pi line  $R=0.0139 \Omega/\text{km}$ ,  $L=159 \mu\text{H}/\text{km}$ ,  $C=0.331 \mu\text{F}/\text{km}$ , Pi line length= 150 km, DC capacitor=0.005 F, smoothing reactor:  $R=0.0251\Omega$ ,  $L=100\text{mH}$ .

Parameters of the classic vector control: current loop:  $k_p=5$ ,  $k_i=1$ , AC voltage control:  $k_i=20$ , active power control:  $k_i=20$ , DC voltage control:  $k_p=5$ ,  $k_i=2$ .

Tuned of the the SHVDC parameters for SHVDC interconnection (Fig. 1)

Network data (Fig. 13)

Generators G1, G2: Rated 200 MVA, 20 kV

$X_l$  (p.u): leakage Reactance = 0.18,  $X_d$  (p.u): d-axis synchronous reactance = 1.305,  $T'd_0$  (s): d-axis open circuit sub-transient time constant =0.296,  $T'd_0$  (s): d-axis open circuit transient time constant = 1.01

$X_q$  (p.u): q-axis synchronous reactance = 0.053,  $X_q$  (p.u): q-axis synchronous reactance = 0.474,  $X''_q$  (p.u):  
 q-axis sub-transient reactance =0.243,  $T''_{q0}$  (s): q-axis open circuit sub transient time constant=0.1  
 $M = 2H$  (s): Mechanical starting time = 6.4  
 Governor control system: R (%): permanent droop =5, servo-motor:  $k_a = 10/3$ ,  $t_a$  (s) = 0.07, regulation PID:  
 $k_p = 1.163$ ,  $k_i = 0.105$ ,  $k_d = 0$   
 Excitation control system: Amplifier gain:  $k_a = 200$ , amplifier time constant:  $T_a$  (s) = 0.001, damping filter  
 gain  $k_f = 0.001$ , time constant  $t_e$  (s) = 0.1  
 Generator transformers Rated 400 MVA, 20/ 100 kV , Coupling Delta/  $Y_g$   
 Primary resistance (p.u) =0.002, Primary inductance (p.u) =0.12  
 Secondary resistance (p.u) =0.002, Secondary inductance (p.u) =0.12  
 Loads: PL1=200 MW, PL2= 1GW  
 AC transmission lines  
 Resistance per phase ( $\Omega/km$ ) =0.03, Inductance per phase (mH/km) =0.32, Capacitance per phase (nF/km)  
 =11.5  
 Tuned of the the SHVDC parameters for the hybrid HVAC-SHVDC interconnection (Fig. 13)  
 $K = [ 55; 46.4; 24.0 ; 28.5 ; 65.64; 58.069; 56.39; 25.11; 37.35]$ .