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On-Wafer Measurement Errors due to Unwanted Radiations on High-Q Inductors

Olga Bushueva, Christophe Viallon, Member, IEEE, Ayad Ghannam, and Thierry Parra

Abstract—This paper investigates the disagreements that may occur between on-wafer measurements and electromagnetic (EM) simulations of high-Q inductive devices. Such disagreements are highlighted on a planar spiral inductor and a 3-D solenoid which exhibit measured maximum Q-factors of 27 and 35 respectively, while 42 and 45 were expected from EM simulations. Both devices are fabricated on high-resistivity substrates. A radiative interaction is identified between RF probe and inductive device under test. By using EM simulations, extra-losses associated with this parasitic effect are fully modeled through the calculation of radiation and dissipation related Q-factors. Adjustments of onwafer probe setup are proposed to reduce this parasitic effect. Finally, the 3-D solenoid inductor is characterized using a new experimental fixture, and the maximum Q of 45 predicted by EM simulation is retrieved.

Index Terms—On-wafer measurements, quality factor, high-Q inductor, high-resistivity substrate, RF probe, radiation.

I. INTRODUCTION

THE increasing demand for miniaturized, efficient and low-cost microwave integrated systems requires the development of new technologies offering high-Q interconnections and inductive passive devices. The performance evaluation of such device is usually performed on-wafer by using a vector network analyzer (VNA) along with coaxial cables and RF probes. A high level of accuracy is reached by first removing the contribution of the measurement test-bench up to the end of the RF probe tips [1]. This calibration process involves well-proven methods such as Short-Open-Load-Thru (SOLT). As a second step, specific on-wafer standards can be also measured to deembed the influence of pads and interconnections surrounding the device-under-test (DUT). Measurement reference planes are then shifted toward DUT terminals.

However, even if the previous calibration process is performed carefully, some errors may remain when characterizing high-Q RF inductors. Mismatches between measurements and simulations may be observed which are usually resulting from the probing setup surrounding the inductor. These disagreements depend on the nature of the problem and they are noticed on the shape of the extracted Q-factor versus frequency, with unexpected ripples and/or resonances. Several parasitic phenomena leading to characterization errors have

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been already identified. In [2], a return current path difference to the nearby ground through a high-resistive substrate is observed. RF probes can also stimulate a second propagating mode along with the chuck that couples to the DUT at some specific situations [3], [4]. At millimeter-wave frequencies, the RF probe can couple with the DUT and adjacent test structures enabling new conduction paths [5], [6].

In this paper, a parasitic radiative interaction between the RF probe and the on-wafer measured structure is investigated and fully analyzed. As a parasitic dipole antenna structure is set up, Q-factor values from experiment are well below values extracted from EM simulations. This issue has been observed above few GHz, when the Q-factor of inductors exceeds the range 25 to 35. It is demonstrated in Section II for both inductors, 3-D solenoid and planar spiral integrated on glass or high-resistivity silicon (HR-Si). But it has been noticed also for LTCC inductors [1].

Some EM analyses including the RF probe are carried out on the same devices, as detailed in Section III. The implementation of the RF probe significantly improves the agreement between simulations and measurements. Plotted radiation patterns suggest that measured Q-factors are underestimated because of EM radiations caused by this interaction between RF probe and DUT.

Section IV explains how to quantitatively determine the weight of all parasitic phenomena from the EM field calculated within the simulated volume. Dissipation and radiation contributions are then separated into the overall Q-factor.

In the last section, several approaches are explored to almost remove the radiation. They are based on a better confinement of the field by properly setting the DUT surrounding metal ring.

II. ILLUSTRATION OF DISAGREEMENTS BETWEEN SIMULATION AND MEASUREMENT

Inductors used for the demonstration of this issue are described below. The first one is a 4-turn 3 nH 3-D solenoid (Fig. 1). It has been fabricated either on HR-Si ($4.5 \text{ k}\Omega \text{ cm}$) or on glass substrate using a low-cost single-step 3-D copper electroplating process [7] marketed by 3DiS Technologies company. Metal conductor is $13 \mu\text{m}$ -thick and $30 \mu\text{m}$ -wide. Turn spacing is $120 \mu\text{m}$, and the upper part is placed on top of $100 \mu\text{m}$ -thick, $60 \mu\text{m}$ -wide and $360 \mu\text{m}$ -long SU8 pillars. Further results are related to the HR-Si version. The second device is a 3-turn 7.1 nH planar spiral inductor fabricated on HR-Si substrate using a standard planar process (Fig. 1). Spiral dimensions and process cross-section are given in Fig. 2.

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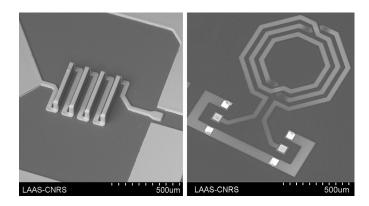


Fig. 1. Microphotographs of the $3 \,\mathrm{nH}$ 3-D solenoid (left) and the $7.1 \,\mathrm{nH}$ planar spiral inductor (right).

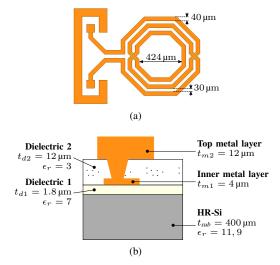


Fig. 2. (a) Top view and (b) schematic cross-section of the $7.1\,\mathrm{nH}$ planar spiral inductor.

S-parameters measurements are carried out using an Anritsu vector network analyser (VNA) MS4647A and a Cascade Microtech Infinity Ground-Signal-Ground (GSG) RF probe with a pitch of 150 µm (Fig. 3). A SOLT calibration is performed prior to measurements in order to set the reference plane at the end of probe tips. The accuracy of the test bench has been carefully checked by repeating several calibrations and measurements on the same DUT, using other VNA, cables, probes and calibration standards. A worst-case error magnitude of 0.002 was observed on the reflection coefficient of the 3-D solenoid all over the measured frequency range from 400 MHz to 20 GHz. At 5 GHz, this value translates into an error of about 6% on a Q-factor of 30 [1], which is quite acceptable. RF pads have not been deembedded to avoid any additional measurement error. EM simulations are performed using the 3-D EM simulation software HFSS from ANSYS. Internal excitation ports are drawn between signal pad and ground to keep the same location as for measurements.

Measured and simulated S-parameters of inductors are converted to Y-parameters. The quality factor Q and inductance value L are then extracted from the admittance Y_{11} using (1) and (2). For a two-port inductor, this formulation considers that port 2 is shorted.

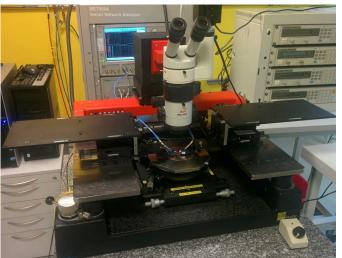


Fig. 3. Photograph of the measurement set-up.

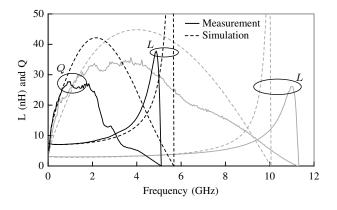


Fig. 4. Measured and simulated Q-factor and inductance value of both 3-D solenoid (grey curves) and planar spiral inductor (black curves).

$$Q = -\frac{\operatorname{Im}\left(Y_{11}\right)}{\operatorname{Re}\left(Y_{11}\right)} \tag{1}$$

$$L = \frac{\mathrm{Im}\left(Y_{11}^{-1}\right)}{\omega} \tag{2}$$

Q-factor and inductor values from simulations and measurements are displayed in Fig. 4 for both inductors. Good agreement between inductance and Q-factor values can be noticed as long as losses are mainly governed by skin and proximity effects, and located into conductors (below 1 GHz for the planar spiral and 2 GHz for the solenoid). However, a large mismatch is observed on Q-factors when frequency is increasing and on self-resonant frequencies (SRF). Simulated Q-factor values (42 and 45) are respectively 29% and 56% highers than measured ones (27 and 35). Such differences cannot be explained by the 6% measurement uncertainty.

The solenoid presented in Fig. 1 has been fabricated on a glass wafer. It is characterized using the previous test bench and, this time, two other $150 \,\mu\text{m}$ GSG RF probes are also used, i.e., a Cascade Z-probe Z67 and a MPI T40. Extracted Q-factors (Fig. 5) show that previously observed mismatches are probe-dependent and are still much larger than stated

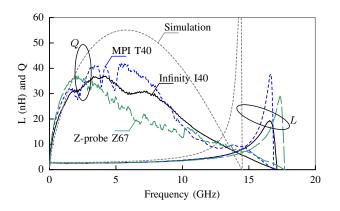


Fig. 5. Influence of RF probes on the extracted Q-factor and inductance values of the 3 nH 3-D solenoid inductor fabricated on a glass substrate.

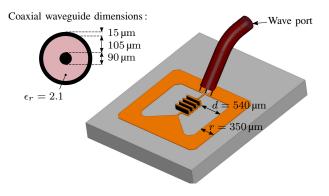


Fig. 6. 3-D modeling of the solenoid inductor including RF probe.

measurement uncertainty. Main differences between all those RF probes are the final part of the feeding waveguide and its elevation angle. The Z67 probe is based on a tapered coplanar waveguide sloped by 30° while 45° coaxial waveguides are used on T40 and Infinity RF probes. This result point out an interaction between RF probes and the DUT. In following sections only the Infinity RF probe is considered but observations and conclusions have been verified to be the same for the other probes.

III. EM SIMULATIONS INCLUDING RF PROBE

A model of the Cascade Infinity RF probe is added into the EM modeling to improve the agreement of this modeling with the measurement environment, and to investigate interactions that occur between the RF probe and DUT. Only the final part of the feeding coaxial waveguide beyond the absorber shielding is added into the simulation box (Fig. 6). All dimensions are given in Fig. 6. They have been extrapolated from the approximate measure of the external diameter of the coaxial waveguide to fit with a 50 Ω characteristic impedance (manufacturer does not provide such data). Probe tips are simplified, as well, since it can be considered that their length $(100 \,\mu\text{m})$ is small enough when compared with wavelength values under 15 GHz. The elevation angle of the actual probe from the horizontal plane is respected. The bend of the coaxial waveguide noticeable in Fig. 6 is added only to reach the lateral face of the simulation box where a wave-port is drawn.

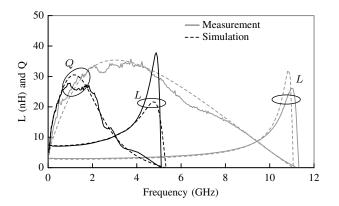


Fig. 7. Measured Q-factor and inductance value of both 3-D solenoid (grey curves) and planar spiral inductor (black curves) compared with simulation results carried out when an RF probe model is used.

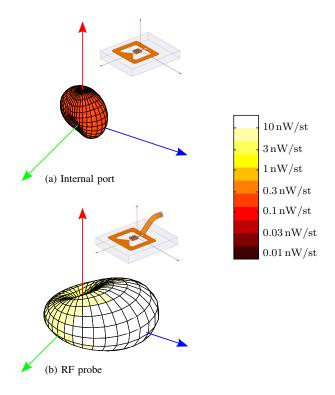


Fig. 8. Radiation intensity at 5 GHz of the solenoid inductor expressed in nWatt/steradian. The excitation port of the DUT is successively (a) internal and (b) external through an RF probe.

The coaxial waveguide and probe tips are deembedded from simulations as a comparison with measurements is performed.

Measured and newly simulated L and Q values are compared in Fig. 7 for both solenoid and planar inductors. As it can be noticed, agreements are now quite good for both devices. Then, this new EM environment can be used to investigate the interaction between the DUT and the RF probe.

3-D radiation intensity patterns are calculated at 5 GHz for the solenoid inductor, when the device is excited either with an internal port [Fig. 8(a)], and also with an RF probe [Fig. 8(b)]. It can be noticed that the radiated power intensity is more than one order of magnitude higher in all directions when the RF probe is included within simulation. As a conclusion, it can be stated that the degradation observed on the experimental Q-factor results from excessive radiations occurring in the GHz range. The 3-D radiation intensity from [Fig. 8(b)] is quite similar to what can be observed for a dipole antenna placed above a reflector plane. The two branches of the dipole would be the DUT structure including the surrounding metal ring and the external conductor of the feeding coaxial waveguide beyond the absorber shielding, whereas the chuck forms the reflector.

All on-wafer characterizations we have done on several planar and 3-D inductors ranging from 1 to 10 nH show that this effect is noticeable when Q-factor values exceed the range 25 to 35 and when frequencies are above 1 or 2 GHz. It is masked when characterizing high-turn inductors because of limited SRF values and/or proximity effects which lower Qfactors. For smaller inductors, this radiation effect should be still present above 20 GHz, but it may be hidden by increasing skin effect losses. The radiated power depends on the inductor topology, as well, as on dimensions and technology of RF probes. The geometry of the inductor's surrounding metal ring also affects radiation losses as we will see in Section V.

IV. QUANTITATIVE EVALUATION OF THE LOSSES OF AN INDUCTOR

A. Extraction of the Powers from Simulated E and H Fields

Radiated and accepted input powers, P_{rad} and P_{acc} respectively, can be computed from EM simulation [8]. Thanks to the energy conservation law, the power dissipated by the inductor (P_{diss}) into the volume is deduced from P_{rad} and P_{acc} using the following relationship

$$P_{acc} = P_{diss} + P_{rad} \tag{3}$$

For the 3-D solenoid, values of these powers are reported in Table I at a frequency of 5 GHz and for both internal and RF probing EM excitations. It can be noticed that P_{diss} remains unchanged in both cases whereas radiated power P_{rad} is over 20 times higher when the RF probe is included into simulation. P_{acc} increases only by the excess amount of P_{rad} . Data of Table I confirm that excess losses observed at 5 GHz when the RF probe is present are mainly resulting from radiations.

This analysis demonstrates that when the power dissipated into the inductor is low, as it is for high-Q inductor, the radiated power can be no longer negligible. In this case, it can become quite difficult to deduce the actual performance of the inductor from on-wafer measurements. As a consequence, special care must be brought in designing the characterization fixture. This design requires an accurate modeling of the whole structure formed when the inductor is under test. This modeling is developed in the following paragraphs.

B. Extraction of the Stored Energy from E and H Fields

Stored electric and magnetic energies, W_e and W_m , can also be computed from vectors of electric field E and magnetic field H, simulated within each volume defined within the simulation box [8]. The stored energy W_{sto} of an inductor is defined as follows [9]

$$W_{sto} = W_m - W_e \tag{4}$$

TABLE I ACCEPTED, RADIATED AND DISSIPATED POWERS OF THE 3-D SOLENOID AT 5 GHz

Inductor	Pacc	P_{rad}	P_{diss}
	(μW)	(µW)	(µW)
Simulation with internal port	340	8	332
Simulation with RF probe	512	171	341

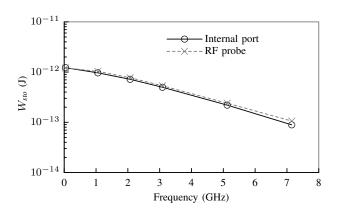


Fig. 9. Stored energy in the 3 nH Solenoid calculated from simulations carried out using an internal port and then an RF probe.

Extracted W_{sto} energy versus frequency is shown in Fig. 9, for both previous excitation ways (i.e., with an internal port or with an RF probe). Both simulations lead to the same W_{sto} values which confirm that the same inductor is simulated in both cases. This last result means that both simulations could be compared from powers P_{acc} , P_{rad} and P_{diss} . However, Q-factors are introduced for next developments as they can apply for components with different values.

C. Modeling with Q-factors

The Q-factor of a reactive device gives energy losses relatively to the stored energy. So it is a very convenient way to compare several devices. Moreover, using the common electrical formulation (1), the Q-factor can be computed from measurements and simulations, as well. Then, comparisons between simulations and measurements become possible, which cannot be easily addressed when powers are considered. However, the radiated and dissipated contributions to the overall Q-factor are mixed and cannot be separated when (1) is used. These contributions, as well as the stored energy, are then extracted from EM simulations.

The overall Q-factor of an inductor is commonly defined by the following expression [9]

$$Q = \frac{2\,\omega\,W_{sto}}{P_{acc}}\tag{5}$$

This definition is consistent with the electrical definition (1). Considering that the accepted power P_{acc} is resulting from dissipation into materials (P_{diss}) and from electromagnetic radiation (P_{rad}), as previously stated by (3), the overall Q can be written as follows

$$Q = \left(\frac{1}{Q_{diss}} + \frac{1}{Q_{rad}}\right)^{-1} \tag{6}$$

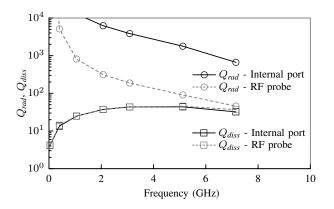


Fig. 10. Q_{rad} and Q_{diss} calculated from EM simulations carried out using an internal port (solid lines) and with an RF probe (dashed lines).

where the Q-factors Q_{diss} and Q_{rad} are defined relatively to each contribution using (7) and (8), respectively.

$$Q_{diss} = \frac{2\,\omega\,W_{sto}}{P_{diss}}\tag{7}$$

$$Q_{rad} = \frac{2\,\omega\,W_{sto}}{P_{rad}}\tag{8}$$

It must be noticed that Q_{rad} value is small when a large radiated power is observed, whereas Q_{rad} becomes very large when radiations losses cancel out (Q becomes close to Q_{diss}).

D. Extraction of Q_{rad} and Q_{diss} for the 3-D Solenoid

Quality factors Q_{rad} and Q_{diss} , as well as the overall Q, are plotted in Fig. 10 and Fig. 11, respectively, for both excitation ways. These graphs show that the overall Q-factor of Fig. 11 results mainly from dissipated losses (Q_{diss}) when the inductor is simulated using an internal port: Q_{rad} remains very high and does not affect the overall quality factor Q. Nevertheless, Q_{rad} is decreasing largely as the frequency is increasing when the RF probe is taken into consideration. So, from a certain frequency, its influence becomes noticeable on the overall Qfactor, which is well decreased. At 5 GHz, this value decreases from 42 to 30 when the RF probe is included, which is consistent with the electrical Q-factor calculated in Fig. 4 and Fig. 7 respectively at the same frequency. In addition, it must be noticed that Q_{diss} remains unchanged in both cases. Finally, in Fig. 11, the good agreement between calculations from (1)and calculations from (6) confirms that all powers are correctly extracted within simulations.

V. ACCURATE ON-WAFER MEASUREMENTS

The correction of this parasitic probe / DUT coupling effect is not trivial since the radiation intensity is very dependent on the DUT geometry. As a consequence, this effect cannot be simply removed by using any dedicated deembedding structure. The best way is then to avoid these radiations. As pointed out in section III, parasitic radiations result from the difference in potential between the wide metal ring and the external conductor of the feeding coaxial waveguide. The floating metal plate of the probe station chuck acts as a

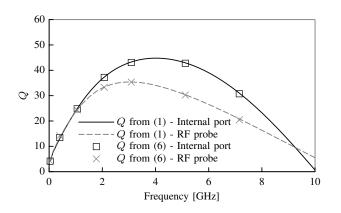


Fig. 11. Q calculated from (1) for both excitation ways (solid and dashed lines) and from (6) (marks).

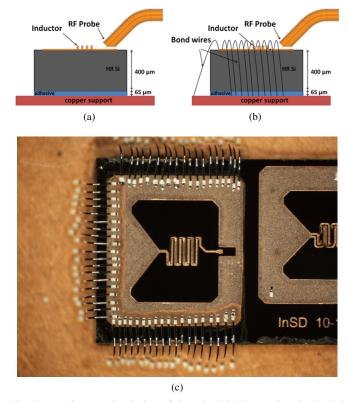


Fig. 12. (a) Cross-sectional view of the solenoid inductor, (b) wire-bonded to a copper back-plate and (c) the microphotograph of the experiment.

reflector plane for the parasitic dipole antenna. One solution is to short the metal ring of the DUT to the metal plate of the probe station. The experiment is shown in Fig. 12. The DUT is diced and sealed on a large copper plate. The inductor's ground ring is wire-bonded to this metal plate on three of the four sides of the test structure. The DUT placed in this new setup is characterized before and after the wire-bonding.

All measurement results are plotted in Fig. 13. As it can be noticed, the maximum measured Q-factor increases from 35 to 45 and a very good agreement with EM simulations is obtained when wire-bonds connect both metal planes. Moreover, initial EM simulations from Fig. 4, carried out using an internal port, have been added to Fig. 13 and a very good agreement can

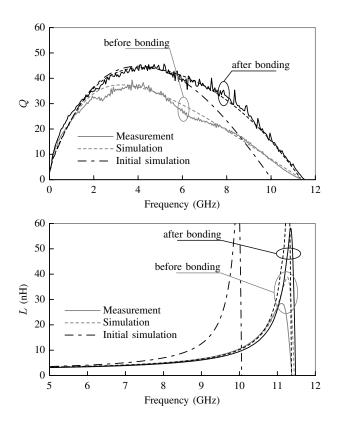


Fig. 13. Measured and simulated Q-factor and inductance value of the solenoid inductor.

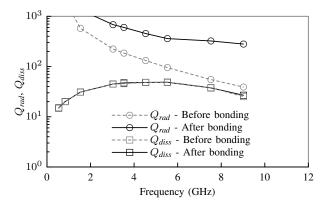


Fig. 14. Q_{rad} and Q_{diss} calculated from the new setup before shorting the inductor metal ring (dashed lines) and after (solid lines).

be observed once again, up to 6 GHz. Beyond 6 GHz, this last agreement becomes worse. The thin layer of adhesive added to stick the inductor on the underlying copper backplate alters the Q-factor and the SRF. This thin dielectric layer increases the distance between the inductor and the backplane and lowers the parasitic capacitor to the ground. Radiation and dissipation Q-factors are extracted from EM simulations before and after bonding (Fig. 14 and Fig. 15). The reduction in radiation losses is confirmed when this new setup is used since Q_{rad} greatly increases when the metal ring is grounded while Q_{diss} remains unchanged in both cases.

The above solution may not be suitable for characterizing large series of high-Q inductors. Another way to reduce

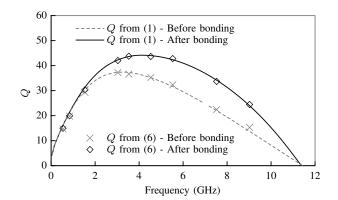


Fig. 15. Q calculated from (1) and (6).

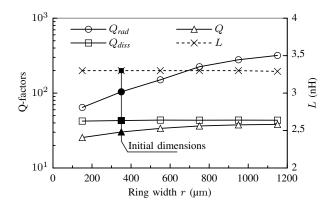


Fig. 16. Effect of the metal ring width r on Q_{rad} , Q_{diss} , Q and L.

radiation losses must be investigated, which can be directly implemented on wafer. Simulation and calculation techniques introduced in sections III and IV can be used to improve the design of the test structure of the inductor regarding radiation. before fabrication. For this purpose, and as an example, effects on radiation losses of the metal ring dimensions are presented in the case of the 3-D solenoid. The width r of the metal ring and its distance d to the inductor (Fig. 6) have been tuned. Values of Q_{rad} , Q_{diss} , Q and L at 5 GHz are plotted versus r and d in Fig. 16 and Fig. 17, respectively. Fig. 16 shows that radiation losses decrease significantly when r is increased since Q_{rad} widely improves without noticeable change on Q_{diss} and L. From Fig. 17, Q_{rad} and Q benefit from the reduction of d which improves the confinement of the electromagnetic field into the device. However, such containment now slightly reduces the inductor value L. As a conclusion, the designer can set d and r that reach the best trade-off.

VI. CONCLUSION

A parasitic radiative interaction is identified between the RF probe and the on-wafer measured structure. It is observed on planar and 3-D on-chip inductors fabricated on high-resistivity or insulating substrates and when Q is exceeding a value of 25. Radiation losses occurring on these devices are leading to experimental Q-factors well under simulated values. However, this effect is reproduced and quite accurately quantified using EM simulations when an RF probe model is included for

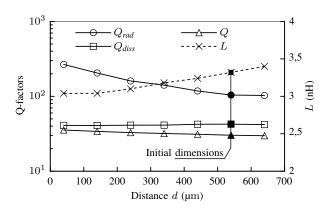


Fig. 17. Effect of the distance d between the metal ring and the inductor on Q_{rad} , Q_{diss} , Q and L.

the test signal feeding of the high-Q structure. To cancel these radiations, a new experiment is designed by shorting the metal ring of the DUT to the underneath metal plate. The measurements performed on this new test structure confirm the large reduction in radiation losses and the measured Q-factor now agrees very well with the performance initially predicted by EM simulations. We also demonstrate that most of these radiations may be avoided by properly setting dimensions of the metal ring surrounding the inductor.

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REFERENCES

- W. Kuhn and A. Boutz, "Measuring and Reporting High Quality Factors of Inductors Using Vector Network Analyzers," *IEEE Trans. Microw. Theory Techn.*, vol. 58, no. 4, pp. 1046–1055, Apr. 2010.
- [2] J. C. Rautio and R. Groves, "A potentially significant on-wafer high-frequency measurement calibration error," *IEEE Microwave Magazine*, vol. 6, no. 4, pp. 94–100, Apr. 2005.
- [3] A. M. E. Safwat, "Study of microstrip mode in RF on-wafer probes," *Microwave and Optical Technology Letters*, vol. 45, no. 4, pp. 324–328, Apr. 2005.
- [4] B. Rejaei, A. Akhnoukh, M. Spirito, and L. Hayden, "Effect of a Local Ground and Probe Radiation on the Microwave Characterization of Integrated Inductors," *IEEE Trans. Microw. Theory Techn.*, vol. 55, no. 10, pp. 2240–2247, Oct. 2007.
- [5] C. Andrei, D. Gloria, F. Danneville, P. Scheer, and G. Dambrine, "Coupling on-wafer measurement errors and their impact on calibration and deembedding up to 110 GHz for CMOS millimeter wave characterizations," in *IEEE International Conference on Microelectronic Test Structures*, *ICMTS* '07, 2007, pp. 253–256.
- [6] K. Mohammadpour-Aghdam, S. Brebels, A. Enayati, R. Faraji-Dana, G. A. E. Vandenbosch, and W. DeRaedt, "RF probe influence study in millimeter-wave antenna pattern measurements," *Int. J. RF and Microwave Comp. Aid. Eng.*, vol. 21, no. 4, pp. 413–420, Jul. 2011.
- [7] A. Ghannam, D. Bourrier, L. Ourak, C. Viallon, and T. Parra, "3-D Multilayer Copper Interconnects for High-Performance Monolithic Devices and Passives," *IEEE Trans. Compon., Packag., Manuf. Technol.*, vol. 3, no. 6, pp. 935–942, Jun. 2013.
- [8] D. M. Pozar, *Microwave Engineering*, 4th ed. New York: John Wiley & Sons, 2011.
- [9] K. O, "Estimation methods for quality factors of inductors fabricated in silicon integrated circuit process technologies," *IEEE J. Solid-State Circuits*, vol. 33, no. 8, pp. 1249–1252, Aug. 1998.



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