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Electronic properties of embedded graphene: doped amorphous silicon/CVD graphene heterostructures

Hakim Arezki¹, Mohamed Boutchich¹, David Alamarguy¹, Ali Madouri², José Alvarez¹, Pere Roca i Cabarrocas³, Jean-Paul Kleider¹, Fei Yao⁴, and Young Hee Lee^{4,5}

¹ GeePs, CNRS UMR8507, Centralesupelec, Univ Paris-Sud, Sorbonne Universités-UPMC Univ Paris 06, 11 rue Joliot-Curie, Plateau de Moulon, 91192 Gif-sur-Yvette Cedex, France.

²LPN-CNRS, Route de Nozay, 91460 Marcoussis, France.

³LPICM, CNRS, Ecole Polytechnique, Université Paris-Saclay, 91128 Palaiseau, France

⁴Center for Integrated Nanostructure Physics, Institute for Basic Science, Sungkyunkwan University, Suwon 440-747, Korea

⁵Department of Energy Science and Department of Physics, Sungkyunkwan University, Suwon 440-747, Korea.

Email: mohamed.boutchich@upmc.fr

Large-area graphene film is of great interest for a wide spectrum of electronic applications, such as field effect devices, displays, and solar cells, among many others. Here, we fabricated heterostructures composed of graphene (Gr) grown by chemical vapor deposition (CVD) on copper substrate and transferred to SiO₂/Si substrates, capped by n or p-type doped amorphous silicon (a-Si:H) deposited by plasma-enhanced chemical vapor deposition. Using Raman scattering we show that despite the mechanical strain induced by the a-Si:H deposition, the structural integrity of the graphene is preserved. Moreover, Hall effect measurements directly on the embedded graphene show that the electronic properties of CVD graphene can be modulated according to the doping type of the a-Si:H as well as its phase *i.e.* amorphous or nanocrystalline. The sheet resistance varies from 360 Ω/sq to 1260 Ω/sq for the (p)-a-Si:H/Gr (n)-a-Si:H/Gr, respectively. [We observed a temperature independent hole mobility of up to 1400 cm²/Vs indicating that charge impurity is the principal mechanism limiting the transport in this heterostructure.](#) We have demonstrated that embedding CVD graphene under a-Si:H is a viable route for large scale graphene based solar cells or displays applications.

Introduction

The recent advances in large-scale synthesis of graphene and alternative 2D materials [1–3] offer new opportunities to investigate various heterostructures composing the building blocks of new devices and architectures [4,5]. However, the electronic properties of graphene differ according to the growth technology utilized [6], the grain size, the interaction with the underlying substrate as well as the non-intended doping that systematically occurs during fabrication [7]. In addition, graphene and 2D materials are extremely sensitive to environment, implying uncontrollable variations such as the threshold voltage of 2D based field effect devices [8,9]. On the other hand, the modulation of the electronic properties of graphene is a key prerequisite to envision its integration as an active

material but also as an electrode [10]. In that regard, a lot of work has been devoted to the study of more or less stable organic or inorganic materials deposited onto graphene [11–13], [14], [15], [16]. Field effect doping and dopant incorporation at these interfaces allow the modulation of the work function, the carrier density and sheet resistance as well as the mobility [17–21], [22], [23]. Kim *et al.* [21] demonstrated GaN-based light-emitting diodes where the conductivity of graphene was substantially improved when capped with silicon nitride (SiN_x) but did not discuss the electrostatic impact of the fixed charges trapped in the SiN_x. Gluba *et al.* [24] studied CVD graphene capped with a-Si:H and polycrystalline silicon. Coupling Raman spectroscopy and Hall effect measurements, they observed that the structural features of graphene were preserved and the mobility is limited at room temperature due to phonon scattering [25–27], [28], [29]. They concluded that large-scale CVD graphene applications might be hindered due to this latter effect. Recently, Ho *et al.* [30] demonstrated a graphene/TiO_x photoactive heterostructure cathode to fabricate an (n) graphene/(p) Si Schottky junction with power conversion efficiency > 10%. These recent advances [31] shine light on the integration of graphene in solar cell applications that are already competitive with respect to organic based demonstrators but also on field effect devices where graphene can be implemented as a channel or a floating gate [3,32].

Here, we investigate graphene/doped amorphous silicon heterostructures. The chemical elements present on the graphene were determined by X-ray spectroscopy (XPS) and the work function was measured by ultraviolet photoemission spectroscopy (UPS). The homogeneity of the transferred graphene layers was confirmed by Raman spectroscopy. The latter also showed that the mechanical strain hinders the field effect doping resulting from the fixed charges in the capping a-Si:H. Hall effect measurements as a function of temperature show that the doping nature of the a-Si:H influences the electronic properties such as the sheet resistance that drops down to 430 Ω/sq for the (p) a-Si:H/Gr for a hole mobility of 1400 cm²/Vs. Moreover, our measurements do not present any temperature dependence, that is in a sharp contrast with the work of Gluba *et al.* [24] on undoped a-Si:H. At last we have shown that the recrystallization of the doped a-Si:H film relaxes the mechanical strain [33], [34] and only the field effect doping remains. We have demonstrated that embedding CVD graphene under a-Si:H is a route for large scale graphene based solar cells or display applications.

Heterostructures and experiments

Graphene layers were grown on commercially available 100 μm thick copper foil (from Nilaco, 99.96%) annealed at 1075 °C with 1000 sccm Ar and 500 sccm H₂ for 2h by CVD at atmospheric pressure. The annealed copper foil was polished using the chemical mechanical polishing method (6 ml copper etchant was diluted in 100ml water). The polished Cu film was placed in a 2 inch quartz tube chamber and heated up to 1075 °C with 1000 sccm Ar and 500 sccm H₂ for annealing for 2h to remove residuals on the copper surface [35,36]. During the atmospheric CVD growth, the H₂ gas was reduced to 170 sccm and 200 sccm of CH₄ (0.1% diluted in Ar was injected) for 25 min. After growth, the CH₄ was turned off and the chamber was cooled down to room temperature. The copper foil was subsequently etched in FeCl₃ etchant and the graphene was transferred onto SiO₂/Si substrate using PMMA resist. To fabricate the heterostructure, p-type and n-type a-Si:H films were deposited by standard plasma-enhanced chemical vapor deposition at 175 °C using a multiplasma-monochamber reactor [37], from the dissociation of silane

mixed with trimethyl-boron or phosphine. The oxygen content of the films, as deduced from secondary ion mass spectrometry measurements is $\sim 2 \times 10^{19} \text{ cm}^{-3}$ resulting from the residual impurities in the reactor [38].

We have studied both configurations : i) the graphene on top of a-Si:H and graphene coated by the a-Si:H thin films which have the advantage of providing a encapsulating layer making the measurements less sensitive to the atmosphere conditions.

We have investigated the electronic properties of graphene based heterostructures: (i) graphene on top of doped a-Si:H on silicon oxide and (ii) graphene on SiO_2 capped with 10 nm doped a-Si:H, Figure 1. The Raman measurements were performed with a frequency-doubled NdYag laser operating at 532 nm as the excitation source with up to 20 mW continuous wave output power. High laser power can induce crystallization of the a-Si:H, so the laser power was judiciously tuned to investigate the Raman signature before and after crystallization of the a-Si:H. XPS and UPS measurements for the study of the chemical composition and the work function (WF), respectively, have been performed using a PHI 5000 Versaprobe spectrometer (Physical Electronics) operating at a base pressure of 10^{-9} mbar. For the XPS analysis, a focused monochromatic Al K_{α} X-ray source ($h\nu = 1486.6 \text{ eV}$) was used. The work function was determined from the secondary electron cut-off in UPS experiments using a He I discharge lamp ($h\nu = 21.2 \text{ eV}$). The position of the Fermi level was calibrated by measuring the Fermi edge of a sputtered gold sample. During the WF measurements, a bias of -8 V was applied to the sample. Pieces of the doped a-Si:H/Gr heterostructures have been patterned in Hall bars using optical lithography to investigate the transport properties of the encapsulated graphene, Figure 1. These measurements were performed in an ambient environment.

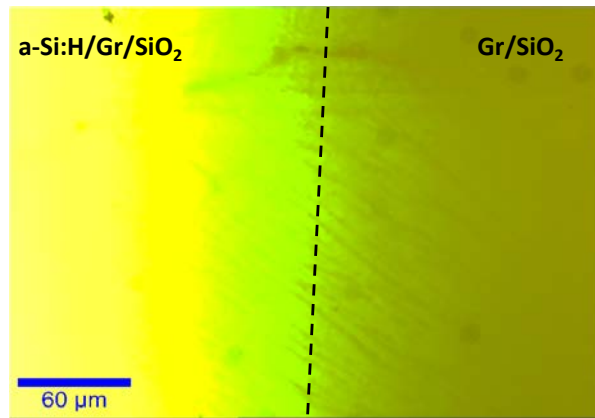


Figure 1: Optical image of the graphene sample utilized in the experiments. The yellow region corresponds to the graphene capped with a-Si:H whereas the green region corresponds to graphene on SiO_2 . The light green region corresponds to a thinner thickness of a-Si:H that diffused under the hard mask during the deposition.

Results and discussion

Let us first address the case where the graphene films are deposited on top of the a-Si:H layers. Figure 2 presents the Raman spectra of CVD graphene transferred onto 10 nm n- and p-type a-Si:H. All samples show a peak at 480 cm^{-1} , characteristic of a-Si:H, as well as the G and 2D peaks of graphene. However, the G peak shift is 7 cm^{-1} and the 2D peak shift is 3 cm^{-1} on the p-type a-Si:H with respect to the n-type a-Si:H. This indicates that the graphene on p-type

a-Si:H has been slightly n-type doped. This doping may result from contamination subsequent to the transfer process or possibly from a charge transfer at the heterostructure interface, [Figure 2](#).

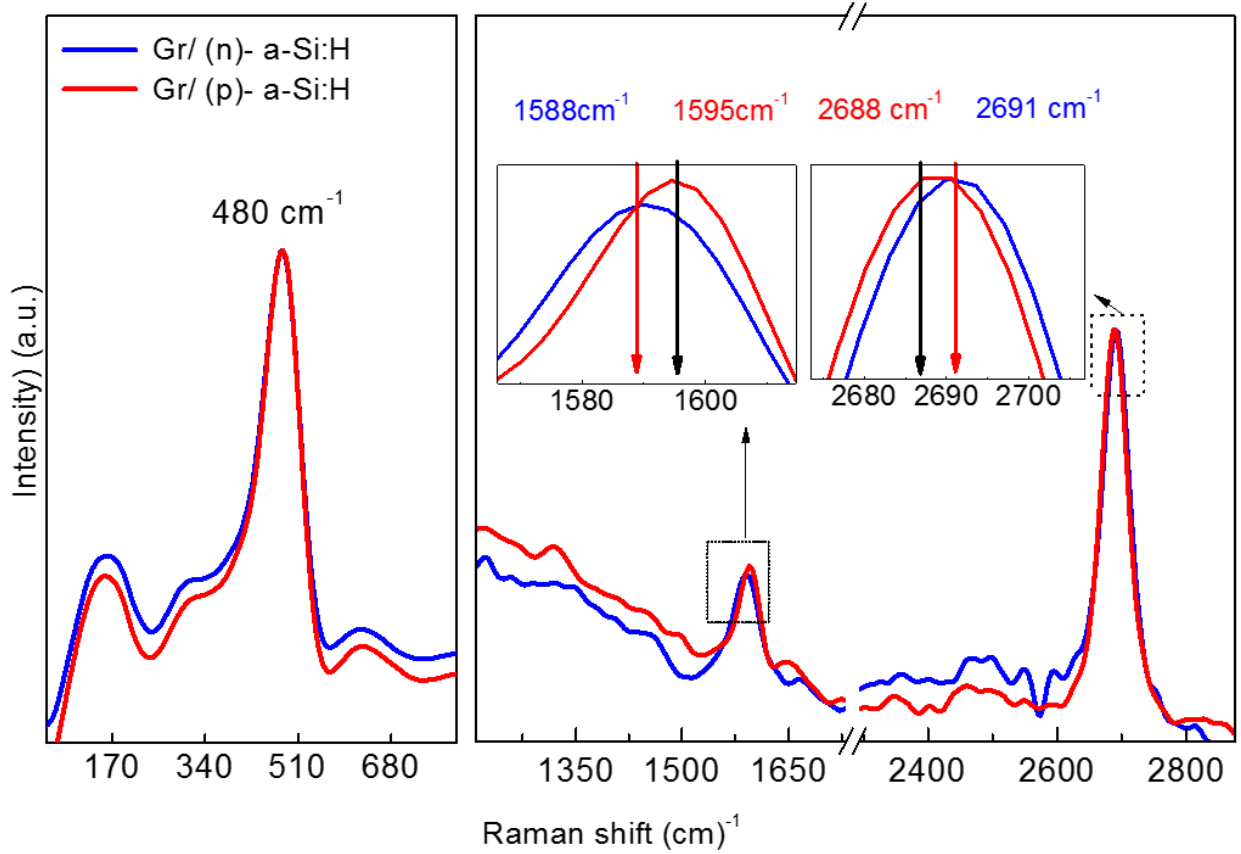


Figure 2: Raman spectra of CVD graphene transferred onto n- and p-type a-Si:H.

Indeed, XPS measurements (not shown) showed the presence of oxygen (O), fluorine (F), nitrogen (N) and chlorine (Cl) species on graphene. Oxygen is known as a p-type dopant whereas F and N are n-type dopants [39]. The concentrations of these species vary from sample to sample and they are not controllable. We have carried out transport measurements on graphene on top of the doped a-Si:H.

Heterostructures	R_{sh} (Ω/sq)	n_s (cm^{-2})	μ (cm^2/Vs)
Gr/SiO ₂	450	1×10^{13}	1400
Gr/(n)-a-Si:H/SiO ₂	420	8×10^{12}	1800
Gr/(p)-a-Si:H/SiO ₂	780	4×10^{12}	2000

Table 1: Transport properties for graphene transferred onto amorphous silicon/silicon oxide stacks. These data represent average between 1.3 K and 300 K.

[Table 1](#) shows the temperature dependence of the Hall-effect mobility, charge carrier density, and sheet resistance of transferred CVD graphene (Gr) on top of SiO₂, as well as on (p)-a-Si:H/ SiO₂ and (n)-a-Si:H/SiO₂ stacks. We do not observe any dominant scattering mechanism and the as-transferred layers exhibit hole conduction with electrical properties that are nearly temperature independent. Note that the sheet resistance of graphene on both types of a-Si:H films presents values that become interesting for transparent electrode application without any additional

doping process. UPS measurements confirm that the heterostructures are electronically different. WF is equal to 4.35 eV for the Gr/(p)-a-Si:H heterostructure and 4.15 eV for the Gr/(n)-a-Si:H one. However, it is questionable to assign this 0.2 eV difference to a charge transfer at the interface of the heterostructure. The carrier density is doubled between Gr/(p)-a-Si:H and Gr/(n)-a-Si:H while the mobility decreases from 2100 to 1800 cm²/Vs. It is likely that this change is a consequence of the unintended doping during the transfer and fabrication processes.

In order to elucidate whether there could be charge transfer between a-Si:H and graphene, we have studied capped graphene *i.e.* (n)-a-Si:H/Gr and (p)-a-Si:H/Gr. Here, a 10 nm thick doped a-Si:H layer has been deposited onto Gr and Hall bars have been patterned. The graphene has been contacted first and the a-Si:H film has been etched to reach the contact pads. Figure 3 shows the Raman spectra of the different heterostructures benchmarked to the graphene onto SiO₂. The measurements are carried out directly on different locations of the Hall bar device to take into account the homogeneity of the sample.

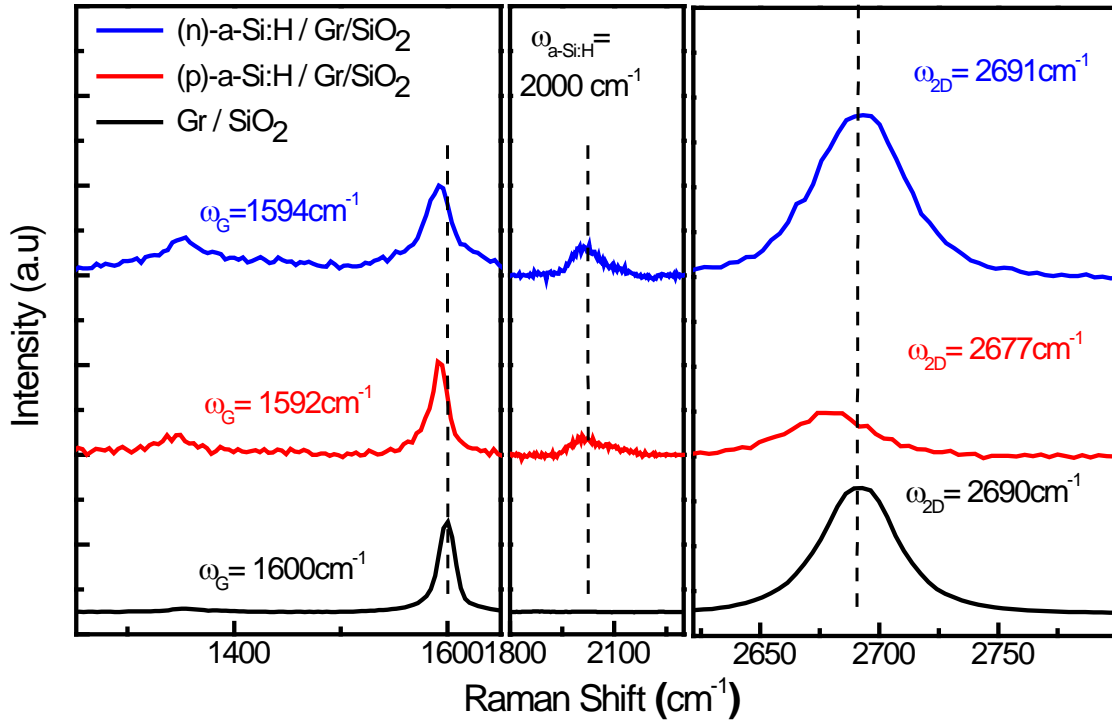


Figure 3: Raman spectra of the graphene on SiO₂ substrate capped with doped a-Si:H. For clarity, the spectra are shifted vertically.

Interestingly, the D, G and 2D Raman modes of the graphene are preserved after deposition of a-Si:H indicating that the capping process is nondestructive although the low intensity of the D mode is slightly higher for the (n)-a-Si:H/Gr compared to the (p)-a-Si:H/Gr heterostructure. However compared to the Gr/SiO₂ sample, the G and 2D modes of both a-Si:H based heterostructures exhibit a shift to lower frequencies, Figure 3. Moreover, for the (p)-a-Si:H/Gr heterostructure, the 2D/G ratio decreases. This trend could result from mechanical strain induced by the boron or phosphorus doped a-Si:H layers [40,41], or from field effect doping from the charges at the interface. Although field effect doping cannot be excluded, it is clearly hindered by the mechanical strain that is the dominant

mechanism as can be seen by the downshift observed on both the G and 2D modes for the two types of heterostructures [42,43].

We carried out Hall effect measurements as a function of temperature to evaluate the electronic properties of the capped graphene. Figure 4 shows the sheet resistance, mobility and carrier density of the embedded graphene benchmarked to graphene on SiO₂.

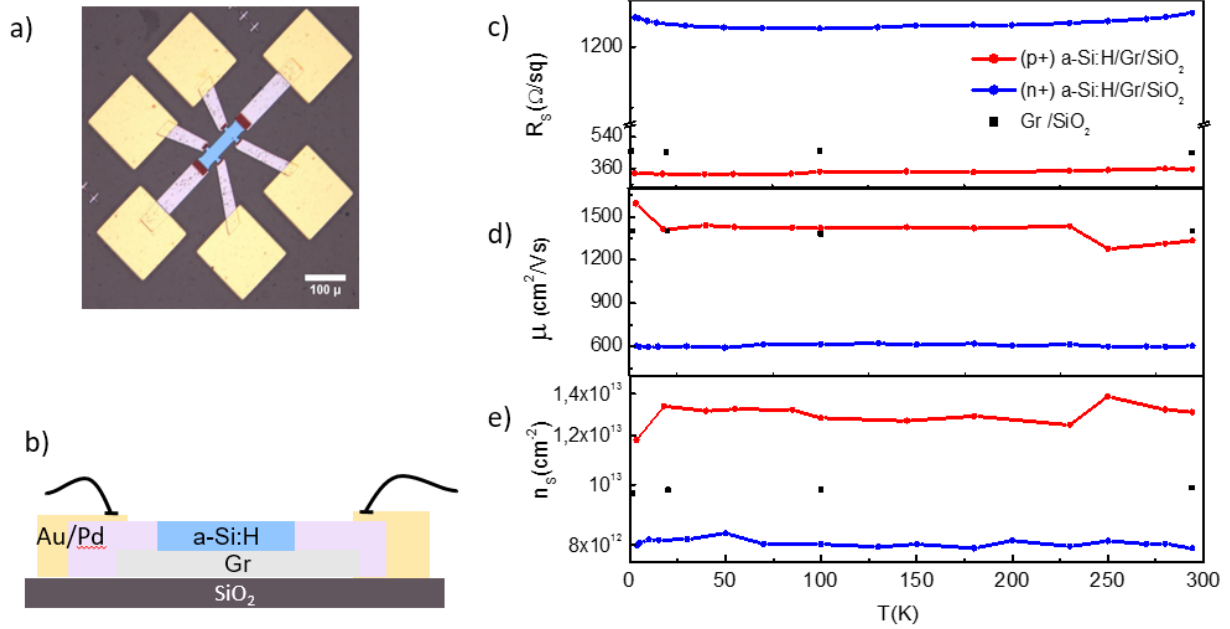


Figure 4: Graphene Hall bars capped with a-Si:H (a) optical image (30x70 μm), (b) schematic of the Hall bars, temperature dependence of (c) sheet resistance, (d) Hall mobility and (e) charge carrier density of graphene capped with n or p-type a-Si:H. The dashed line represents data on SiO₂.

Here both capped heterostructures *i.e.* (n,p) a-Si:H/Gr, present hole conduction with an average mobility of 1400 cm²/Vs and a sheet resistance as low as 420 Ω/sq for (p)-a-Si:H/Gr. For the latter the carrier density has increased to 1.3x10¹³ cm⁻² compared to the 9.8x10¹² cm⁻² measured on SiO₂ that is known to present hole accumulation. On the other hand, (n)-a-Si:H/Gr presents a lower mobility of 600 cm²/Vs with a sheet resistance of 1300 Ω/sq and a carrier density of 8x10¹² cm⁻². The lower mobility observed for the (n)-a-Si:H/Gr may be a result of structural defects induced by the mechanical strain. Clearly, the doping type of the as-deposited a-Si:H influences the electronic properties through field effect doping induced by the fixed charges at the interfaces notably for the p-type sample. This doping has already been suggested by the Raman spectra of Figure 3; although the shift induced by the doping is hindered by the mechanical strain, the 2D mode intensity of the Gr/(p)-a-Si:H reduces and the full width half maximum increases. Using the measured carrier density we can calculate the position of the Fermi level E_F relative to the Dirac point E_D : $E_D - E_F = -0.33$ eV for (n)-a-Si:H/Gr and $E_D - E_F = 0.65$ eV for (p)-a-Si:H/Gr. Note that we do not observe any temperature dependence that is in contrast with the work on graphene capped with undoped a-Si:H by Gluba *et al.* that observed a strong temperature dependence of capped graphene assigned to phonon assisted

scattering starting at 100 K [24]. In our configuration, the graphene sheet is embedded between SiO₂ substrate and doped a-Si:H. These two materials are both impurity rich and therefore inherently hinder the electronic transport. It is therefore reasonable to consider the charge impurity scattering as the dominant mechanism. Nevertheless, the temperature dependence of the phonon scattering often observed on graphene on SiO₂ is not absent but rather postponed beyond 300 K as discussed by Chen *et al.* for impurity rich samples by [26,44]. Nevertheless, despite this limitation, the magnitude of the mobility observed in the capped graphene remains very interesting for the large-scale application of graphene. As shown in Figure 4, the graphene films capped with p-type a-Si:H have a higher mobility and carrier concentration, leading to a square resistance of 420 Ohm/square, which makes them suitable as an electrode for heterojunction solar cells and/or gate contact.

At last we have investigated the crystallization of a-Si:H on the Raman spectra using the 532 nm laser. The a-Si:H crystallizes at a power of 2.15 mW. Figure 5 presents the Raman spectra for both heterostructures before and after crystallization. We observe that the a-Si:H peaks at 480 cm⁻¹ and 2000 cm⁻¹ disappear whereas the c-Si peak appears around 520 cm⁻¹.

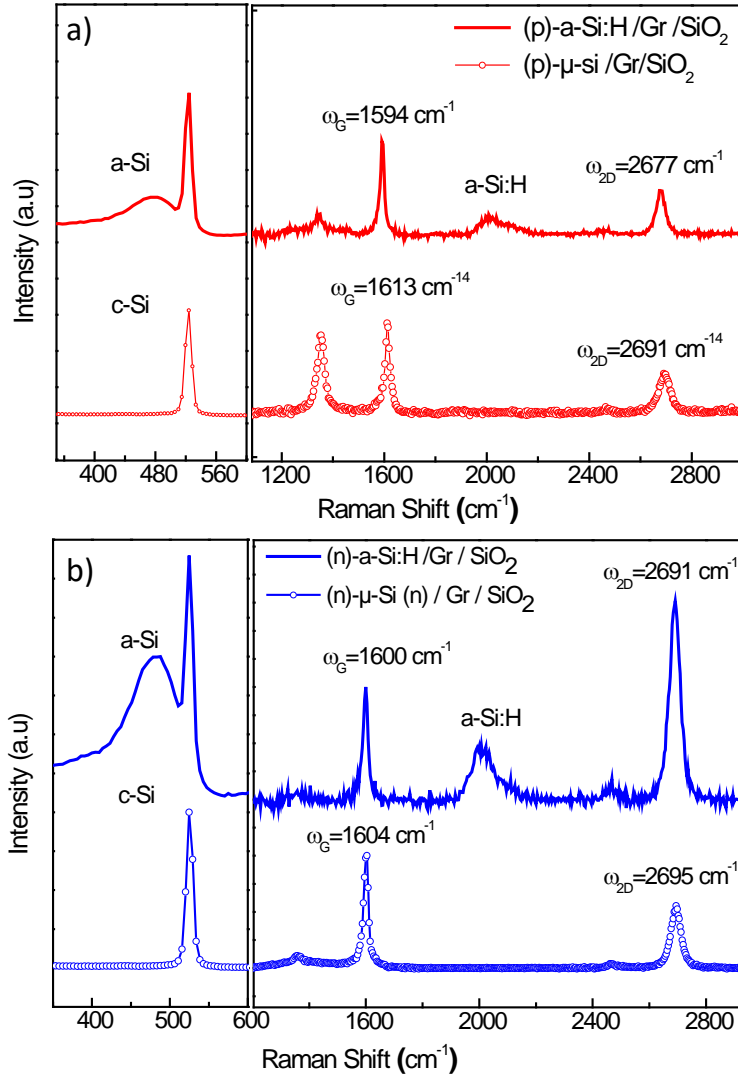


Figure 5: Raman spectra before and after crystallization of a) (p)-a-Si:H/Gr heterostructure and b) (n)-a-Si:H/Gr heterostructure. The as-deposited spectra present a shoulder at 480 cm^{-1} which is absent on the recrystallized spectra characterized by a single peak at $\sim 520 \text{ cm}^{-1}$.

The G and 2D bands in the (p)-a-Si:H/Gr heterostructure exhibit an upshift of 19 and 14 cm^{-1} respectively. The (n)-a-Si:H/Gr heterostructure, also presents upshifts for both G and 2D bands but of lower magnitude, 4 cm^{-1} for both G and 2D modes. This trend indicates two mechanisms: (i) the release of the compressive strain, (ii) a p-type doping. The increase in the D band intensity implies an increase in the defect density assigned to the volume change of the Si thin film during crystallization. It is particularly pronounced for the (p)-a-Si:H/Gr heterostructure. Moreover, the 2D/G ratio also reduces both samples suggesting an increase in carrier density *i.e.* doping originating from the Gr/SiO₂ and/or a-Si:H/Gr interfaces. The strength is nevertheless stronger for the (p)-a-Si:H/Gr heterostructure. It is known that (p)-a-Si:H conductivity increases after crystallization[45] and therefore more charges are available for transferring to the graphene. These data show that embedded CVD graphene maintains its features and the transport properties can be modulated according to the nature and the phase of the deposited a-Si:H.

Conclusion

In this work, we have investigated the capping of CVD graphene film by n- and p-type doped a-Si:H. The deposition process of the a-Si:H has a limited impact on the structural integrity of graphene as observed by Raman spectroscopy and the graphene features are clearly visible on all heterostructures. However the electronic properties are affected whether graphene is capped by n- or p-type a-Si:H. Post deposition Raman shifts indicate that the mechanical strain is dominating however the reduction of the I_{2D}/I_G ratio particularly for the (p)-a-Si:H/Gr heterostructure suggests a field effect doping as well. This observation has been evidenced by Hall effect measurements showing a substantial reduction of the sheet resistance. *The transport is limited by the charge impurities from the SiO₂ and the doped a-Si:H, the hole mobility reaches up to 1400 cm²/Vs without temperature dependence suggesting that phonon scattering is not dominant up to 300 K but would appear at higher temperature.* At last we have shown that the recrystallization of the doped a-Si:H film relaxes the mechanical strain and only the field effect doping remains. We have demonstrated that embedding CVD graphene under a-Si:H is a route for large scale graphene based solar cells or displays applications.

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