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Silicon germanium on graded buffer as a new platform for optical interconnects on silicon

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ABSTRACT

We experimentally and theoretically investigate the use of silicon germanium (SiGe) on silicon substrate as a new platform for optical interconnects. The system composed of Germanium (Ge) rich $Si_{1-x}Ge_x$ guiding layer on a graded SiGe layer is showed to be suitable for the realization of all main building blocks of passive optical circuitry. We show experimentally at a wavelength of 1550nm that sharp $12\mu m$ radius bends can be obtained by light confinement tuning. Mach-Zehnder interferometer with more than 10 dB extinction ratio is also demonstrated. Moreover, Ge-rich $Si_{1-x}Ge_x$ based passive components are very interesting for their native integration with Ge-rich active optical devices. Hence, by using this new platform for optical integrated circuits, lattice mismatch between silicon and germanium is no longer a major constraint for the integration of Ge-rich active photonic components on silicon.

Keywords: Silicon photonics, Germanium, Waveguide, Bends, Mach-Zehnder interferometer

I. INTRODUCTION

Continuously increasing data consumption pushes electronic integrated circuits to their limits. To tackle high power consumption issue and bandwidth limitations, optical interconnects may possibly be the most viable solution. In this context, photonic platform material must be compatible with mature silicon (Si) based technology. Hence, Germanium is identified as a promising candidate for Si compatible photonic platform. Germanium, for its pseudo-direct band gap properties was widely studied for the realization of laser sources¹⁻². Additionally, Germanium based modulators³⁻⁶ and photodetectors⁷ are remarkable for their speed and power consumption. However, the integration of these devices with passive circuitry is challenging. As Si and Ge have 4% lattice mismatch, straightforward integration of high quality active devices is difficult due to high dislocation rate. An efficient strategy to accommodate Si and Ge lattice parameters is the use of a graded buffer layer. We recently proposed that such graded buffer layer can be used to confine light in a Ge-rich SiGe virtual substrate and obtain SiGe waveguide on bulk silicon. Such approach makes high Ge content photonic components integration direct: high quality bulk Ge or Ge/SiGe multi-quantum well structures can be grown on Si_{1-x}Ge_x (with x higher then 0.8) guiding layer.An optical link composed of a Ge/SiGe quantum well (QW) photodetector and modulator connected by Si_{0.16}Ge_{0.84} waveguide has thus been demonstrated⁸. Until now only straight waveguides were demonstrated with this approach and compact guiding structures were still a challenge due to the low index contrast in these waveguides. We experimentally show that Ge rich SiGe on graded buffer waveguides are a promising platform for optical interconnects on silicon. Sharp bends and Mach Zehnder interferometers are demonstrated. Hence Ge-rich SiGe optical circuitry paves the way for the monolithic integration of Ge based active devices on silicon.

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II. EPITAXIAL GROWTH, DESIGN AND FABRICATION

2.1 SIGE GROWTH

The structure was grown by low energy plasma enhanced chemical vapor deposition (LEPECVD). The Si_{1-y}Ge_y graded buffer is grown on a 100mm Si substrate with a deposition rate of 5-10 nm/s. Ge concentration y is linearly increased from 0 to 79% during the growth over 11µm thickness which correspond to a refractive index variation from 3.477 to 4.112 at 1550nm wavelength. Then, 2µm thick relaxed Si_{0.2}Ge_{0.8} guiding layer is grown on the top of the graded buffer. A difference of 1% in Ge concentration between the top of the graded buffer and the guiding layer does not affect the material quality and provides better vertical light confinement. A cross section of sample layers is shown in Fig.1. It is important to stress that this stack can be used for fabrication of passive circuitry and can also act as a substrate on which quantum wells with well-chosen thickness and germanium content surrounded by doped regions can be deposited by low temperature (≤450°C) epitaxial growth.



Figure 1. Schematic diagram of Ge-rich SiGe waveguide

2.2 PASSIVE DEVICES DESIGN AND FABRICATION

To obtain sharp and compact footprint 90° bends two kind of waveguides were chosen: $2\mu m$ wide and $1\mu m$ etched (Fig.2(a)) for straight portions and $2\mu m$ wide and $2.5\mu m$ etched (Fig.2(b)) for bent portions.



Figure 2. (a) Slightly etched waveguide used for straight waveguide portions. (b) Deeply etched waveguide for bent portions

Slightly etched waveguides, with their low confinement, as shown in Fig.3(a), are used for their low propagation losses. By deep etching bent waveguide sections (Fig.3(b)) we reduce radiation losses of the bends⁹. Thus sharper bends can be achieved with light confinement tuning. Using only slightly etched rib waveguides it is impossible to obtain bends with radii below $200\mu m$ with acceptable losses for telecom wavelengths.



Figure 3. (a) Slightly etched waveguide field intensity profile in TE polarization. (b) Deeply etched waveguide field intensity profile in TE polarization

Based on the growth reported on Figure 1, sharp bends and asymmetric Mach-Zehnder interferometers were fabricated. As a first step, 200nm silicon dioxide (SiO₂), was deposited on the top of the waveguide layer. Then, SiO₂ hard mask was fabricated by deep UV lithography and inductively coupled plasma etching (ICP). Rib waveguides were then defined by ICP etching (Fig2. (a)). Finally 90° bends and bent portions of Mach Zehnder interferometers were deeply etched using 200nm SiO₂ hard mask (Fig4.(a) and (b)). Silicon dioxide hard mask is used for a selfalignement of slightly and deeply etched waveguides. After each ICP etch, hydrogen peroxide solution (H₂O₂) was used to smooth sidewall roughness



Figure 4. (a) Optical microscope view of fabricated bends. (b) Zoom on a 90° bend, deeply etched region appear darker. (c) Zoom on the transition between slightly and deeply etched waveguide regions (d) propagation simulation for the transition section calculated in TE polarization with eigen mode expansion solver

A transition is needed between differently etched portions of the bend to avoid an abrupt change in the effective index. Thus, to reduce transition losses, a tapered 20μ m long transition as in Fig. 4(c) was designed. Numerical simulations were performed for the design of this transition and the results are reported in Fig. 4(d). For the side view, a slice located 1μ m below the top surface of the waveguide is considered for the calculation while a simulation is performed on the slice located in the middle of the waveguide for the side view. From simulations no radiation losses originating from the transition between the two waveguides is confirmed.

It is critical, for a photonic platform to be able to tune the phase of one wave with respect to another one. In this way, Mach-Zehnder interferometers based on SiGe waveguides were investigated to prove the validity of the chosen SiGe on graded buffer photonic platform. In this objective Multi-Mode beam splitters have been designed, based on the slightly etched waveguide reported in Fig.2 (a). Parameter optimization gave a 187 μ m long, 11 μ m wide beam splitter with a 2 μ m width input waveguide and 3.5 μ m width output waveguides separated by 2.3 μ m. In Fig. 5 (a) and (b) are shown the schematic view of the optimized beam splitter and the corresponding propagation simulation. Using this type of beam splitter, an asymmetric Mach-Zehnder was designed as shown in Fig.5(c).



Figure 5. (a) Optimized design for 1x2 MMI beam splitter. (b) Electric field's component propagation simulation. (c) asymmetric Mach Zehner interferometer general schematic view. (d) Zoom on fabricated beam splitter under optical microscope. (e) view of the deeply etched region (they appear darker) in the bent region of the Mach Zehnder interferometer

Fig 5.(d) and (e) shows a zoom under an optical microscope on the different parts of the fabricated Mach-Zehnder: MMI beam splitter and bent parts using deeply etched waveguide to separate both arms.

III.PASSIVE STRUCTURE CARACTERISATION AND DISCUSSION

3.1 BENDS CARACTERISATION

Fabricated 90° SiGe were characterized using a butt coupling technique: TE polarized light from a tunable laser was injected in the waveguide with a lensed fiber, an objective collected the output light and injected it to a photodetector. 12, 25, 50 and 100 μ m radii were studied. To obtain reliable bend loss data a set of 32 consecutive bends for each bend radius were fabricated. Measurements were performed at 1550nm which is above the indirect optical absorption bandedge for Si_{0.2}Ge_{0.8}



Figure 6. (c) Measured losses of the 90° bends as function of bend radius

Straight waveguide losses were estimated to be 1dB/mm. Propagation losses can be reduced by optimizing both surface passivation and etching process to reduce sidewall roughness. In Fig.6(c) are reported 90° bend losses as a function different bend radii. For 50 μ m bend radius, 0.5dB/90° losses were obtained. Numerical calculations show that the main contribution for the bend losses is the modal mismatch between straight and bent section of a 90° bend. 0.1dB/90° can further be obtained by using an offset technique⁹ or by continuously varying bend curvature¹⁰.

3.2 MACH ZEHNER INTERFEROMETERS

Asymmetric Mach Zehnder interferometers were characterized by the same butt coupling method. As reported in Fig.7, more than 10dB extinction ratio is obtained. The measured free spectral range is 24.4nm and it matches theoretical expectations for a $24\mu m$ arm difference.



Figure 7. Optical transmission of the fabricated asymmetric Mach-Zehnder interferometer. 24.4nm free spectral range is corresponding to the length difference of 24μ m between both arms of the Mach-Zehnder

Between the input and output 12dB losses were estimated. This includes: 8 dB losses for 8mm total length of access waveguides i.e input and output, 2dB loss in Mach-Zehnder arms and 1dB losses for each coupler. The length of access waveguides can be decreased, thus dramatically reducing their contribution to the total losses.

CONCLUSIONS

Ge-rich silicon-germanium on graded buffer offers a new possibility of integration of Ge based active and passive devices on bulk silicon substrate. The high Ge content of the proposed passive circuitry allows the growth of high quality $Si_{1-x}Ge_x$ layers for active devices. Main building blocks such as sharp and compact footprint 90° bends and Mach-Zehnder interferometers with good extinction ratio have been designed and experimentally validated. Light confinement tuning was used to develop compact passive circuitry and reduce radiation losses. Perfect alignment during the fabrication was achieved by self-aligned two etching process to use both slightly and deeply etched region. This work paves the way for the demonstration of wavelength division multiplexing devices. Their further integration with light sources, modulators, photodetectors will lead us to SiGe based photonic integrated transceivers on silicon substrate.

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