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Scheduling on unspecified heterogeneous distributed resources

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Abstract

In this paper, we present an adaptive method for scheduling parallel applications on unspecified distributed memory platforms. The presented method can be used to schedule parallel applications when the total workload and the execution parameters (communication speed, available computing power...) are unspecified. When used to schedule divisible load applications according to a master-worker model, this method delivers the workload through multiple rounds. In order to maximize the throughput of each worker, it can prevent both idleness in the use of workers and contentions in the use of the links between master and workers. Before focusing on the proposed scheduling method, the paper recalls the underlying methods on which its development relies. The paper then gives a theoretical analysis of the method before presenting results of simulations obtained with the SimGrid framework on a limited distributed memory platform.

Keywords-parallel application; multi-round divisible load scheduling; heterogeneous platform; adaptive scheduling; unspecified distributed memory platform;

I. Introduction

We consider a master-worker model and a load that can be arbitrarily divided into any number of chunks; each of them can be processed by a worker independently from the others. The problem of scheduling the chunks over the set of workers belongs to the Divisible Load Scheduling (DLS) category. Clearly, such a load distribution can be of any use only when the chunk processing duration is higher than the corresponding communication durations (between master and worker). The resources are supposed to be heterogeneous and we want to maximize the throughput of each worker (independantly from the others). Let us

suppose that the durations of communicating and processing a chunk are affine according to chunk size.

On the one hand, we suppose that the master has an unlimited buffering capability and receives a continuous input stream of data to be processed by the workers. The size of the total workload happens to be known only when the last item is acquired by the master; then the so called cleanup phase begins. But, as this size is unknown when scheduling starts, the master must proceed to an iterated distribution as the workload flows in. We call "round" a sequence of consecutive actions leading the master to feed all the workers once and collect the corresponding results. The resulting algorithm is therefore multi-round. After a worker has processed a chunk, the result of the processing is sent back to the master. On the other hand, we suppose that the platform is a distributed memory platform whose communication and computation resources are inaccurately specified. The available communication speeds, available computation speeds, latencies... that characterize these resources, and that we call execution parameters in the sequel, are supposed constant according to time. The lack of information about both the size of the total workload and the execution parameters of the platform increases the difficulty of scheduling the processing of the chunks efficiently. In this paper, we assume that all the considered workers are used, in a predefined order. Based on the estimates of the execution parameters, the considered workers could be a priori selected in order to maximize the total throughput.

We assume that computation can overlap communication. As stated later on in the paper, we consider a 1-port bi-directional communication model (as used by [1]), which allows a communication from master to worker to overlap a communication from worker to master. With the 1-port communication model, a risk of contention may appear when workers compete to access the master.

Generally, when the input stream delivers a huge work-

load, scheduling methods focus on the steady state phase, and make all workers contribute to the computation as soon as possible, through a startup phase which provides the workers with small enough chunks. For our part, the method we present in this paper adjusts the chunk sizes for the first round in order to prevent idleness and contentions. In order to minimize the duration of the cleanup phase, the workers must complete their work simultaneously (cf DLS theory). Therefore, the scheduling in the method adapts the size of the chunks so that the remaining load on the workers is not too unbalanced; otherwise it would give rise to costly communications to reallocate the chunks during the cleanup phase.

The rest of the paper is organized as follows. The next section is dedicated to related works. Section III is a reminder of the underlying methods on which the AS4DR (Adaptive Scheduling for Distributed Resources) method relies. Section IV presents the proposed method, which overcomes the main drawbacks of the previous ones, and a theoretical analysis of its behaviour. Section V studies in more detail how AS4DR can avoid idleness and contentions. Finally, we conclude and give hints on future work.

II. Related works

Although most general researches on scheduling address applications with intertask dependencies [2], [3], quite a lot of applications [4] are eligible to the Divisible Load Scheduling (DLS) model. This model has been largely studied; it is the first model that enables optimality proofs for scheduling methods in the context we have chosen [4], [5], [6], [7], [8]. It usually deals with problems for which the size of the total workload and the execution parameters are known [9], [10], [8].

Several multi-round methods have been proposed in the literature [11], [10], [9]. On the one hand the iterated distributions of multi-round methods have the advantage (when using a 1-port model) of making the workers active earlier, and on the other hand they have the drawback of increasing the amount of time wasted by latencies. Several strategies for distributing the load to workers have already been studied. First of all, some strategies fix the number of rounds arbitrarily (multi-installment methods [11]). They are well suited to a model with linear costs for homogeneous platforms. Concerning heterogeneous platforms [12], other strategies have been proposed, which are able to take account of the non-linearity [13] of costs when determining the load of the nodes for each round [14]. For instance, the workload which is delivered at each round by the UMR method [10] follows a geometric progression whose common ratio is adjusted so that all the nodes work for the same duration in each round,

and so that computation overlaps communication totally. It is proved [10] that this method minimizes the total execution time; provided that we know how to select the best set of nodes to be used. Another method [9] introduces periodicity for the rounds (without imposing any particular value for the period) and also requires that all nodes work during the whole period and that computation overlaps communication totally. It is proved [9] that this method maximizes the amount of load processed by time unit.

Unfortunately none of these methods can be used when the execution parameters are inaccurately specified. To deal with such a lack of information, some methods probe to get the missing information in order to adjust the scheduling [15], [16], [17], [18], [19]. For other methods, like RUMR method [20], the law of probability which governs the fluctuation of the value of the execution parameters is supposed to be known.

To the best of our knowledge, the only methods that can cope with the unspecification of the execution parameters and the unawareness of the total workload are the DA1 method [18] and the OLMR method [19]. Both methods make a very strong assumption: that the master can communicate with the workers without any contentions. These methods make the worker's access to the master become periodic in order to maximize the throughput of each worker. More precisely, from a contention-free situation, the DA1 and OLMR algorithms can keep up an organisation of the successive rounds, we call asymptotically periodic in the sequel, which gives each worker dedicated time frames to dialog with the master. It has been proved [18], [19] that, thanks to the multi-round approach and to the previous rounds history, DA1 and OLMR schedulings can cope with platform heterogeneity and with the lack of knowledge of execution parameters a priori. Drozdowski's results, established for a monodirectional 1-port communication model and linear costs [18], have been extended to a bidirectional 1-port communication model with affine costs [21]. Whereas DA1's scheduling experiences workers idleness systematically, the OLMR method aims to avoid it by allowing computation to overlap communication, thus improving the throughput of each worker.

However, starting with a contention-free situation is unlikely in practice. Therefore this paper reconsiders the OLMR method and presents the AS4DR scheduler which successfully avoids network contentions as well as workers idleness.

III. Reminder of the underlying methods

The underlying methods are the so called DA1 [18] and OLMR [22] methods. Let's first recall briefly the context in which these methods have been proposed and

studied. Their initial goal was to automatically adapt the scheduling of a divisible load application to the evolution of the execution parameters of the platform over time. Let $\alpha_{w,i}$ be the size of the chunk sent to a worker w for round i . Let τ and $\sigma_{w,i}$ be respectively the wanted and the estimated (measured for DA1 and computed for OLMR) time durations between the sending of a chunk of size $\alpha_{w,i}$ and the reception of the corresponding result by the master. In order to keep the communications contention-free, these methods adapt $\alpha_{w,i}$ so that $\sigma_{w,i}$ converges [18], [19] towards the same reference value τ for all the workers.

Sending subchunks of arbitrarily chosen sizes $\hat{\alpha}_{w,1}$ and $\check{\alpha}_{w,1}$ to each worker w for the first round, the OLMR scheduler then sends to worker w , for each round i , two subchunks \hat{s} and \check{s} of respective sizes $\hat{\alpha}_{w,i}$ and $\check{\alpha}_{w,i}$, such that

$$\hat{\alpha}_{w,i} + \check{\alpha}_{w,i} = \alpha_{w,i}; \quad (1)$$

where the value of $\alpha_{w,i}$ is determined by the following computation:

$$\alpha_{w,i} := \alpha_{w,i-1} \frac{\tau}{\sigma_{w,i-1}} \quad \text{for } i > 1. \quad (2)$$

Dividing the chunks in two parts is enough to apply the principle of the multi-installment strategy [23], in order to allow the computation to overlap the communications between a worker and the master as can be seen in Figure 3. Let us suppose that the ratio between $\hat{\alpha}_{w,i}$ and $\alpha_{w,i}$ is constant, and let us denote θ_w this ratio :

$$\theta_w \equiv \frac{\hat{\alpha}_{w,i}}{\alpha_{w,i}}. \quad (3)$$

As the DA1 method can be considered like a specific case of the OLMR one (when θ_w equals one), only the OLMR method is presented in Figures 1 and 2.

As can be seen in Figure 3, worker round i for worker w is composed of three phases :

- transmission of the data from master to worker, lasting $\hat{D}_{w,i}$ and $\check{D}_{w,i}$ for subchunks \hat{s} and \check{s} respectively,
- worker computation on the received data, lasting $\hat{C}_{w,i}$ and $\check{C}_{w,i}$ for subchunks \hat{s} and \check{s} respectively,
- transmission of the computation result from worker to master, lasting $\hat{R}_{w,i}$ and $\check{R}_{w,i}$ for subchunks \hat{s} and \check{s} respectively,

As, we have supposed that the communication and computation costs are both roundwise affine in the size of the corresponding chunk,

$$\begin{aligned} \hat{D}_{w,i} &= \theta_w \frac{\alpha_{w,i}}{B_w^D} + b_w^D, & \check{D}_{w,i} &= (1 - \theta_w) \frac{\alpha_{w,i}}{B_w^D} + b_w^D, \\ \hat{C}_{w,i} &= \theta_w \frac{\alpha_{w,i}}{F_w} + f_w, & \check{C}_{w,i} &= (1 - \theta_w) \frac{\alpha_{w,i}}{F_w} + f_w, \\ \hat{R}_{w,i} &= \theta_w \frac{\alpha_{w,i}}{B_w^R} + b_w^R, & \check{R}_{w,i} &= (1 - \theta_w) \frac{\alpha_{w,i}}{B_w^R} + b_w^R. \end{aligned}$$

Where F_w is the available computation speed of worker w .

```

• Arbitrarily set  $\tau$  and  $(\alpha_{w,1}, \theta_w)_{0 \leq w \leq W-1}$ 
• Set  $(\hat{\alpha}_{w,1}, \check{\alpha}_{w,1})_{0 \leq w \leq W-1} \dots \dots \dots (3), (1)$ 
• As soon as possible, post  $\hat{s}$  and  $\check{s}$  data to each worker
while (the last data item has not been acquired) do
  • Get a subchunk result from some worker  $w$ 
  if ( $\check{s}$ ) then
    • Compute the size of the next  $\hat{s}$  and  $\check{s}$  for worker  $w \dots \dots \dots (2), (3), (1)$ 
    • Post  $\hat{s}$  and  $\check{s}$  data to worker  $w$ 
  end if
end while

```

Fig. 1. OLMR scheduling : master

```

while (the last subchunk has not been posted) do
  • Get a subchunk data from master
  • Process the subchunk data
  • Post subchunk result to master
end while

```

Fig. 2. OLMR scheduling : worker

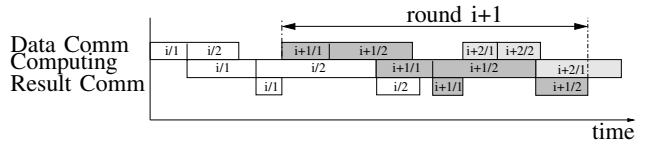


Fig. 3. Overlapping between communication and computation with the OLMR method

This computation speed is relative to the processing of one workload unit. It has to be noticed that the computation speed depends both on the platform and the application, by means of the clock frequency of worker w and the algorithm complexity respectively. Likewise, B_w^D (resp. B_w^R) is the available communication speed (relative to one workload unit) of the link from the master to worker w (resp. from worker w to the master). Finally b_w^D , b_w^R and f_w are the respective latencies for a transfer of data from the master to worker w , for a transfer of result from worker w to the master and for a computation on worker w .

Would a contention-free situation be workable, overlapping between computations and communications for OLMR would help keep workers busy, which DA1 scheduling cannot do. More, under this hypothesis, both underlying methods have ultimately reached another target, that to adapt the scheduling to the evolution of the execution parameters: the automatic taking into account of the platform heterogeneity.

IV. Presentation and analysis of the AS4DR method

A large number of simulations we made (with Sim-Grid [24]) of the OLMR scheduling convinced us that working out a contention-free situation is unlikely in practice. The main contribution of AS4DR is to address this contentions avoidance problem.

Figures 4 and 5 give the scheduling algorithm of the AS4DR method and Figure 6 shows how computation overlaps communication with this method. In order to

- With CIP set τ and $(\alpha_{w,1}, \theta_w) \dots \dots \dots$ (Figure 12)
 $0 \leq w \leq W-1$
- Set $(\dot{\alpha}_{w,1}, \ddot{\alpha}_{w,1})_{0 \leq w \leq W-1} \dots \dots \dots$ (3), (1)
- After an appropriate delay, post \dot{s} and \ddot{s} data to each worker

while (the last data item has not been acquired) **do**

- Get a \dot{s} result from some worker w
- Compute the size of the next \dot{s} and \ddot{s} for worker $w \dots \dots \dots$ (4), (6), (3), (1)
- Post \dot{s} and \ddot{s} data to worker w
- Get previous \ddot{s} result from worker w

end while

Fig. 4. AS4DR scheduling : master

while (the last subchunk has not been posted) **do**

- Get a subchunk data from master
- Process the subchunk data

if (\dot{s}) **then**

- Post \dot{s} and previous \ddot{s} results to master

end if

end while

Fig. 5. AS4DR scheduling : worker

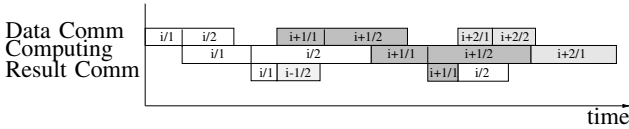


Fig. 6. Overlapping between communication and computation with the AS4DR method

make contentions avoidance easier, AS4DR delays the return of \ddot{s} . As it can be noticed in Figure 6 that, contrary to OLMR scheduling, the result corresponding to the \ddot{s} subchunk of some round is returned to the master just after the result corresponding to the \dot{s} subchunks of the next round has been itself returned. As will be stated later on, in

Proposition 1, data sending and computation is asymptotically periodic over the successive rounds. Thus delaying the return of \ddot{s} result makes result return asymptotically periodic too. We will see that this asymptotic periodicity helps contentions avoidance.

We define

$$\tilde{\sigma}_{w,i} \equiv \frac{\dot{C}_{w,i} - f_w}{\theta_w} + 2f_w. \quad (4)$$

The value of $\tilde{\sigma}_{w,i}$ is an extrapolation of the measured value of $\dot{C}_{w,i}$, in order to estimate $C_{w,i}$. So,

$$\tilde{\sigma}_{w,i} = \alpha_{w,i} \frac{1}{F_w} + 2f_w. \quad (5)$$

The AS4DR method computes the next chunk size using equation (6) in the same way as DA1 method does using equation (2).

$$\alpha_{w,i} := \alpha_{w,i-1} \frac{\tau}{\tilde{\sigma}_{w,i-1}} \quad \text{for } i > 1. \quad (6)$$

Proposition 1: For each worker w the sequence $(\tilde{\sigma}_{w,i})_i$ built according to the AS4DR method converges, and

$$\lim_{i \rightarrow +\infty} \tilde{\sigma}_{w,i} = \tau.$$

So, if the communications between master and workers are contention-free, both dates of posting data from the master and dates of getting results by the master are asymptotically periodic, with the same period for all the workers. In the absence of idleness of the workers, this period equals, τ .

Proof: From (5) we can deduce that, $\forall i \in \mathbb{N}$,

$$\tilde{\sigma}_{w,i+2} - \tilde{\sigma}_{w,i+1} = (\alpha_{w,i+2} - \alpha_{w,i+1}) \frac{1}{F_w}. \quad (7)$$

Besides, according to (6),

$$\alpha_{w,i+2} = \alpha_{w,i+1} \frac{\tau}{\tilde{\sigma}_{w,i+1}} \quad \text{and} \quad \alpha_{w,i+1} = \alpha_{w,i} \frac{\tau}{\tilde{\sigma}_{w,i}}.$$

Hence

$$\tilde{\sigma}_{w,i+2} - \tilde{\sigma}_{w,i+1} = \tau \left(\frac{\alpha_{w,i+1}}{\tilde{\sigma}_{w,i+1}} - \frac{\alpha_{w,i}}{\tilde{\sigma}_{w,i}} \right) \frac{1}{F_w}. \quad (8)$$

Due to (7), equation (8) can be rewritten

$$\tilde{\sigma}_{w,i+2} - \tilde{\sigma}_{w,i+1} = \frac{2\tau f_w}{F_w \tilde{\sigma}_{w,i+1} \tilde{\sigma}_{w,i}} (\alpha_{w,i+1} - \alpha_{w,i}). \quad (9)$$

Now let us suppose that

$$\frac{\tau}{\tilde{\sigma}_{w,i}} \geq 1,$$

then it follows from (6) that $\alpha_{w,i+1} \geq \alpha_{w,i}$ and thus from (9) that

$$\tilde{\sigma}_{w,i+2} \geq \tilde{\sigma}_{w,i+1}. \quad (10)$$

Due to (7), inequality (10) can be rewritten

$$\alpha_{w,i+2} \geq \alpha_{w,i+1}.$$

This proves that the sequence $(\alpha_{w,i})_i$ is monotonously growing when $\frac{\tau}{\tilde{\sigma}_{w,i}} \geq 1$. A similar demonstration can prove that the sequence $(\alpha_{w,i})_i$ is monotonously decreasing when $\frac{\tau}{\tilde{\sigma}_{w,i}} \leq 1$.

From (7), $\tilde{\sigma}_{w,i}$ is a growing function of $\alpha_{w,i}$. So the sequence $(\tilde{\sigma}_{w,i})_i$ is monotonously increasing when $\frac{\tau}{\tilde{\sigma}_{w,i}} \geq 1$, and monotonously decreasing when $\frac{\tau}{\tilde{\sigma}_{w,i}} \leq 1$.

In order to show that both sequences $(\alpha_{w,i})_i$ and $(\tilde{\sigma}_{w,i})_i$ have limits, it suffices to show that they are either upper bounded when increasing or lower bounded when decreasing. But, when sequence $(\tilde{\sigma}_{w,i})_i$ is increasing, sequence $(\alpha_{w,i})_i$ is increasing too, and from (6), we have $\frac{\tau}{\tilde{\sigma}_{w,i}} \geq 1, \forall i \in \mathbb{N}$. In this case, the elements of sequence $(\tilde{\sigma}_{w,i})_i$ are upper bounded by τ . Thus, from (5), we have

$$\alpha_{w,i} \leq \frac{\tau - 2f_w}{\frac{1}{F_w}} \quad \forall i \in \mathbb{N}.$$

So, if $\frac{\tau}{\tilde{\sigma}_{w,i}} \geq 1$, the increasing sequences $(\alpha_{w,i})_i$ and $(\tilde{\sigma}_{w,i})_i$ are upper bounded thus have a limit. A similar argumentation would prove that, if $\frac{\tau}{\tilde{\sigma}_{w,i}} < 1$, then the decreasing sequences $(\alpha_{w,i})_i$ et $(\tilde{\sigma}_{w,i})_i$ are lower bounded and thus have a limit. From now on, we denote α_w and $\tilde{\sigma}_w$ the respective limits of $(\alpha_{w,i})_i$ and $(\tilde{\sigma}_{w,i})_i$:

$$\begin{aligned} \alpha_w &\equiv \lim_{i \rightarrow \infty} \alpha_{w,i}, \\ \tilde{\sigma}_w &\equiv \lim_{i \rightarrow \infty} \tilde{\sigma}_{w,i}. \end{aligned} \quad (11)$$

There now remains to show that the limit of $\tilde{\sigma}_{w,i}$ does not depend on the chosen worker w , and more precisely that this limit is τ .

So let us show (ab absurdo) that $\tilde{\sigma}_w$ equals τ . Assume, first, that

$$\tilde{\sigma}_w < \tau. \quad (12)$$

Let

$$\varepsilon_0 \equiv \alpha_w \left(1 - \frac{\tilde{\sigma}_w}{\tau} \right).$$

By hypothesis (12),

$$\varepsilon_0 > 0.$$

Thanks to the convergence of $\alpha_{w,i}$ up towards α_w , we can say that $\forall \varepsilon \in \mathbb{R}^{+*}, \exists N \in \mathbb{N}^*$ such that $i > N$ entails $\alpha_w - \alpha_{w,i} < \varepsilon$.

In particular, for

$$\varepsilon = \varepsilon_0$$

a natural i_0 exists such that

$$\begin{aligned} \alpha_w - \alpha_{w,i_0} &< \alpha_w \left(1 - \frac{\tilde{\sigma}_w}{\tau} \right), \\ \alpha_{w,i_0} &> \alpha_w \frac{\tilde{\sigma}_w}{\tau}. \end{aligned} \quad (13)$$

Besides, the sequence $(\tilde{\sigma}_{w,i})_i$ is increasing and upper bounded by $\tilde{\sigma}_w$. In particular,

$$\tilde{\sigma}_w \geq \tilde{\sigma}_{w,i_0}. \quad (14)$$

Thanks to (6), we have

$$\alpha_{w,i_0+1} = \alpha_{w,i_0} \frac{\tau}{\tilde{\sigma}_{w,i_0}}.$$

By (13) and (14), this equation allows us to write

$$\begin{aligned} \alpha_{w,i_0+1} &> \alpha_w \frac{\tilde{\sigma}_w}{\tau} \frac{\tau}{\tilde{\sigma}_w}, \\ &> \alpha_w. \end{aligned}$$

This contradicts the fact that α_w is an upper bound of $\alpha_{w,i}$ for all $i \in \mathbb{N}^*$. Therefore, hypothesis (12) is false. Similarly, the hypothesis :

$$\tilde{\sigma}_w > \tau,$$

leads to a contradiction too. Therefore, $\tilde{\sigma}_w$ equals τ . ■

Remark 1: It has to be noticed that this result is true either if the master serves the workers in a pre-determined order (round-robin) or if it serves the first worker to be idle systematically (FIFO).

Figure 8 shows a simulation result obtained with Sim-Grid [24], which illustrates the convergence of $\tilde{\sigma}_{w,i}$ towards τ stated in Proposition 1. To get this illustration, we considered a small star-shaped platform with a master connected to 4 workers $(w_i)_{i=1,4}$ by bi-directional links (Figure 7). Table I gives the value of the execution param-

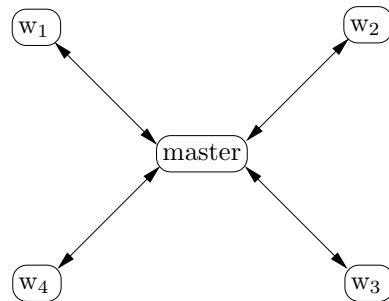


Fig. 7. Star-shaped platform

eters considered for this platform. Times are in seconds, computation and communication speeds are in MB/s.

		w ₁	w ₂	w ₃	w ₄
computation	latency	3.0	0.1	0.1	0.1
	speed	0.0935	0.0893	0.0587	0.0617
communication master → w _i	latency	0.8	0.1	0.9	0.8
	speed	0.9025	0.9318	0.9633	0.9143
communication master ← w _i	latency	2.0	0.8	1.0	0.9
	speed	0.9025	0.9318	0.9633	0.9143

TABLE I. Execution parameter values

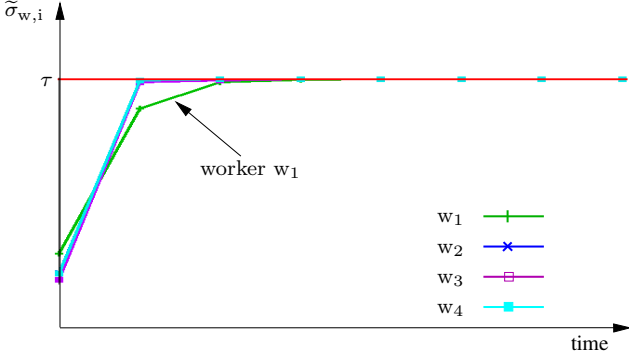


Fig. 8. AS4DR method: convergence of $\tilde{\sigma}_{w,i}$

From the contents of Table I, it can be claimed that the considered platform is heterogeneous. For instance, it takes worker w_1 3.0935 seconds to process a chunk of unit size whereas it takes worker w_3 only 0.1587 seconds. As a result, the duration to process a chunk can turn out to be about 20 times longer, from one worker to another one.

For this elementary example, $\tilde{\sigma}_{w,i}$ converges slower for worker 1 than for the other workers because its computation latency is 30 times larger than that of the others (see Table I).

As AS4DR aims to improve the throughput of the DA1 method, let us compare the respective average throughputs δ_w^{DA1} and δ_w^{AS4DR} of both methods on a time interval of duration τ :

$$\delta_w^{\text{DA1}} \equiv \frac{\alpha_w^{\text{DA1}}}{\tau} \quad \text{and} \quad \delta_w^{\text{AS4DR}} \equiv \frac{\alpha_w^{\text{AS4DR}}}{\tau}.$$

Proposition 2: Let suppose firstly that the communications between master and workers are contention-free and that there is no intra-round idleness, and secondly that

$$\frac{F_w}{B_w^D} + \frac{F_w}{B_w^R} \geq \frac{3f_w - b_w^D - b_w^R}{\tau - 2f_w}; \quad (15)$$

where $\tau \neq 2f_w$.

If

$$\frac{f_w}{\tau} < \frac{1}{2} \quad (16)$$

then

$$\delta_w^{\text{AS4DR}} \geq \delta_w^{\text{DA1}}$$

else

$$\delta_w^{\text{AS4DR}} \leq \delta_w^{\text{DA1}}.$$

Proof: Inequation (15) leads to

$$1 + \frac{F_w}{B_w^D} + \frac{F_w}{B_w^R} \geq 1 + \frac{3f_w - b_w^D - b_w^R}{\tau - 2f_w},$$

$$\frac{\frac{1}{B_w^D} + \frac{1}{F_w} + \frac{1}{B_w^R}}{\frac{1}{F_w}} \geq \frac{\tau - (b_w^D - f_w + b_w^R)}{\tau - 2f_w}.$$

Thus, if (16) holds, we have :

$$\frac{\tau - 2f_w}{\frac{1}{F_w}} \geq \frac{\tau - (b_w^D - f_w + b_w^R)}{\frac{1}{B_w^D} + \frac{1}{F_w} + \frac{1}{B_w^R}},$$

$$\alpha_w^{\text{AS4DR}} \geq \alpha_w^{\text{DA1}},$$

$$\delta_w^{\text{AS4DR}} \geq \delta_w^{\text{DA1}}.$$

Conversely, if (16) does not hold, then

$$\delta_w^{\text{AS4DR}} \leq \delta_w^{\text{DA1}}.$$

■

Remark 2: Practically, the duration τ , typically targeted for a round, is much larger than that of the computation latency f_w . Therefore, inequation (16) should generally hold and AS4DR throughput be larger than DA1's one.

Remark 3: Inequation (15) states that the smaller the communication speeds and the larger the communication latencies, the more efficient the overlapping between communication and computation. Although latencies have to be considered in order to validate the convergence result for AS4DR, it should be clear that multi-round scheduling is only interesting when latencies are negligible compared to the duration τ of rounds. In this case, hypothesis (15) can be rewritten:

$$\frac{F_w}{B_w^D} + \frac{F_w}{B_w^R} \geq 0$$

which is always true.

In order to measure the possible throughput improvement provided by AS4DR over DA1, simulations have been conducted with SimGrid [24]. To get a deeper insight into the respective performance of both methods, we define γ :

$$\gamma \equiv 100 \cdot \frac{x_{\text{AS4DR}} - x_{\text{DA1}}}{x_{\text{DA1}}};$$

for each scheduling characteristic under consideration (idleness, throughput...), where x_{DA1} and x_{AS4DR} are the measured values for DA1 and AS4DR respectively. In order to be able to start DA1 without contention, the CIP algorithm, which is part of AS4DR (see section V), had to be used (with $\theta = 1$). Table II gives the results obtained

when each method was used to schedule a 20000 MB workload on the previous simple platform (cf Table I). Idleness is in seconds, throughput is in MB/s.

	DA1	AS4DR	γ
Idleness of the workers	52858.70	0.00	+100.00%
Throughput	0.2506	0.3020	+20.50%

TABLE II. Comparison between DA1 and AS4DR

These results illustrate Proposition 2: with the AS4DR method, the measured throughput is 20.5% higher than the one obtained with the DA1 method, coupled with the CIP algorithm (in order to avoid contentions). As AS4DR totally prevents idleness, workers are fully busy computing, and thus no other improvement of the throughput can be hoped: for this example, the scheduling is optimal.

To the best of our knowledge, it is not possible to avoid contentions when using the OLMR method, even if this method is coupled with the CIP algorithm (because of the unpredictability of the date for the return of the subchunks \tilde{s} with OLMR). As the contention avoidance is a necessary condition to use the OLMR method [22], the comparison of AS4DR with the OLMR method cannot be made.

V. Towards prevention of idleness and contentions

In spite of their theoretical interest, intensive Sim-Grid [24] simulations have revealed a huge drawback of the DA1 and OLMR methods: the difficulty to work out values for τ and $\alpha_{w,1}$ that lead to a contention-free starting situation. More, contention prevention is not enough to give a practical interest to the AS4DR method, as it could experience workers idleness. So the preliminary step CIP (for Contentions and Idleness Prevention) described in this section has been introduced, in order to try to avoid both pitfalls. Firstly, we are going to exhibit necessary and sufficient conditions for the prevention of workers idleness. Then, we are going to expose necessary and sufficient conditions to avoid contentions on the links between master and workers. Finally, we will see how the CIP algorithm takes these two results into account to avoid contentions and idleness during the AS4DR scheduling first round.

Figures 9 and 10 illustrate the two kinds of idleness we can meet: inter-round idleness and intra-round idleness.

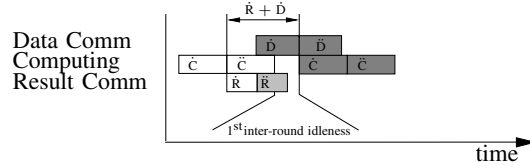


Fig. 9. Example of inter-round idleness with AS4DR

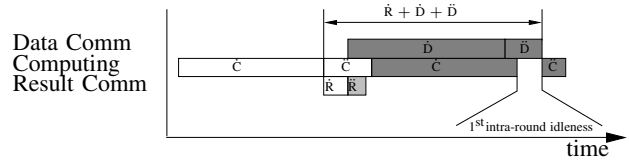


Fig. 10. Example of intra-round idleness with AS4DR

Proposition 3: Let us respectively define, $\forall i$, $\theta_{w,i}^{\min}$ and $\theta_{w,i}^{\max}$ as:

$$\theta_{w,i+1}^{\min} \equiv \frac{D_{w,i+1} - (b_w^D + f_w)}{D_{w,i+1} + C_{w,i+1} - 2(b_w^D + f_w)}, \quad (17)$$

$$\theta_{w,i+1}^{\max} \equiv \frac{C_{w,i} - (b_w^D + f_w + b_w^R)}{D_{w,i+1} + C_{w,i} + R_{w,i} - 2(b_w^D + f_w + b_w^R)}. \quad (18)$$

The AS4DR method prevents idleness, if and only if, for each worker w , and $\forall i$

$$\theta_{w,i+1}^{\max} \geq \theta_w \geq \theta_{w,i+1}^{\min}. \quad (19)$$

Proof: If $\theta_{w,i+1}^{\max} \geq \theta_w$, then

$$\frac{\frac{\alpha_{w,i}}{F_w} + f_w - b_w^R - b_w^D}{\frac{\alpha_{w,i}}{F_w} + \frac{\alpha_{w,i+1}}{B_w^D} + \frac{\alpha_{w,i}}{B_w^R}} \geq \theta_w,$$

$$(1 - \theta_w) \frac{\alpha_{w,i}}{F_w} + f_w \geq \left(\theta_w \frac{\alpha_{w,i+1}}{B_w^D} + b_w^D \right) + \left(\theta_w \frac{\alpha_{w,i}}{B_w^R} + b_w^R \right).$$

Therefore,

$$\dot{C}_{w,i} \geq \dot{R}_{w,i} + \dot{D}_{w,i+1},$$

and no inter-round idleness can occur; and reciprocally.

Now, if we suppose furthermore that $\theta_w \geq \theta_{w,i+1}^{\min}$, then

$$\theta_w \geq \frac{\frac{\alpha_{w,i+1}}{B_w^D} + b_w^D - f_w}{\alpha_{w,i+1} \left(\frac{1}{F_w} + \frac{1}{B_w^D} \right)},$$

$$\theta_w \frac{\alpha_{w,i+1}}{F_w} + f_w \geq (1 - \theta_w) \frac{\alpha_{w,i+1}}{B_w^D} + b_w^D.$$

Thus

$$\dot{C}_{w,i+1} \geq \ddot{D}_{w,i+1}.$$

As $\theta_{w,i+1}^{\max} \geq \theta_w$, then $\dot{C}_{w,i} \geq \dot{R}_{w,i} + \dot{D}_{w,i+1}$ holds too. Summing these two inequalities leads to :

$$\dot{C}_{w,i} + \dot{C}_{w,i+1} \geq \dot{R}_{w,i} + \dot{D}_{w,i+1} + \ddot{D}_{w,i+1},$$

and so no intra-round idleness can occur; and reciprocally. ■

Figure 11 shows the model of asymptotic τ -periodic

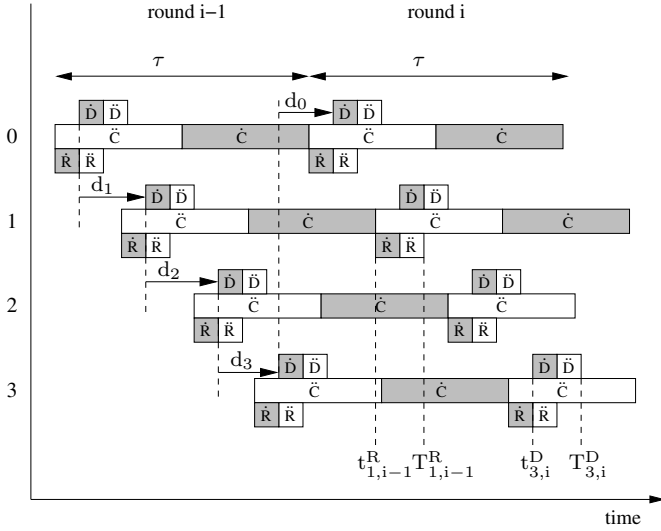


Fig. 11. contention-free asymptotic τ -periodic schedule

(thus round-robin) scheduling we are looking for, in the case of a four workers platform. It illustrates the definition of instants $t_{w,i}^R$, $T_{w,i}^R$, $t_{w,i}^D$ and $T_{w,i}^D$.

To make the instant each worker accesses to the master far enough from the instants the others access too, time delays d_w are introduced before posting the very first subchunk to each worker w . We are going to see that introducing such laxity in the scheduling makes the AS4DR method more compliant with the errors on the estimate of the execution parameters.

Proposition 4: The AS4DR method prevents contentions, if and only if, $\forall i$,

$$d_w \geq \max(\dot{D}_{w-1,i+1} + \ddot{D}_{w-1,i+1}, \ddot{R}_{w-1,i-1} + \dot{R}_{w,i}) \quad (20)$$

for each worker w (modulo W).

Proof: There are no contentions if and only if, for any worker and any round, the instant for data posting from the master to a worker occurs after the end of all

past emissions of data to other workers and before the beginning of all future receptions of results from other workers. In fact, due to the spontaneous round-robin order, this condition amounts, for worker w and all round i , to:

$$T_{w-1,i}^R \leq t_{w,i}^R \text{ and } T_{w-1,i}^D \leq t_{w,i}^D. \quad (21)$$

Hence, for all w (modulo W) system (21) can be rewritten as:

$$d_w \geq \dot{D}_{w-1,i+1} + \ddot{D}_{w-1,i+1}, \\ d_w \geq \ddot{R}_{w-1,i-1} + \dot{R}_{w,i}.$$

Thus, for $0 \leq w \leq W-1$ (modulo W),

$$d_w \geq \max(\dot{D}_{w-1,i+1} + \ddot{D}_{w-1,i+1}, \ddot{R}_{w-1,i-1} + \dot{R}_{w,i}). \quad \blacksquare$$

The larger the d_w time intervals, the lower the risk of contentions in case of inaccurate execution parameters estimation.

When launching the AS4DR scheduler (see Figure 4), the value for τ and three sets of values $(\alpha_{w,1})_{0 \leq w \leq W-1}$, $(\theta_w)_{0 \leq w \leq W-1}$ and $(d_w)_{0 \leq w \leq W-1}$ must be determined in order to start the scheduling without contention. This is done by CIP algorithm from an estimate of the execution parameters, as shown by Figure 12.

Let \overline{B}_w^D (resp. \overline{F}_w , \overline{B}_w^R , \overline{b}_w^D , \overline{f}_w and \overline{b}_w^R) denote this estimate of B_w^D (resp. F_w , B_w^R , b_w^D , f_w and b_w^R).

Depending on the inaccuracy in the estimation of the execution parameters, the delay d_w , computed by the CIP algorithm to be introduced in the scheduling, is defined as follows.

$$d_w := (1 + \lambda_w) \max(\overline{D}_{w-1} + \overline{D}_{w-1}, \overline{R}_{w-1} + \overline{R}_w) \text{ (modulo } W); \quad (22)$$

where λ_w is a positive constant factor for worker w , and \overline{D}_w (resp. \overline{D}_w , \overline{R}_w and \overline{R}_w) are estimates of $\dot{D}_{w,1}$ (resp. $\dot{D}_{w,1}$, $\dot{R}_{w,1}$ and $\dot{R}_{w,1}$) thanks to

$$\overline{D}_w := \theta_w \alpha_w \frac{1}{\overline{B}_w^D} + \overline{b}_w^D, \quad (23)$$

$$\overline{D}_w := (1 - \theta_w) \alpha_w \frac{1}{\overline{B}_w^D} + \overline{b}_w^D, \quad (24)$$

$$\overline{R}_w := \theta_w \alpha_w \frac{1}{\overline{B}_w^R} + \overline{b}_w^R, \quad (25)$$

$$\overline{R}_w := (1 - \theta_w) \alpha_w \frac{1}{\overline{B}_w^R} + \overline{b}_w^R. \quad (26)$$

For relation (20) to hold, the greater this inaccuracy on the estimate of the execution parameters, the larger λ_w should be chosen.

The computation of \overline{D}_w , \overline{D}_w , \overline{R}_w and \overline{R}_w requires the

values of α_w and θ_w :

$$\alpha_w := \frac{\tau - 2\overline{f_w}}{\frac{1}{F_w}} \quad (27)$$

$$\theta_w := \phi_w \theta_w^{\max} + (1 - \phi_w) \theta_w^{\min}; \quad (28)$$

where θ_w^{\min} and θ_w^{\max} are respectively deduced from (17) and (18) as follows

$$\theta_w^{\min} := \frac{\frac{\alpha_w}{\overline{B_w^D}} + \overline{b_w^D} - \overline{f_w}}{\alpha_w \left(\frac{1}{\overline{F_w}} + \frac{1}{\overline{B_w^D}} \right)},$$

$$\theta_w^{\max} := \frac{\frac{\alpha_w}{\overline{F_w}} + \overline{f_w} - \overline{b_w^R} - \overline{b_w^D}}{\alpha_w \left(\frac{1}{\overline{F_w}} + \frac{1}{\overline{B_w^R}} + \frac{1}{\overline{B_w^D}} \right)}.$$

When

$$\phi_w = 0.5, \quad (29)$$

the risks of intra-round idleness and inter-round idleness are well balanced. But the value of θ_w could be obtained by averaging θ_w^{\min} and θ_w^{\max} with other weights than 0.5 (see Remark 5).

- Estimation of $(\overline{B_w^D}, \overline{F_w}, \overline{B_w^R}, \overline{b_w^D}, \overline{f_w}, \overline{b_w^R})_{0 \leq w \leq W-1}$
- Set $(\phi_w)_{0 \leq w \leq W-1} \dots \dots \dots (29)$
- Set $(\lambda_w)_{0 \leq w \leq W-1} \dots \dots \dots (36)$
- $\tau := 2 \max_{0 \leq s \leq W-1} \overline{f_w}$
- repeat**
- $\tau := \tau + \hat{\tau}$
- Set $(\alpha_w)_{0 \leq w \leq W-1} \dots \dots \dots (27)$
- Set $(\theta_w)_{0 \leq w \leq W-1} \dots \dots \dots (28)$
- Set $(\overline{D_w}, \overline{D_w}, \overline{R_w}, \overline{R_w})_{0 \leq w \leq W-1} \dots \dots (23),(24),(25),(26)$
- Set $(d_w)_{0 \leq w \leq W-1} \dots \dots \dots (22)$
- until** $(\tau \geq \sum_{w=0}^{W-1} d_w)$

Fig. 12. CIP algorithm

Besides, the time intervals d_w should allow all the workers to be served during the first round, i.e. within a τ period. Thus τ must verify:

$$\tau \geq \sum_{w=0}^{W-1} d_w. \quad (30)$$

So, starting from a computed initial value, an iterative process increments τ with an arbitrarily fixed value $\hat{\tau}$, then computes $(\alpha_w)_{0 \leq w \leq W-1}$ and $(d_w)_{0 \leq w \leq W-1}$ successively, thanks to (27) and (22) respectively, until (30) holds.

Once CIP is processed, proper AS4DR scheduling starts with a first round which sets the initial time-lags between successive round beginnings, according to the values $(d_w)_{0 \leq w \leq W-1}$ previously computed by CIP.

For the next rounds, the AS4DR method, thanks to assignment (6), helps maintain the duration of each round close to the reference value τ . Then the algorithm loops until the end of the flow. In order to state the next Proposition, we define the value Λ_w for each worker w as

$$\Lambda_w \equiv \frac{1}{\overline{F_w} \max \left(K_w + \frac{1}{\overline{B_w^D}}, \frac{1}{\overline{B_w^R}} \right)} - 1;$$

$$\text{where } K_w \equiv \frac{1}{\overline{B_w^R}} \left(\phi_w \frac{1}{1 + \overline{F_w} \left(\frac{1}{\overline{B_w^D}} + \frac{1}{\overline{B_w^R}} \right)} + (1 - \phi_w) \frac{1}{1 + \frac{\overline{B_w^D}}{\overline{F_w}}} \right).$$

Proposition 5: Let us assume that for each worker w

$$\lambda_w \leq \min(\Lambda_w, \Lambda_{w-1}) \quad (\text{modulo } W). \quad (31)$$

Then the CIP preliminary step provides a τ value and sets of values $(\theta_w)_{0 \leq w \leq W-1}$, $(\alpha_w)_{0 \leq w \leq W-1}$ and $(d_w)_{0 \leq w \leq W-1}$ that allow the AS4DR scheduling to start with neither contention nor idleness.

Proof: In order to make this proof easier to read, we drop the overlined notation for the estimate values in the body of the proof.

As the value of ϕ_w is chosen within the interval $[0, 1]$, θ_w satisfies the hypotheses of Proposition 3; therefore the AS4DR method avoids idleness during the first round.

As both τ and $\sum_{w=0}^{W-1} d_w$ are affine functions of α_w , showing that τ increases quicker than $\sum_{w=0}^{W-1} d_w$ does, should ensure that τ will finally dominate $\sum_{w=0}^{W-1} d_w$ and the loop will end; and thus prove the result. Due to hypothesis (31), for each worker w :

$$\lambda_w \leq \Lambda_w \quad \text{and} \quad \lambda_{w+1} \leq \Lambda_w \quad (\text{modulo } W).$$

Thus,

$$\max(\lambda_w, \lambda_{w+1}) \leq \Lambda_w, \leq \frac{1}{\overline{F_w} \max \left(K_w + \frac{1}{\overline{B_w^D}}, \frac{1}{\overline{B_w^R}} \right)} - 1.$$

Therefore

$$(1 + \max(\lambda_w, \lambda_{w+1})) \max \left(K_w + \frac{1}{\overline{B_w^D}}, \frac{1}{\overline{B_w^R}} \right) \leq \frac{1}{\overline{F_w}}. \quad (32)$$

Since

$$\begin{aligned}\frac{\partial (\dot{D}_w + \ddot{D}_w)}{\partial \alpha_w} &= \frac{1}{B_w^D}, \\ \frac{\partial \dot{R}_w}{\partial \alpha_w} &= K_w, \\ \frac{\partial \ddot{R}_w}{\partial \alpha_w} &= \frac{1}{B_w^R} - K_w, \\ \text{and } \frac{\partial \tau}{\partial \alpha_w} &= \frac{1}{F_w}.\end{aligned}$$

then inequality (32) can be rewritten

$$(1 + \max(\lambda_w, \lambda_{w+1})) \left(\frac{\partial \dot{R}_w}{\partial \alpha_w} + \max \left(\frac{\partial (\dot{D}_w + \ddot{D}_w)}{\partial \alpha_w}, \frac{\partial \ddot{R}_w}{\partial \alpha_w} \right) \right) \leq \frac{\partial \tau}{\partial \alpha_w}. \quad (33)$$

Taking into account the definition of d_w , given by (22), the derivation of $\sum_{w=0}^{W-1} d_w$ with respect to α_w can potentially lead to six values according to the α_w dependency of the different terms of the sum :

$$\begin{aligned}0, & (1 + \lambda_{w+1}) \frac{\partial (\dot{D}_w + \ddot{D}_w)}{\partial \alpha_w}, \\ (1 + \lambda_w) \frac{\partial \dot{R}_w}{\partial \alpha_w}, & (1 + \lambda_{w+1}) \frac{\partial \ddot{R}_w}{\partial \alpha_w}, \\ (1 + \lambda_w) \frac{\partial \dot{R}_w}{\partial \alpha_w} + (1 + \lambda_{w+1}) \frac{\partial (\dot{D}_w + \ddot{D}_w)}{\partial \alpha_w}, & \\ (1 + \lambda_w) \frac{\partial \dot{R}_w}{\partial \alpha_w} + (1 + \lambda_{w+1}) \frac{\partial \ddot{R}_w}{\partial \alpha_w}. & \end{aligned}$$

As all the derivatives have positive values, due to the cost model, the greater of the last two of these six values can be an upper bound for the derivative of $\sum_{w=0}^{W-1} d_w$ with respect to α_w .

So,

$$\frac{\partial \sum_{w=0}^{W-1} d_w}{\partial \alpha_w} \leq (1 + \max(\lambda_w, \lambda_{w+1})) \left(\frac{\partial \dot{R}_w}{\partial \alpha_w} + \max \left(\frac{\partial (\dot{D}_w + \ddot{D}_w)}{\partial \alpha_w}, \frac{\partial \ddot{R}_w}{\partial \alpha_w} \right) \right). \quad (34)$$

From inequalities (33) and (34), we deduce

$$\frac{\partial \sum_{w=0}^{W-1} d_w}{\partial \alpha_w} \leq \frac{\partial \tau}{\partial \alpha_w}. \quad (35)$$

So, under the hypotheses of Proposition 5, τ as a function of α_w increases quicker than $\sum_{w=0}^{W-1} d_w$. Hence, incrementing α_w for each worker w iteratively, as CIP does, leads the loop to an end. ■

Thanks to Proposition 5, the CIP algorithm sets the λ_w values as follows:

$$\lambda_w := \min(\Lambda_w, \Lambda_{w-1}) \pmod{W}. \quad (36)$$

If the chosen values for λ_w are too small for in-equation (20) to hold, then contentions avoidance during AS4DR is no more guaranteed (Proposition 4). So a condition should be added to the main loop of AS4DR in order to exit in case of contention, and re-run the scheduling with a reduced number of workers which makes equation (30) easier to satisfy. The optimal selection among the workers through this reduction process remains to be tackled.

Remark 4: To get an acceptable θ_w value, the interval $[\theta_w^{\min}, \theta_w^{\max}]$ must not be empty. This is the case, for example, when latencies are negligible compared to effective computation and communication durations and when the available communication speed is greater than the available computation one. Indeed:

$$\begin{aligned}\overline{F_w} &\leq \overline{B_w^D}, \\ \frac{1}{\overline{F_w}} \left(\frac{1}{\overline{F_w}} + \frac{1}{\overline{B_w^R}} \right) &\geq \frac{1}{(\overline{B_w^D})^2}, \\ \frac{1}{\overline{F_w}} \left(\frac{1}{\overline{F_w}} + \frac{1}{\overline{B_w^D}} + \frac{1}{\overline{B_w^R}} \right) &\geq \frac{1}{\overline{B_w^D}} \left(\frac{1}{\overline{F_w}} + \frac{1}{\overline{B_w^D}} \right), \\ \theta_w^{\max} &\geq \theta_w^{\min}.\end{aligned}$$

Remark 5: By analysing the evolution of K_w according to $\phi_w \in [0,1]$, it can be proved that Λ_w is an increasing function of ϕ_w if and only if

$$\frac{1}{\overline{F_w}} \geq \frac{1}{\overline{B_w^D}} \sqrt{1 + \frac{\overline{B_w^D}}{\overline{B_w^R}}},$$

and decreasing otherwise. So, in order to reduce the risk of contentions, the value of ϕ_w can be adjusted to increase the value of Λ_w (Proposition 5). But, such an adjustment brings θ_w closer to the boundary of the interval $[\theta_w^{\min}, \theta_w^{\max}]$ and thus increases the risk of idleness.

Remark 6: Multi-round scheduling is known to favor early involvement of the workers by sending small chunks for the first round. Here, paradoxically, the size of chunks for the first round is not minimized. Still worse, sending the first chunk to successive workers is delayed. These are only apparent paradoxes, since we consider applications for which the duration of the first round is negligible compared to the computation time for the whole flow.

VI. Conclusion

The AS4DR method presented in this paper succeeds in maximizing the throughput of each worker, when scheduling a divisible load of unknown total size on heterogeneous distributed resources with inaccurately specified steady characteristics. Up to now we have considered that the whole set of resources is used. Despite the fact that a bidirectional 1-port communication model is prone to contention, AS4DR can avoid this pitfall, thanks both to the asymptotic periodicity it installs (for both data and results) and to its preliminary step CIP. It has been proved and illustrated by simulations, that CIP can give a valid collection of initial values which reduce the risk of both link contention and worker idleness. Simulations with SimGrid have shown that there definitely exists an application domain for the proposed scheduling method.

Firstly, simulations of more realistic and larger platforms will be conducted in order to assess the scalability of AS4DR. Of course, for a given set of execution parameters values, using all the available resources will ultimately become impossible with an evermore increasing amount of resources. So, secondly, we will study the relationship between the optimality of the throughput of the global scheduling and that of each worker individually, when all the resources are not usable. From the conclusion of this work, the better understanding of this relationship could lead to a method to select a relevant subset of resources.

Besides, being able to provide an efficient scheduling without knowing at the initial moment the constant value of execution parameters, AS4DR should also be able to provide an efficient scheduling when this value is unknown at each moment when it is needed. For this reason AS4DR should be able to provide an efficient scheduling when the value of execution parameters evolves over time. Thus, future work will also try to check this hypothesis by means of simulations, studying the impact on the throughput of diverse ways to introduce dynamicity into the behaviour of the platform: frequency, amplitude, smoothness,... of the variations of the execution parameters value.

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