# Automatic source-to-source error compensation of floating-point programs

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# Context and motivation

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- Numerical computations can be innacurate: rounding errors
- ► **Techniques** are available for programmers to improve their numerical programs: expansions, software librairies,...
- These techniques are costly: improving accuracy impacts execution-time
- Error compensation technique allows a good tradeoff between accuracy and execution-time but reserved to experts

#### Motivation

- Accuracy and execution-time are two major concerns of software developpers
- Critical in many systems (from automotive to aerospace industry)

Automate compensation to allow non-expert users to use it:

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# Outline

#### Background on floating-point arithmetic

Error-free transformations Double-double expansions and compensated algorithms

#### Automatic program transformation

Improving accuracy: methodology Experimental results

Conclusion and perspectives

A standard to represent real numbers since 1985

- $\blacktriangleright$  F, the finite floating-point (FP) numbers following one of the formats of IEEE 754
- ▶ This set is defined by a precision p, and an exponent range  $[e_{min}, e_{max}]$  such that

p = 53,  $e_{max} = 1 - e_{min} = 1023$  in binary64 format.

▶ Has several rounding modes: to nearest (RN), to zero (RZ), to infinities (RU, RD)



 $\hookrightarrow$  A way of estimating the **accuracy** of  $\hat{x} = \mathbb{R}^*(x)$  is through the number of significant bits  $0 \le \#_{sig} \le p$  shared by x and  $\hat{x}$ :

$$\#_{sig}(\widehat{x}) = -\log_2(E_{rel}(\widehat{x})),$$

where  $E_{ref}(\hat{x})$  is the relative error defined by:  $E_{rel}(\hat{x}) = |x - \hat{x}|/|x|, \quad x \neq 0.$ 

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# Error-free transformations (EFTs)

Allow to compute the error generated by a floating-point addition or multiplication

Principle [MBdD10] Let  $\circ \in \{+, -, \times\}$ , if  $x = RN(a \circ b)$ , then the floating-point error  $y = RN(a \circ b) - x$  is exactly representable in  $\mathbb{F}$ : EFTs allow to compute y with floating-point arithmetic!

#### For the sum...

#### ... and the product

function FastTwoSum $(a, b) \triangleright [Dekker, 71]$
$x \leftarrow RN(a+b) \qquad \triangleright  a  \ge  b $
$y \leftarrow RN((a - x) + b)$
return (x, y)
end function
function TwoSum(a, b) ▷ [KNUTH, 69]
$x \leftarrow RN(a+b)$
$z \leftarrow RN(x - a)$
$y \leftarrow RN((a - (x - z)) + (b - z))$
return (x, y)
end function

$$\begin{array}{l} \mbox{function TwoProduct}(a,b) & \triangleright [DEKKER, 71] \\ x \leftarrow RN(a \times b) \\ [a_H, a_L] = Split(a) \\ [b_H, b_L] = Split(b) \\ y \leftarrow RN(a_L \times b_L - (((x - a_H \times b_H) - a_L \times b_H) - a_H \times b_L))) \\ \mbox{return} (x, y) \\ \mbox{end function} \\ \hline \\ \hline \\ \begin{array}{l} c \leftarrow RN(f \times a) \\ a_H \leftarrow RN(c - (c - a)), \\ a_L \leftarrow RN(a - a_H) \\ \mbox{return} (a_H, a_L) \\ \mbox{end function} \end{array}$$

- EFTs are costly: 3, 6, and 17 FP operations for FastTwoSum, TwoSum, and TwoProduct
- fused-multiply-add instruction can reduce the cost of TwoProduct to 2

Double-double expansions and compensated algorithms  ${\sf Improving\ accuracy\ using\ EFTs}$ 

## Two methods based on EFTs

- Double-double (DD) expansions: introduced in the 1970's [Dek71]
- Compensated algorithms: popularized in the 2000's [ROO05, GLL09]

## Double-double

- ▶ Dekker, 1971
  - ► BAILEY+, QD Lib, 2000
  - SAITO, Scilab Toolbox: QuPAT, 2010
- generic method, algorithms applied to each elementary operations
- easy automatic application (overloading)

## **Compensated algorithms**

- ▶ RUMP+, Sum2, Dot2, 2005
  - LOUVET, CompHorner, 2007
  - GRAILLAT+, CompHornerDer, 2013
- specific, expert work: a thesis or research paper per algorithm
- today: sum, dot product, polynomial evaluations

#### They provide roughly the same accuracy. . .

- Double-double is generic but has a strong impact on performance
- Compensation allows better performance: more instruction level parallelism [LL07] but it is very specific

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Benefit from the good ILP of compensation automatically: detect FP sequences

#### Detect floating-point sequences

- A sequence is the set S of all dependent operations required to obtain one or several results
- CoHD tool performs this step after transform original code in three-address form
- In this example one sequence of two operations is detected

```
double
Horner(double *P, uint n, double x) {
    double r;
    uint i;
    r = P[n];
    for(i = n-1; i >= 0; i--) {
        r = r * x * P[i];
    }
    return r;
}
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        r = tmp + P[i];
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#### Compute error terms and accumulate them

- ▶ For each  $s \in \mathscr{S}$ :
  - replace floating-point operations by EFTs,
  - and accumulate errors (inherited, generated),

with the following algorithms:

 $\frac{\text{AutoComp_TwoSum}\langle a, \delta_a \rangle, \langle b, \delta_b \rangle}{[s, \delta_+] = \text{TwoSum}(a, b)}$ 

 $\delta_s \leftarrow \mathsf{RN}((\delta_a + \delta_b) + \delta_+)$ return  $\langle s, \delta_s \rangle$ 

AutoComp\_TwoProduct $\langle a, \delta_a \rangle, \langle b, \delta_b \rangle$ 

 $\begin{array}{l} [s, \delta_{\times}] = \texttt{TwoProduct}(a, b) \\ \delta_{s} \leftarrow \mathsf{RN}(((a \times \delta_{b}) + (b \times \delta_{a})) + \delta_{\times}) \\ \texttt{return} \ \langle s, \delta_{s} \rangle \end{array}$ 

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double						
Horner(double *P, wint n, double x) {						
double r, tmp, d_tmp, d_r, c, rh, rl,						
xh, xl, d_2p;						
uint i;						
$\mathbf{r} = \mathbf{P}[\mathbf{n}];$						
$a_r = 0.0;$ for (i = n-1; i >= 0; i) f						
tmp = r * x:						
c = r * 134217729;						
rh = c - (c - r);						
rl = r - rh;						
c = x * 134217729;						
xh = c - (c - x);						
$x_1 = x - x_1;$ $d_{2n} = n + n + n + n + n + n + n + n + n + n$						
-rl * xh) - rh * xl);						
$d_{tmp} = d_{2p} + d_{r} * x;$						
r = tmp + P[i];						
}						
return r;						
1						
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<pre>double Hornar(double *P, uint n, double x) { double r, tmp, d_tmp, d_r, c, rh, rl, xh, xl, d_2p; uint i;</pre>
$ r = P[n];  d_r r = 0.0;  for(i = n-1; i >= 0; i) {  tmp = r + x;  c = r + 134217729;  rh = c - (c - r);  rl = r - rh;  rn = rh; r$
<pre>c = x * 13421//29; xh = c - (c - x); xl = x - xh; d_2p = rl * xl - (((x - rh * xh)</pre>
return r; }

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```
Horner(double *P, uint n, double x) {
  double r, tmp, d_tmp, d_r, c, rh, rl,
    xh, xl, d_2p, u, d_2s;
  uint i:
  r = P[n];
  d r = 0.0;
  for (i = n-1; i \ge 0; i--) f
    tmp = r * x:
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    xl = x - xh:
    d_2p = rl * xl - (((x - rh * xh)))
               - rl * xh) - rh * xl);
    d_{tmp} = d_{2p} + d_{r} * x;
    r = tmp + P[i];
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    d_2s = (tmp - (r - u)) + (P[i] - u);
    d_r = d_{2s} + d_{tmp};
  return r:
3
```

Benefit from the good ILP of compensation automatically: close sequences (error compensation)

#### Compensate errors: close sequences

For each  $s \in \mathscr{S}$ , close(s) means computing

 $n \leftarrow \mathsf{RN}(n + \delta_n)$ 

for *n* being a result of *s* 

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 return r + d_r;
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Experimental results: case studies from compared works

Our method results compared to existing double-double expansions and compensated ones

1. Sum2 for the recursive summation of n values [ROO05]

Data	# values	condition number	Data	# values	condition number
$d_1$	$32  imes 10^4$	10 <sup>8</sup>	$d_4$	$32  imes 10^4$	$10^{16}$
$d_2$	$32  imes 10^5$	10 <sup>8</sup>	$d_5$	$32 imes10^5$	10 <sup>16</sup>
d <sub>3</sub>	$32 imes10^6$	108	$d_6$	$32 imes10^6$	10 <sup>16</sup>

- 2. CompHorner [GLL09] and CompHornerDer [JGH13] for Horner's evaluation of  $p_H(x) = (x 0.75)^5(x 1)^{11}$  and its derivative
- 3. CompdeCasteljau and CompdeCasteljauDer [JLCS10] for evaluating  $p_D(x) = (x 0.75)^7(x 1)$  and its derivative, written in the Bernstein basis (deCasteljau's scheme)
- 4. CompClenshawI and CompClenshawII [JBL11] for evaluating  $p_C(x) = (x 0.75)^7 (x 1)^{10}$  written in the Chebyshev basis (Clenshaw's scheme)

# Experimental results: performance comparison

Our method results compared to existing double-double expansions and compensated ones



- Compensated algorithms, Comp (manualy implemented), and AC (automatically generated) present similar performance
- Comp and AC algorithms present more ILP than DD (double-double) ones

 $\hookrightarrow$  measurments done with papi tool (API of performance counters) on an intel Core i5, 2.53GHz over Linux kernel 3.2 and gcc4.6.3 -02  $\hookrightarrow$  ideal measurements (not shown here) validate experimental measures

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## Experimental results: accuracy comparison

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 Compensated algorithms, Comp (manualy implemented), and AC (automatically generated) present similar accuracy

 $\hookrightarrow$  DD (double-double) algorithms are more accurate when data are too ill-conditioned (three leftmost cases)

# Conclusions and perspectives

## Summary

- A new method for automatically compensating the FP error of the computations
- ► Similar results to those of manual implementations of compensated algorithms → better accuracy and ILP exposure

## Perspectives

- **Support**  $\div$ ,  $\sqrt{\phantom{a}}$  and elementary functions
- Support all C and validate our approach on other case studies
- Unbounded compensation: SumK, HornerK
- Integrate of our code transformation into gcc

 $\hookrightarrow$  ask me if you want to see a demonstration of our actual prototype (CoHD)

#### Related works

- 6 months of knowledge transfer in a startup
- ► First step toward multi-criteria optimization (accuracy and execution time) → https://hal.archives-ouvertes.fr/hal-01157509

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#### References

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