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# Experimental Validation of a Sampled-Data Passivity-Based Controller for Coordination of Converters in a Fuel Cell System

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**Abstract**—This paper deals with the control of a hybrid source combining a proton exchange membrane fuel cell and supercapacitors. To achieve this objective, an interconnection and damping assignment passivity-based control in a sampled-data context is implemented. This paper first details with this new controller and shows that it ensures the stabilization of the hybrid system at its desired equilibrium point under large range of sampling periods. These considerations are followed by a detailed discussion on experimental results achieved on a 1.2-kW test bench.

**Index Terms**—Experimentation, fuel cell (FC), interconnection and damping assignment passivity-based control (IDA-PBC) methodology, port-controlled Hamiltonian systems, power management, sampled-data controller, supercapacitors.

## I. INTRODUCTION

CONSIDERATION of atmospheric pollution becomes a significant public health problem in industrializing countries as their automotive market develops. Hence, in these countries and in advanced industrial countries, electric vehicles are beginning to play a key role to comply with environmental

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standards, which are becoming more stringent. To develop environmentally friendly vehicles compatible with the current requirements concerning automotive sector, the combination of hydrogen tank and fuel cell (FC) is a promising path. It ensures no local emission, the traction power, and a high cruising range. To improve this solution energy efficiency, a bidirectional storage device has to be added: it enables to recover braking energy and assist the FC during rapid demand power changes, which significantly increases the FC lifetime [1]–[16]. For this function, supercapacitors (SCs) are good candidates since they have a high power density and are able to produce a large number of charge/discharge cycles.

Many power structures can combine FC and SC power sources, as mentioned in [17]. The one based on the double-converter structure [18] enables both energy management and bus voltage regulation, which is particularly important for some applications such as electric vehicles. This paper is based on this structure. The power demand can be supplied by both sources. The SCs can harvest the load regenerative power and the FC transient excess power delivery. Indeed, for life span reasons, FC has to avoid idle functioning or sharp power changes.

Power management of this system relies on frequency decoupling. The controller has to meet three main objectives: first, respect the FC dynamic by limiting FC power slope regardless of load demand; second, comply with the load demand; and third, control the SC charge. Among the many techniques that are available for this application (see, for instance, [19], for a short bibliography), passivity-based controllers are attractive because they guarantee stability properties [20]. The practical implementation is actually based on a microcontroller, and hence depends on signals sampling and zero-order hold (ZOH) devices. It consequently involves discrete theory. In order to best approximate a pure continuous controller (which is called emulation process in a sampled-data context), the implementation may use either very small sampling period or a set of low-value gains [19], [21]. Both cases do not offer optimal solutions. The first case requires a microcontroller with high performances (fast analog-to-digital conversion and a high clock frequency of the control unit), whereas the second leads to poor hybrid system performance with respect to selected criteria. The aim of this paper is to prove that a pure digital passivity-based controller can fulfill the hybrid source requirement, although the imposed sampling frequency of the control process is quite low.

Motivated by a recent theoretical result in [22], this study builds on a previous theoretical study, which demonstrates that a direct sampled-data controller based on the interconnection and damping assignment passivity [interconnection and damping assignment passivity-based control (IDA-PBC)] theory can successfully achieve stabilization without any overcurrent [23] under large sampling time.

The main motivation of this work is the design of a controller that:

- 1) ensures the stability of the whole system;
- 2) generalizes regular linear controller such as PI controller (see, for instance, [19, eqs. (12) and (13)], which could be useful);
- 3) integrates the knowledge of the system to reproduce regular controllers;
- 4) allows large sampling time without degrading the performances.

Items 1–3 are detailed in [19] for the same power architecture based on the double-converter structure, whereas item 4 is addressed in this paper. As a result, the four items have been followed and carried out leading to a complete procedure for designing a controller enabling the real-time coordination of many converters. The latter has been implemented on a test bench with two sources and a load and compared with the analog passive approach of [19].

This paper is organized as follows. In Section II, the modeling of hybrid source and the background of continuous IDA-PBC theory are recalled (see [19] for more details). Section III details the proposed sampled-data passivity-based controller. Section IV shows and discusses the actual experimental results achieved on a reduced-scale test bench based on a 1.2-kW Nexa Ballard FC and compared it with the analog passive approach of [19]. Section V provides concluding remarks.

## II. SYSTEM ARCHITECTURE

### A. Problem Settlement

The hybrid source consists of a low-transient unidirectional power flow source (FC) and a fast-transient bidirectional power flow source (SC) interconnected by a two-converter structure proposed in [18], [19], and [23] and represented in Fig. 1. Such architecture permits the control scheme to properly manage the different powers. The control of the SCs converter has to regulate the dc bus voltage at a constant value equal to its reference  $v_b^*$ , whereas the control of the FC converter has to drive the SCs state of charge toward its constant target value. To work properly, the control scheme is divided in three subblocks.

- A digital nonlinear controller (outer loop) controls the dc bus voltage and the charge of SCs by computing current references  $i_{fc}^*$  and  $i_{sc}^*$  values. Saturations ensure that their values do not exceed  $[0; i_{fc \max}]$  and  $[-i_{sc \max}; +i_{sc \max}]$ , which are defined in order to protect FC, SCs, and converters.
- Two fast inner proportional–integral (PI) current loops featuring antiwindup compensation schemes ensure that whatever load requirements,  $i_{fc}$  and  $i_{sc}$  go rapidly to their reference values  $i_{fc}^*$  and  $i_{sc}^*$ , respectively, which are

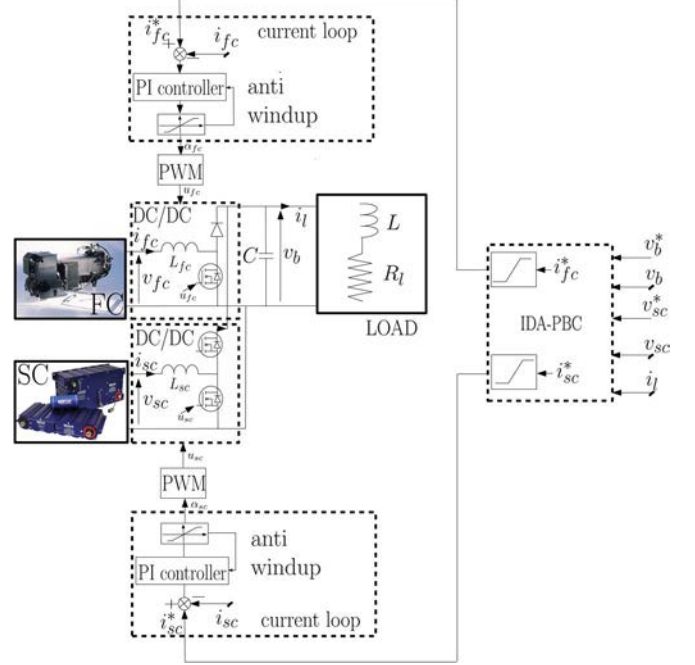


Fig. 1. Studied system with its associated control scheme.

imposed by the outer loop, which is the digital IDA-PBC controller in this study. The outputs of both PI controllers define the duty cycles  $\alpha_{fc}$  and  $\alpha_{sc}$  of the two converters.

The complete “fuel cell—supercapacitors” system with the associated control scheme is represented in Fig. 1. The whole system is described by a fifth-order nonlinear state-space model called *singular perturbed system* [24], because of the timescale difference between the voltages and the currents. Consequently, assuming that currents  $i_{fc}$  and  $i_{sc}$  are instantaneous compared with voltages  $v_b$  and  $v_{sc}$ , the fifth-order differential system degenerates to a third-order differential system as follows [19]:

$$\begin{aligned} \frac{d}{dt} v_b(t) &= \frac{1}{C} \left( \frac{v_{fc}(t)}{v_b(t)} i_{fc}^*(t) + \frac{v_{sc}(t)}{v_b(t)} i_{sc}^*(t) - i_l(t) \right) \\ \frac{d}{dt} v_{sc}(t) &= -\frac{i_{sc}^*(t)}{C_{sc}} \\ \frac{d}{dt} i_l(t) &= \frac{-R_l(t) i_l(t) + v_b(t)}{L} \end{aligned} \quad (1)$$

with  $x_r(t) = [x_1(t); x_2(t); x_3(t)]^T = [v_b(t); v_{sc}(t); i_l(t)]^T$ , control reference inputs  $u_r(t) = [i_{fc}^*(t); i_{sc}^*(t)]^T$ , and measures  $y_r(t)$  and  $z_r(t)$ , where  $y_r(t) = [v_b(t); v_{sc}(t); i_l(t)]^T$ , and  $z_r(t) = [i_{fc}(t); i_{sc}(t); v_{fc}(t)]^T$ .

*Remark 1:* The charge has been considered as a  $R_l L$  load in order to represent the current as a time-varying profile without abrupt change by a proper value of the inductance  $L$ .

### B. IDA-PBC Outer-Loop Controller Design

The IDA-PBC needs to handle the state  $x_r(t)$  to the reference equilibrium point  $x_r^*(t) = [v_b^*(t); v_{sc}^*(t); v_b^*(t)/R_l(t)]^T$ , with  $v_b^*(t)$  and  $v_{sc}^*(t)$  being the dc bus and SCs reference voltages, respectively.

A storage energy function  $H_d$  is defined as  $H_d(\tilde{x}_r(t)) = (1/2)\tilde{x}_r(t)^T Q \tilde{x}_r(t)$  with  $\tilde{x}_r(t) = x_r(t) - x_r^*(t)$  and  $Q = \text{diag}(C, C_{sc}, L)$ , so that this function is equal to zero at the equilibrium point  $x_r^*(t)$ . In this context, the dynamical error equation is as follows:

$$\dot{\tilde{x}}_r(t) = [\mathcal{J} - \mathcal{R}] \nabla H_d(\tilde{x}_r(t)) + g(x_r(t)) u_r(t) + d(t) \quad (2)$$

$$y_r(t) = g^T(x_r(t)) \nabla H_d(\tilde{x}_r(t)) \quad (3)$$

with

$$\begin{aligned} \mathcal{J} - \mathcal{R} &= \begin{bmatrix} 0 & 0 & -\frac{1}{LC} \\ 0 & 0 & 0 \\ \frac{1}{LC} & 0 & -\frac{R_l}{L^2} \end{bmatrix} \\ \nabla H_d(\tilde{x}_r(t)) &= [C\tilde{v}_b(t) \quad C_{sc}\tilde{v}_{sc}(t) \quad L\tilde{i}_l(t)]^t \\ g(x_r(t)) &= \begin{bmatrix} \frac{v_{fc}(t)}{Cv_b(t)} & \frac{v_{sc}(t)}{Cv_b(t)} \\ 0 & -\frac{1}{C_{sc}} \\ 0 & 0 \end{bmatrix} \\ d(t) &= \begin{bmatrix} -\frac{i_l^*(t)}{C} \\ 0 \\ 0 \end{bmatrix}. \end{aligned}$$

An algebraic equation needs to be solved in order to obtain the controller, with the constraint of skew symmetry and positive semidefiniteness of  $\mathcal{J}_d$  and  $\mathcal{R}_d(x_r(t))$ , respectively [20], i.e.,

$$\mathcal{J}_d = \begin{bmatrix} 0 & J_{12} & J_{13} \\ -J_{12} & 0 & J_{23} \\ -J_{13} & -J_{23} & 0 \end{bmatrix} \quad (4)$$

$$\mathcal{R}_d(x_r(t)) = \begin{bmatrix} r_1 & 0 & 0 \\ 0 & r_2 & 0 \\ 0 & 0 & r_3 \end{bmatrix} \quad (5)$$

leading to some solutions [19]. It follows that setting up  $r_1 = x_2\alpha/x_1C^2$ ,  $r_2 = 0$ ,  $r_3 = R_l/L_l^2$ ,  $J_{12} = -(\alpha/CC_{sc})$ ,  $J_{13} = -(1/CL)$ , and  $J_{23} = 0$  with  $\alpha > 0$  leads to the nonlinear control law (see [19] for more design details) that matches regular controllers [25] based on PI controller, i.e.,

$$i_{fc}^* = \frac{v_b}{\max\{v_{fc}, v_{fc \min}\}} \left( \frac{v_b^*}{R_l} - \alpha\tilde{v}_{sc} \right), \quad \alpha > 0 \quad (6)$$

$$i_{sc}^* = -\alpha\tilde{v}_b. \quad (7)$$

Finally, the closed-loop system error is as follows:

$$\dot{\tilde{x}}_r(t) = [\mathcal{J}_d - \mathcal{R}_d(x_r(t))] \nabla H_d(\tilde{x}_r(t)) \quad (8)$$

with

$$\begin{aligned} \mathcal{J}_d &= \begin{bmatrix} 0 & -\frac{\alpha}{CC_{sc}} & -\frac{1}{LC} \\ \frac{\alpha}{CC_{sc}} & 0 & 0 \\ \frac{1}{LC} & 0 & 0 \end{bmatrix} \\ \mathcal{R}_d(x_r(t)) &= \begin{bmatrix} \frac{\alpha v_{sc}}{C^2 v_b} & 0 & 0 \\ 0 & 0 & 0 \\ 0 & 0 & \frac{R_l}{L^2} \end{bmatrix}. \end{aligned}$$

*Remark 2:* In [19], it has been shown that the outer closed-loop system is globally asymptotically stable. Moreover, according to Tikhonov's theorem [26], the whole system is locally asymptotically stable (see [19] for more details).

### C. Practical Issues and Implementation

Equation (6) shows that FC current reference  $u_{r1} = i_{fc}^*$  is highly dependent on the load conductance. Consequently, the IDA-PBC would guarantee the dc bus voltage control by an estimation of the charge  $R_l$ . The proposed solution consists in the implementation of the following algorithm:

$$\hat{Y}_l = \frac{K_{Rl}}{s + K_{Rl}} \frac{i_l}{v_b} \quad (9)$$

$$i_{fc}^*(t) = \frac{v_b(t) \left( v_b^*(t) \hat{Y}_l(t) - \alpha\tilde{v}_{sc}(t) \right)}{\max\{v_{fc}(t), v_{fc \min}\}} \quad (10)$$

$$i_{sc}^*(t) = -\alpha\tilde{v}_b(t) \quad (11)$$

where an estimate of the conductance  $Y_l = 1/R_l$  is implemented in order to avoid a division that is more computationally expensive than a multiplication. Gain  $K_{Rl}$  enables to adjust the sensitivity of control value  $u_{r1}(t) = i_{fc}^*(t)$  to  $R_l$ . Choosing a small parameter value ensures slow  $i_{fc}^*(t)$  variations even in the case of large  $R_l(t)$  changes. In such a situation, strong variations of  $i_{sc}(t)$  ensure a good control of the bus voltage.

*Remark 3:* It could be noticed that the proposed controller needs an increase of sensors in order to measure the load current  $i_l$  compared with regular controller proposed in the literature. Nevertheless, in industrial applications, such current measurement could be useful to protect the equipment and ensure its reliability.

*Remark 4:* Stability analysis with a load estimator. The outer-loop voltage control composed of the third-order system (1), the load estimator, and the IDA-PBC is globally asymptotically stable, invoking a theorem on stability of cascaded systems (see [19] for more details).

*Remark 5:* The two sources have current and voltage limits. Consequently, saturation is included in the algorithm, as in [25].

*Remark 6:* The tuning of nonlinear controllers such as IDA-PBC is not obvious and trivial. To analyze the influence of the tuning parameters on the closed-loop system, more specifically on the FC current dynamics, some simulations have been carried out. After trials and errors, a reasonable choice for the two adjusting parameters  $\alpha$  and  $K_r$  is (10,0.5). It ensures that the dynamic of the FC current is less than 4 A/s so that no significant impact on the ageing FC could be noticed [27]. It follows that the closed-loop bandwidths of the dc bus and SCs have been roughly evaluated nearly 1.6 and 0.25 Hz, respectively, ensuring the frequency decoupling of the two sources.

Specifically, the controller [see (9)–(11)] is implemented with a microcontroller, which involves sampled-data inputs  $v_b[k]$  and  $v_{fc}[k]$ , and control values  $i_{fc}^*[k]$  and  $i_{sc}^*[k]$  remained

constant between two consecutive sampling times by means of a ZOH device. The controller is

$$\hat{Y}_l[k] = e^{-K_{Rl}T_s} \hat{Y}_l[k-1] + (1 - e^{-K_{Rl}T_s}) \frac{\dot{y}_l[k]}{v_b[k]} \quad (12)$$

$$i_{fc}^*[k] = \frac{v_b[k] \left( v_b^*[k] \hat{Y}_l[k] - \alpha \tilde{v}_{sc}[k] \right)}{\max\{v_{fc}[k], v_{fc \min}\}} \quad (13)$$

$$i_{sc}^*[k] = -\alpha \tilde{v}_b[k] \quad (14)$$

where  $T_s$  is the sampling period of the outer loop.

### III. SAMPLED-DATA IDA-PBC

#### A. Introduction

The passivity-based control technique developed in [28] has been further enhanced with the IDA-PBC methodology (see [29] and the references therein). Nevertheless, properties of the continuous controller are usually lost under sampling, leading to a decrement of the controller closed-loop performances. As a result, a direct sampled-data design, permitting the design of digital IDA-PBC in a quite systematic way, is more appropriate [22]. The sampled-data controller is designed in such a way that the energetic behavior of the sampled closed-loop system is equal to that of the continuous one at the sampling instant.

#### B. Sampled-Data Conception

The analysis of the interconnection and dissipation matrices shows that  $\tilde{v}_b$  and  $\tilde{v}_{sc}$  are connected by the two inputs ( $i_{fc}^*$  and  $i_{sc}^*$ ) and ensure the stabilization of the continuous closed-loop system at the desired equilibrium point  $x_r^*$  with a good transient response. Therefore, the digital controller must preserve this energetic behavior by an appropriate design method.

Let us denote  $\tilde{x}_r(t)$  and  $u_r(t)$  the closed-loop continuous-time state and the control input, respectively, as detailed in Section II. The sampled state under control  $u_r[k]$  is denoted, at the time instant  $t = kT_s$ , by  $\tilde{x}_r[k]$ . As stated in [22], the main idea is to design a sampled-data controller that reproduces the energetic behavior of the Hamiltonian function at the sample instants in order to guarantee the stability of the equivalent sampled-time closed-loop system.

Given a Hamiltonian function  $H_d : \mathbb{R}^n \implies \mathbb{R}$ ,  $H_d(0) = 0$ , and a continuous input  $u_r(x_r(t))$  that ensures  $\dot{H}_d(\tilde{x}_r(t)) \leq 0$ , it is desired to design a constant piecewise control  $u_r[k]$  to ensure, at the sampling instants

$$H_d(\tilde{x}_r[k+1]) - H_d(\tilde{x}_r[k]) = \int_{kT_s}^{(k+1)T_s} \dot{H}_d(\tilde{x}_r(\tau)) d\tau \quad (15)$$

with

$$\dot{H}_d(\tilde{x}_r(\tau)) = L_{f(\cdot)} H_d|_{\tilde{x}_r(\tau), t=\tau} \quad (16)$$

where  $L$  is the usual Lie derivative, and  $f(\cdot) = (\mathcal{J} - \mathcal{R})Q\tilde{x}_r(t) + g(\tilde{x}_r(t) + x_r^*(t))u_r(t) + d(t)$ .

The evolution of  $H_d$  in (16) reads

$$H_d(\tilde{x}_r((k+1)T_s)) = e^{T_s \mathcal{L}_{f(\cdot)}} H_d(\cdot)|_{\tilde{x}_r = \tilde{x}_r(kT_s), kT_s}. \quad (17)$$

Similarly, the evolution  $\tilde{x}_r(\tau)$  of the continuous closed-loop system is defined as

$$\tilde{x}_r(\tau) = e^{(\tau - kT_s) \mathcal{L}_{f(\cdot)}} \tilde{x}_r|_{\tilde{x}_r = \tilde{x}_r(kT_s), kT_s} \quad (18)$$

where  $\mathcal{L}_{f(\cdot)}$  is the Lie-Backlund derivative, which is defined according to the following recall.

**Recall:** [30] The first Lie-Backlund derivatives of any function  $\Gamma(x, t)$  with respect to any vector field  $\zeta(x, t)$  is defined as

$$\mathcal{L}_{\Gamma(x,t)} \zeta(x, t) = \frac{\partial \zeta(x, t)}{\partial x} \Gamma + \frac{\partial \zeta(x, t)}{\partial t}. \quad (19)$$

Iteratively, one has

$$\mathcal{L}_{\Gamma(x,t)}^{j+1} \zeta(x, t) = \mathcal{L}_{\Gamma(x,t)} \left( \mathcal{L}_{\Gamma(x,t)}^j \zeta(x, t) \right). \quad (20)$$

Now, considering the control  $u_r(t) = u_r[k]$ , the equilibrium point  $x_r^*(t) = x_r^*[k]$ , and the perturbation  $d(t) = d[k]$  constants on the sampling interval, one obtains in (17)

$$H_d(\tilde{x}_r[k+1]) = e^{T_s \mathcal{L}_{\tilde{f}(\cdot)}} H_d(\cdot)|_{\tilde{x}_r[k], k} \quad (21)$$

where  $\mathcal{L}$  is replaced by the usual Lie derivative  $L$  (all the derivatives with respect to  $t$  are equal to zero), i.e.,  $\tilde{f}(\cdot) = (\mathcal{J} - \mathcal{R})Q\tilde{x}_r(t) + g(\tilde{x}_r(t) + x_r^*[k])u_r[k] + d[k]$ .

In order to design the enhanced sampled controller, we look for a controller defined as follows:

$$u_r[k] = u_{r0}[k] + \sum_{i \geq 1} \frac{T_s}{(i+1)!} u_{ri}[k] \quad (22)$$

such that (16) holds when  $x_r[k] = x_r(t)|_{t=kT_s}$ .

It follows that, by equating homogeneous terms in powers of  $T_s$  in the right- and left-hand sides of (16), one iteratively computes each ‘‘corrective’’ term  $u_{ri}$  of the sampled-data controller, thus obtaining the following first terms:

$$u_{r0}[k] = u_c(t)|_{t=kT_s} \quad (23)$$

$$u_{r1}[k] = \dot{u}_c(t)|_{t=kT_s}. \quad (24)$$

The computation of higher order corrective term (i.e.,  $i \geq 2$ ) is unnecessary in practice. Therefore, an interesting solution can be proposed at the first order of approximation, i.e.,  $u_r[k]^T = [i_{fc}^*[k] \ i_{sc}^*[k]]^T$  with

$$\hat{Y}_l[k] = e^{-K_{Rl}T_s} \hat{Y}_l[k-1] + (1 - e^{-K_{Rl}T_s}) \frac{\dot{y}_l[k]}{v_b[k]} \quad (25)$$

$$i_{fc}^*[k] = \frac{v_b}{\max\{v_{fc}, v_{fc \min}\}} \left( v_b^* \hat{Y}_l - \alpha \tilde{v}_{sc} \right) + \frac{T_s}{2!} \gamma(\cdot) \Big|_{t=kT_s} \quad (26)$$

$$i_{sc}^*[k] = -\alpha \tilde{v}_b + \frac{T_s}{2} \frac{\alpha}{C} \left( \alpha \frac{v_{sc}}{v_b} \tilde{v}_b + \alpha \tilde{v}_{sc} + \tilde{y}_l \right) \Big|_{t=kT_s} \quad (27)$$



with

$$\begin{aligned} \gamma(\cdot) = \dot{u}_c = & -\alpha^2 \frac{v_b^* \tilde{v}_b}{C_{sc} v_{fc}} \\ & - \frac{1}{C_{sc} v_{fc}} \left( \alpha \frac{v_{sc}}{v_b} \tilde{v}_b + \alpha \tilde{v}_{sc} + \tilde{i}_l \right) \\ & \times \left( v_b^* \hat{Y}_l + \alpha \frac{v_{sc}}{v_b} - 2\alpha \tilde{v}_{sc} + \alpha v_{sc}^* \right) \Big|_{t=kT_s}. \end{aligned} \quad (28)$$

As a result, the proposed sampled-data controller  $u_r[k]^T = [i_{fc}^*[k], i_{sc}^*[k]]^T$  computed as in (25)–(28) has better performances compared with the simple emulated controller because it improves to an error in  $O(T_s^3)$  both the energetic performances ensuring convergency to the desired equilibrium (one step matching of the Hamiltonian function) and matching of the state variables evolutions at the sampling instants. These performances are discussed and well illustrated in Section IV through real implementation tests.

However, due to the interplay between the sampling period length and how far one is from the equilibrium, one cannot theoretically prove asymptotic convergency under approximate digital controller. It is reasonable to conjecture that, as usual, in such a context, some practical stability such as condition holds at the sampling instants.

### C. Practical Implementation

In practice, the controller leads to a flat variation of the FC current. Hence, the corrective input  $\gamma(v_b, v_{sc}, i_l, Y_l) = \dot{u}_c$  is rather low, thus possible to neglect without compromising on the performances. Conversely, the  $i_{sc}^*$  corrective term may produce high values. This latter becomes important in case of large sampling period.

Finally, the practical proposed sampled-data controller  $u_r[k]^T = [i_{fc}^*[k], i_{sc}^*[k]]^T$  is defined as follows:

$$\hat{Y}_l[k] = e^{-K_{Rl} T_s} \hat{Y}_l[k-1] + (1 - e^{-K_{Rl} T_s}) \frac{i_l[k]}{v_b[k]} \quad (29)$$

$$i_{fc}^*[k] = \frac{v_b}{\max\{v_{fc}, v_{fc \min}\}} \left( v_b^* \hat{Y}_l - \alpha \tilde{v}_{sc} \right) \Big|_{t=kT_s} \quad (30)$$

$$i_{sc}^*[k] = -\alpha \tilde{v}_b + \frac{T_s}{2} \frac{\alpha}{C} \left( \alpha \frac{v_{sc}}{v_b} \tilde{v}_b + \alpha \tilde{v}_{sc} + \tilde{i}_l \right) \Big|_{t=kT_s}. \quad (31)$$

## IV. EXPERIMENTAL RESULTS

### A. Test Bench Description

The experimental setup (see Table I) is based on a Nexa Ballard FC (46 A/1200 W) associated with two Maxwell SC modules in series (125 F). The FC and SC boost converters are based on metal–oxide–semiconductor field-effect transistor modules switching at 20 kHz. The load is emulated by a programmable electronic equipment from Höcherl & Hackl (model ZS1806, rated power of 1800 W,  $i_{max} = 150$  A,  $V_{max} = 60$  V). Finally, the controllers are implemented in a dSPACE DS1103 board where the voltage outer control loops sampling has been set to 20 kHz or 500 Hz. The current closed loops are based on PI controller updated at 20 kHz [17], [19].

TABLE I  
ELECTRIC CHARACTERISTICS OF THE HYBRID SYSTEM

Fuel cell parameters			
Open circuit voltage E	45 V	$v_{fc \min}$	26 V
Rated voltage	26 V	Rated current	46 A
Supercapacitors parameters			
Capacitance	125 F	$v_{sc}^*$	21 V
Rated voltage	30 V	Rated current	200 A
Electric load parameters			
Rated voltage	60 V	Rated current	150 A
Rated power	1800 W		
Inductance and capacity parameters			
$L_{fc}$ inductance	200 $\mu$ H	$L_{sc}$ inductance	100 $\mu$ H
Rated current $L_{fc}$	100 A	Rated current $L_{sc}$	150 A
Capacity $C$	9 mF	Inductance $L$	1 mH
$v_b^*$	50 V		
Control parameters			
$Kp_{fc}$	0.030	$Ki_{fc}$	30
$Kp_{sc}$	0.030	$Ki_{sc}$	30
$\alpha$	10	$K_{Rl}$	0.5

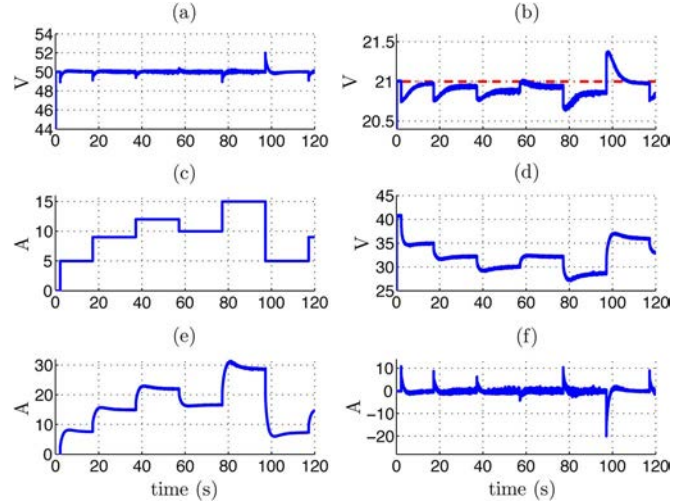


Fig. 2. Experimental result of the emulated controller— $T_s = 50 \mu$ s. (a) bus voltage  $v_b$ . (b) SCs voltage  $v_{sc}$ . (c) load current  $i_l$ . (d) fuel cell voltage  $v_{fc}$ . (e) fuel cell current  $i_{fc}$ . (f) SCs current  $i_{sc}$ .

### B. Result Analysis

All the experimental results detailed thereafter have been performed in order to validate the outer-loop control algorithm. Hence, in order to highlight the performances of the new digital controller, a comparison between the emulated controller based on the continuous-time design [implementation of the continuous IDA-PBC with a ZOH device, (12)–(14)] and the sampled-data controller (29)–(31) are detailed for different sampling periods.

Figs. 2 and 3 show the responses of the emulated controller for sampling periods equal to 50  $\mu$ s and 2 ms, respectively, where the dc bus voltage reference is set equal to 50 V, and the load current varies between 0 and 15 A (this is equivalent to a load admittance variation from 0 to 0.3 S).

First, all the abrupt variations of the power load at time 1, 16, 36, 76, or 116 s have been balanced by an abrupt variation of the SC current [see Figs. 2(f) and 3(f)], ensuring that the dc bus reference voltage is reached [see Figs. 2(a) and 3(a)]. On the contrary, any load power decrease (at  $t = 56$  or 96 s) leads to

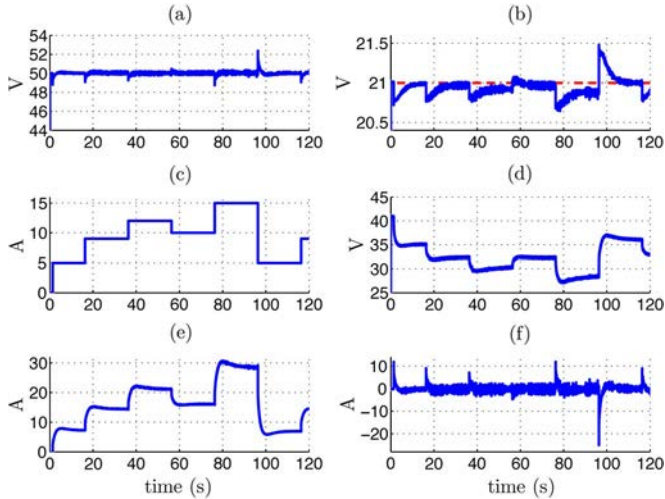


Fig. 3. Experimental result of the emulated controller— $T_s = 2$  ms. (a) bus voltage  $v_b$ . (b) SCs voltage  $v_{sc}$ . (c) load current  $i_l$ . (d) fuel cell voltage  $v_{fc}$ . (e) fuel cell current  $i_{fc}$ . (f) SCs current  $i_{sc}$ .

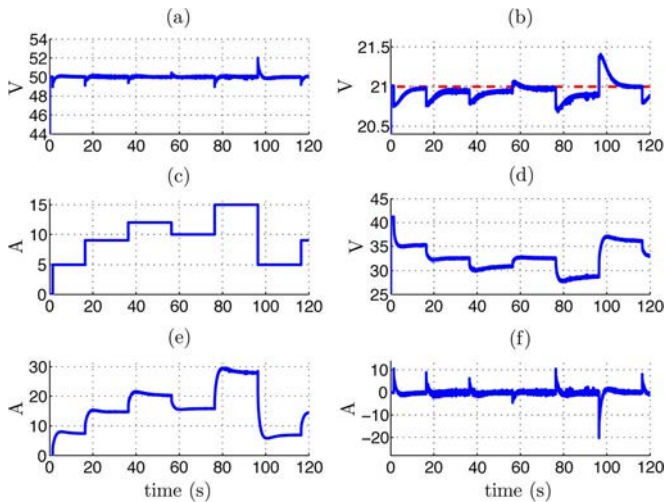


Fig. 4. Experimental result of the sampled-data controller— $T_s = 50 \mu s$ . (a) bus voltage  $v_b$ . (b) SCs voltage  $v_{sc}$ . (c) load current  $i_l$ . (d) fuel cell voltage  $v_{fc}$ . (e) fuel cell current  $i_{fc}$ . (f) SCs current  $i_{sc}$ .

a slow decrease in FC current  $i_{fc}$  and, thereafter, a recharge of the SCs with the excess of energy.

Second, we can note that the SC voltage is not perfectly equal to its reference at steady state. In fact, the controller design supposes that the converters are lossless (no perturbation) and all the parameters are well known. Therefore, in practice, this small steady-state error could be removed by adding a small integral term without deteriorating the stability [19], [31].

Third, the remaining useful life of the FC is ensured by the smooth variation of the current  $i_{fc}^*$  (adjusted by gain  $K_{RI}$ ) despite rapid power load requests.

Fourth, it can be noticed that the increase in the sampling period amplifies the peak of the SCs' current and the signals' noise.

Figs. 4 and 5 show the responses of the sampled-data controller for sampling periods equal to  $50 \mu s$  and  $2$  ms, respectively. We can notice that the sampled-data controller prevents overshoots of the SC current and the dc bus voltage,

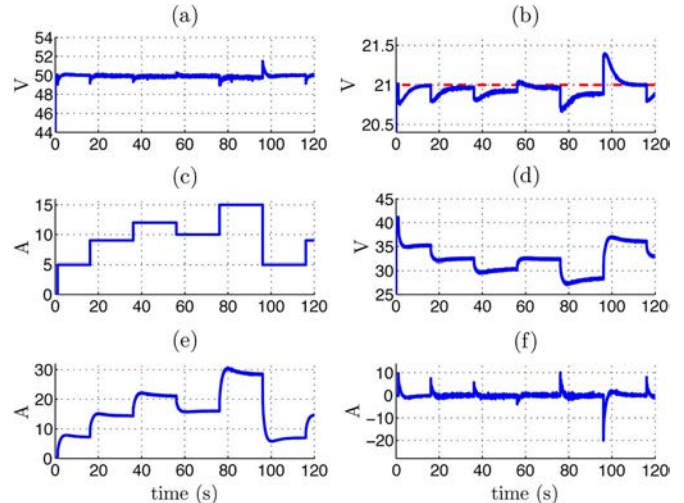


Fig. 5. Experimental result of the sampled-data controller— $T_s = 2$  ms. (a) bus voltage  $v_b$ . (b) SCs voltage  $v_{sc}$ . (c) load current  $i_l$ . (d) fuel cell voltage  $v_{fc}$ . (e) fuel cell current  $i_{fc}$ . (f) SCs current  $i_{sc}$ .

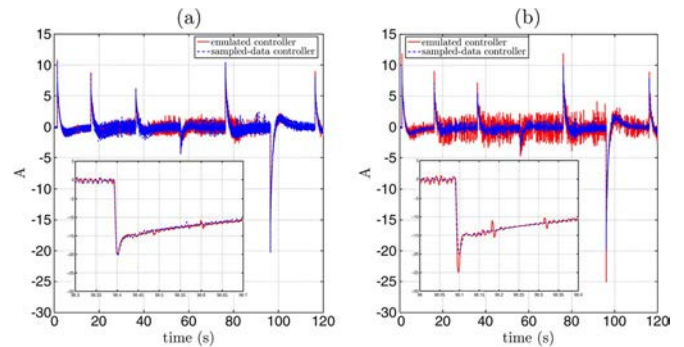


Fig. 6. Experimental result of the emulated and sampled-data controllers. (a) SCs current  $i_{sc} - T_s = 500 \mu s$ . (b) SCs current  $i_{sc} - T_s = 2$  ms.

although the sampling frequency is significantly reduced. In conclusion, the performances are maintained with the sampled-data controller, whereas the emulated algorithm greatly reduces them.

Indeed, for a sampling time equal to  $2$  ms, the emulated controller has an SC peak current  $i_{sc}^{\max}$  at about  $11.85$  A at  $t = 1$  s, as opposed to the sampled-data controller, which only induces a  $9.67$ -A peak. An analog observation can be noticed at  $t = 96$  s during a load power decrease. The emulated controller requests a  $i_{sc}^{\min} = -25$  A, whereas sampled-data controller requests  $i_{sc}^{\min} = -20$  A. These observations are illustrated in Fig. 6. This confirms that the proposed controller is actually effective in a sampled-data context. Using the sampled-data controller leads to very satisfactory performances of the equipment under control, and this is increasingly so when the sampling period is high.

## V. CONCLUSION

Motivated by recent theoretical results from sampled passivity-based control, a new controller for coordination of an FC and SCs has been detailed. Practical results show that the performances of the system under control with an emulated controller have been downgraded when the sampling frequency

has been reduced. By contrast, in the same context, the sampled IDA-PBC has maintained the requested performance. It follows that such controller is suitable for high-power industrial applications where the switching and sampling frequencies are low.

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