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Integrated programmable photonic filter on the silicon-on-insulator platform

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Abstract: We propose and demonstrate a silicon-on-insulator (SOI) on-chip programmable filter based on a four-tap finite impulse response structure. The photonic filter is programmable thanks to amplitude and phase modulation of each tap controlled by thermal heaters. We further demonstrate the tunability of the filter central wavelength, bandwidth and variable passband shape. The tuning range of the central wavelength is at least 42% of the free spectral range. The bandwidth tuning range is at least half of the free spectral range. Our scheme has distinct advantages of compactness, capability for integrating with electronics.

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OCIS codes: (130.7408) Wavelength filtering devices; (120.2440) Filters; (130.3120) Integrated optics devices.

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1. Introduction

Photonic filters are considered one of the key elements in optical communication systems [1, 2] and have attracted significant interest due to their potential for a wide range of applications, including noise suppression, signal quality improvement, message capacity increasing and on-chip networking [3–5]. In particular, programmable filters are critical parts since they can provide enhanced system flexibility [5]. To date, demonstrated tunable photonic filters can be mainly divided into three categories, providing either tunability of the central wavelength, tunability of the bandwidth and tunability of both central wavelength and bandwidth. Central wavelength tunable filters have been achieved by gratings [6], Fabry-Perot spectrometers [7–9], Mach-Zehnder interferometers (MZIs) [10], thin-films [11], acousto-optical structures [12] and liquid crystals [13]. However, their fixed bandwidth limited their applications in wavelength division multiplexing networks with high spectral efficiency. Bandwidth tunable filters have been implemented by microdisks [14], Gires-Tournois interferometers [15] and microrings [16], which can meet the requirements of many signal process applications. With the rapid development of wireless communications and access networks, programmable filters with both central wavelength and bandwidth tunability attract more and more attention nowadays [17–19]. In fact, the capability to tune both the filter central wavelength and bandwidth is not easily to realize [5]. Widely adopted approaches are based on free space optical technology [20] and liquid crystal modulation [21, 22]. These schemes have shown flexibility in reconfiguration and large tuning range, and some of them are commercialized. However such filters are bulky, expensive and unable to be integrated on silicon chips.

In this paper, we theoretically propose and experimentally demonstrate an on-chip programmable filter based on a four-tap finite impulse response (FIR) structure. By thermally controlling the amplitude and phase modulation of each tap, we obtain tunability of both central wavelength and bandwidth of the filter. Besides, three examples of shape-variable filters are also proposed to prove the programmability of our filter. FIR structure, despite of being well known, have not been fabricated on silicon-on-insulator (SOI) platform to our knowledge. Comparing to the filter based on fiber technology, space optical technology and liquid crystal modulation, our scheme has distinct advantages of compactness, small power consumption and capability for integration with electronics [23, 24].

2. Principle

The principle of the filter is based on a four-tap FIR structure. The pattern structure is monolithically integrated on an SOI wafer, with the advantages of easy fabrication and compact footprint. The filter architecture is shown in Fig. 1. The input signal is divided into four taps by cascaded multimode interference (MMI) couplers, and then propagates through the four taps with a series of time delays. An amplitude modulation unit (realized by a Mach-Zehnder interferometer (MZI) with one arm phase-modulated) and a phase modulation unit are present on each tap. All phase modulation units are controlled by thermal electrodes. It is known that the refractive index of silicon varies with temperature, leading to the required change of phase. Thus, heating the waveguides can implement phase modulation. Implementing such a phase modulation unit in one arm of an MZI can further realize amplitude modulation.

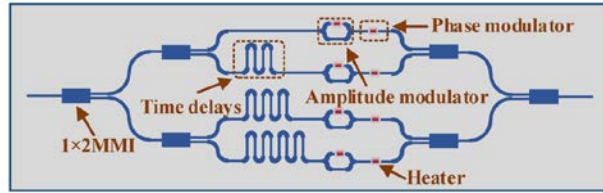


Fig. 1. Schematic diagram of the proposed integrated programmable filter.

Assuming that the amplitudes and phases of the four taps are $\alpha_1, \alpha_2, \alpha_3, \alpha_4$ and $\phi_1, \phi_2, \phi_3, \phi_4$, respectively, and the time delay between two adjacent taps is τ , the transfer function of the optical filter can be expressed as [25]

$$H_0(\omega) = \sum_{n=1}^4 \alpha_n e^{j(n\omega\tau + \phi_n)} \quad (1)$$

where ω is angular frequency of light. Equation (1) indicates that the output spectrum can be reshaped by modifying the relative amplitude weights and phase shifts of the four taps. Assuming that the relative time delay between two consecutive taps is 10 ps, a programmable filter can be obtained with a free spectral range (FSR) of 100 GHz.

2.1. Central wavelength-tunable filter

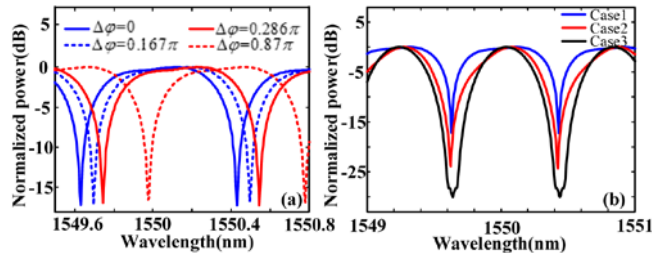


Fig. 2. (a) Simulated transfer functions of the filter with different $\Delta\phi$ values of 0 (blue solid line), 0.167π (blue dotted line), 0.286π (red solid line) and 0.87π (red dotted line), respectively, (b) Simulated transfer functions of the filter with bandwidth tunability. The 3dB bandwidths of case 1, case 2 and case 3 are 0.61 nm, 0.38 nm and 0.31 nm, respectively.

In order to tune the central wavelength of the filter but keep the filter shape unchanged, we can add constant phase shifts between consecutive taps, which is referred to as a complex-coefficient filter. Assuming that the phase difference between two adjacent taps is changed by $\Delta\phi$, then the transfer function of the filter becomes

$$H_{\Delta\varphi}(\omega) = \sum_{n=1}^4 \alpha_n e^{j[n(\omega\tau + \Delta\varphi) + \phi_n]} = \sum_{n=1}^4 \alpha_n e^{j\left[n\tau\left(\omega + \frac{\Delta\varphi}{\tau}\right) + \phi_n\right]} = \sum_{n=1}^4 \alpha_n e^{j[n\omega'\tau + \phi_n]} = H_0(\omega') \quad (2)$$

where $\omega' = \omega + \Delta\varphi/\tau$. From Eq. (2), we can see that the shape of the filter remains the same and that the central frequency of the filter is shifted by $\Delta\varphi/\tau$. Figure 2(a) illustrates the spectral shift for different values of $\Delta\varphi$. For example, the initial amplitude array and phase array for the four taps are set as $\alpha = [0.05, 1, 0.8, 0.225]$ and $\phi = [0.1\pi, 0.9\pi, 0.25\pi, -0.25\pi]$, respectively. When the phase step $\Delta\varphi$ is 0 (blue solid line), 0.167π (blue dotted line), 0.286π (red solid line) and 0.87π (red dotted line), respectively, we can see that the central wavelength has been shifted by 0.065 nm, 0.115 nm and 0.345 nm, respectively. The maximum tuning range of central wavelength can reach the FSR of the filter if $\Delta\varphi = 2\pi$.

2.2. Bandwidth tunable filter

A bandwidth-tunable filter can be achieved by jointly controlling both amplitude and phase arrays for all taps. Figure 2(b) shows an example of the bandwidth tunability by optimizing all amplitudes and phases of all taps. The amplitude array and phase array of the initial condition (named Case 1) are $\alpha = [0.05, 1, 0.8, 0.225]$ and $\phi = [0.1\pi, 0.9\pi, 0.25\pi, -0.25\pi]$, respectively. Then in Case2 we set the amplitude array and phase array to $\alpha = [0.039, 1, 0.63, 0.12]$ and $\phi = [0.86\pi, -0.035\pi, -0.85\pi, 0.67\pi]$, respectively, and in Case3 to $\alpha = [0.27, 0.77, 0.75, 0.25]$ and $\phi = [0.25\pi, -0.85\pi, -1.71\pi, -0.58\pi]$, respectively. As shown in Fig. 2(b), the FSRs of all cases are 0.8 nm i.e., 100GHz, and the 3dB-bandwidths of Case 1, Case 2 and Case 3 are 0.61 nm, 0.38 nm and 0.31 nm, respectively. That is to say, the tuning range of the bandwidth, defined as the maximum bandwidth minus the minimum bandwidth and then divided by FSR, can be at least 37.5% of the FSR, while the central wavelength remains fixed. It should be noted that wider tuning range can be obtained by choosing more amplitude and phase arrays. And when we set the amplitude array and phase array to $\alpha = [1, 1, 1, 1]$ and $\phi = [0, 0, 0, 0]$, the 3dB-bandwidth of our filter is narrowest, i.e., 0.247 nm. So the bandwidth tuning range of our filter is 69.1% of FSR.

2.3. Shape-variable filter

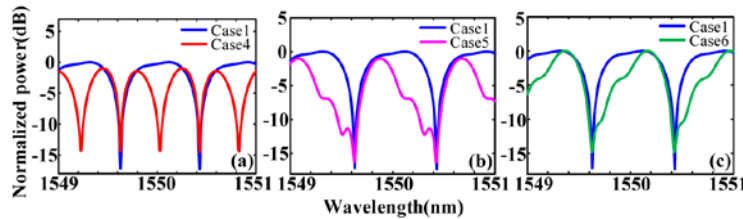


Fig. 3. Simulated transfer function of the filter with variable passband shapes. (a) The FSR of the filter is reduced to half of the original one (the amplitude array and phase array of Case4 are $[0.8, 0.15, 0.8, 0.225]$ and $[\pi, 0.99\pi, 0.207\pi, 0.248\pi]$, respectively), (b) and (c) Ramp-shaped transfer functions (the amplitude arrays and phase arrays of Case5 and Case6 are $[0.6, 0.75, 0.5, 0.25]$, $[-0.167\pi, 0.67\pi, 1.33\pi, 0]$ and $[0.85, 1, 0.5, 0.25]$, $[0.25\pi, -0.455\pi, 0.91\pi, -0.63\pi]$, respectively).

Our filter can be programmed to implement variable passband shapes as well. To fulfil a target filter, one needs to optimize and calculate the amplitude array and phase array of all taps so that the spectral response resembles the target filter as much as possible. For example, Fig. 3 shows some special filter shapes when varying the phase and amplitude arrays. In Fig. 3(a), the FSR of the filter (named as Case 4) becomes half of the original case (Case 1). In Case 4, the amplitude array and phase array are set as $\alpha = [0.8, 0.15, 0.8, 0.225]$ and $\phi = [\pi, 0.99\pi, 0.207\pi, 0.248\pi]$, respectively. In Figs. 3(b) and (c), we design ramp filters with descending and ascending characteristics, respectively. The amplitude and phase arrays

(Case 5 and Case 6) are $\alpha = [0.6, 0.75, 0.5, 0.25]$, $\phi = [-0.167\pi, 0.67\pi, 1.33\pi, 0]$ and $\alpha = [0.85, 1, 0.5, 0.25]$, $\phi = [0.25\pi, -0.455\pi, 0.91\pi, -0.63\pi]$, respectively.

3. Experimental verification

The design layout of the fabricated programmable filter is shown in Fig. 4. The filter was fabricated on an SOI wafer with 250 nm thick top silicon layer and 3 μm thick buried oxide (BOX). The height of the waveguide is 250 nm and the relative time delay of the four taps is 10 ps. The bending radius of the waveguide is 20 μm . The size of our filter is about 2 mm^2 . Fully etched apodized grating couplers [26] were used as input and output ports. A single step of E-beam lithography and inductively coupled plasma reactive ion etching (ICP-RIE) was used to fabricate the grating couplers and silicon waveguides, simultaneously. Then a 700 nm thick silica was deposited on the sample. Another layer of 700 nm bro-phospho-silicate-glass (BPSG) was deposited annealed in nitrogen condition in order to planarize the surface. After that, the top glass layer was thinned to 1 μm by buffered hydrofluoric acid (BHF) etching. Finally, heater patterns (100 nm Ti) were formed by e-beam lithography followed by metal deposition and lift-off.

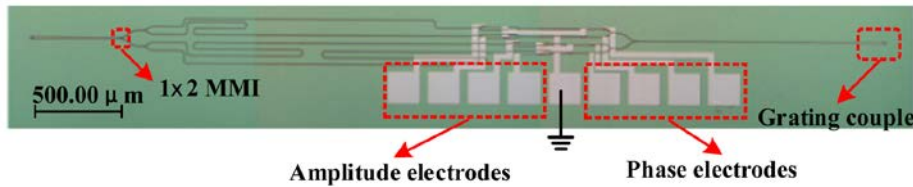


Fig. 4. Metallurgical microscopy image of the programmable filter.

In order to characterize our on-chip filter, light from a broadband optical source was coupled to the silicon chip with a vertical grating coupler. Because of the polarization sensitivity of the couplers and waveguides, a polarization controller (PC) was placed before the input grating coupler. The electrodes of the amplitude and phase arrays were contacted by a probe pin array spaced with 250 μm . Variable voltages generated from independent power supplies were applied to different pins in the array. Finally the output spectrum is measured by a high resolution (0.02 nm) optical spectrum analyzer (YOKOGAWA AQ6370C).

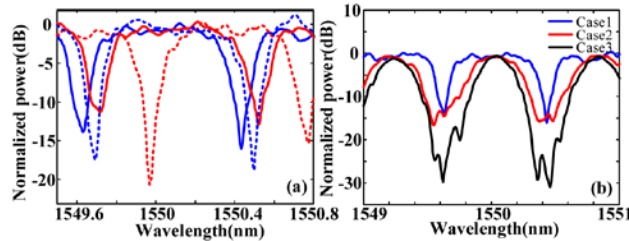


Fig. 5. (a) Measured transfer functions of the filter with different voltages on the phase electrode, which match well with the simulated ones in Fig. 2(a), (b) measured spectra of the filter with bandwidth tunability, the 3dB bandwidth of case1, case2 and case3 are 0.61 nm, 0.34 nm and 0.21 nm, respectively.

The loss of our filter is 9 dB when there is no voltages applied to the electrodes. And the loss of the coupling from the grating to fiber is about 11dB for both sides. Figure 5(a) demonstrates the tunability of the central wavelength of the filter transfer function. First, we adjusted all the voltages applied on the amplitude electrodes and phase electrodes so that the filter shape matches well Case 1. The output spectrum is shown as the blue solid line. The FSR of the filter is about 0.8 nm (i.e., 100 GHz). Then, all the voltages applied to the amplitude electrodes remained fixed, and the voltages applied to the phase electrodes were adjusted. The adjustment criterion is to keep the spectral shape unchanged, but to shift the

resonant wavelength. Finally we observed the wavelength shifts as shown in Fig. 5(a). The spectral shifts are about 0.06 nm, 0.1 nm and 0.34 nm, which match well with the simulated ones in Fig. 2(a).

Figure 5(b) shows measured transfer functions demonstrating bandwidth tunability by adjusting the voltages controlling both amplitude and phase arrays. The measured spectra are in good qualitative agreement with the calculation results shown in Fig. 2(b) for Case1, Case2 and Case3, respectively. The measured 3 dB bandwidths are 0.61 nm, 0.34 nm and 0.21 nm, respectively. We can see the maximum bandwidth tuning range of our filter is 0.4 nm, which is half of the filter's FSR.

In order to demonstrate the variable shape filters, we tune the amplitude array and phase array to synthesize the special transfer function shapes corresponding to Case 4, Case 5 and Case 6. The measured spectra are shown in Fig. 6. In Fig. 6(a) the wavelength separations between adjacent resonant notches are 0.37 nm and 0.43 nm for Case 4 in one FSR, respectively, which has a slight difference from half of FSR (0.4 nm). In Figs. 6(b) and 6(c), we obtain descending and ascending ramp filters, in good agreement with the simulated results in Figs. 2(b) and 2(c), respectively.

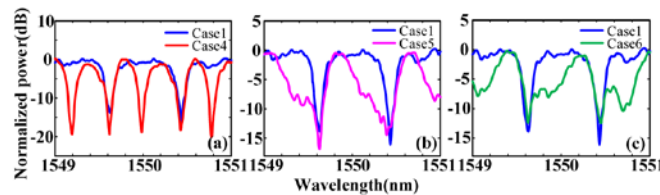


Fig. 6. Measured transfer functions for shape-variable filters. (a) The FSR of the filter is reduced to half of the original one. (b) and (c) Ramp-shaped transfer functions.

The power consumption varies when the filter is in different functions, and the maximum power consumption is about 100 mW. The spectrum of the programmable filter is periodical due to the four-tap FIR structure. The FSR of the filter is inversely proportional to the time delay τ . Thus we can increase the FSR thereafter increase the filter tuning range by decreasing the time delay. For example, when $\tau = 10$ ps, the FSR is 0.8 nm, and the maximum theoretical tuning range of central wavelength is about 0.8 nm. The situation is changed when $\tau = 2.5$ ps. The FSR and tuning range are 3.2 nm, four time as large as in the case of $\tau = 10$ ps. Comparing the measured transfer functions with the simulated ones, some deviations appear, which result from the non-uniform thermal conductivity of each electrode and device fabrication imperfections. To mitigate the impact of thermal non-uniformities, we can separate the thermal electrodes and waveguides over a larger region to prevent unwanted thermal diffusion. Besides, the number of taps influences the filter's characteristics as well. We can obtain more elaborate filter transfer functions by fabricating FIR structures with eight or more taps.

4. Conclusions

We have proposed and demonstrated an on-chip programmable filter based on a four-tap FIR structure. By adjusting the voltages to control the amplitude and phase of taps, we have implemented a filter with central wavelength, bandwidth and passband shape tunability. The central wavelength tunability is realized by adjusting the phase electrodes. The bandwidth tunability and variable passband shape are implemented by adjusting both the amplitude and phase electrodes. Our scheme has distinct advantages of compactness as well as capability for integration with electronics.

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