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Feedback Linearization for the DC Voltage Control of a VSC-HVDC Terminal

Yijing Chen¹, Gilney Damm², Abdelkrim Benchaib³, Françoise Lamnabhi-Lagarrigue¹

Abstract—This paper investigates the use of feedback linearization to develop a control system for regulating the DC voltage and the reactive power of a VSC terminal. Firstly, a direct-quadrature (dq) state-space model of the VSC terminal is established. By analyzing the zero dynamics of the system, the static feedback linearization can be applied when the VSC terminal operates in inversion mode (power flowing from the DC side to the AC one). However, the system is not feedback linearizable when the VSC works in rectification mode (power flowing from the AC side to the DC one). Then, based on a simplified model, a new dynamic is introduced by an additional control variable which yields a higher order system, allowing the terminal to operate in rectification mode. Numerical simulations are carried out to verify the validity of the proposed control system.

I. INTRODUCTION

In the past decades, significant advances have been made in the development of high power devices for Voltage Source Converter based High Voltage Direct Current (VSC-HVDC) systems. These systems are capital to attain the objective of integrating large-scale offshore renewable energy sources. One of the most significant progress was the improvement of high rated transistors, such as insulated gate bipolar transistors (IGBT), that made VSC-HVDC systems operate in a flexible, efficient and reliable way. With these types of semiconductor devices, a second degree of freedom is given since they can be turned both on and off. This additional controllability gives many advantages. It is possible to control the reactive power independently of the active power and power transmission can be bidirectional. The application of Pulse Width Modulation (PWM) [1] improves the harmonic distortion of the converter with less low frequency harmonics and gives a faster response. VSC-HVDC systems have also the potential benefits of transferring power flows to/from a weak AC network such as wind farms and solar plants while improving the stability and the robustness of the overall system [2], [3], [4], [5].

Many studies have been devoted to the control design of VSC-HVDC systems. Traditional control structure involves the field-oriented vector control technique based on several PI controllers [6]. Generally speaking, two PI controllers

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are used to control the dq currents. According to different control objectives, an additional PI controller is dedicated to controlling the DC voltage or the active power. However, there exists a limitation in this conventional control method [7]. Moreover, when the dynamics of the currents and the DC voltage are close to each other, such kind of vector control is not so efficient [8]. Other possibilities have been recently studied. For example, a passivity property of the dynamical system has been exploited in [9]. Though the global stability of the system is ensured, the control strategy in [9] is based on the equilibrium values of the system which need to be calculated in advance and are strongly dependent on parameters and measurements.

In this work, we propose a new control system based on feedback linearization theory, which ensures that the converter can operate well in both rectification mode and inversion mode. The zero dynamics of the system are derived and analyzed in this paper, which is very important to help us understanding the behavior of the system.

This paper is organized as follows. A synchronous dq reference frame based state-space model is introduced in Section II. The control structure is built in Section III where the zero dynamics of the system are analyzed. It is found that the system has the property of being feedback linearizable in inversion mode but not in rectification mode. Then, a control law based on a simplified model is derived for the converter in rectification mode. Simulation results are presented in Section IV. Finally, conclusions are drawn in Section V.

II. MODELING OF A VSC TERMINAL

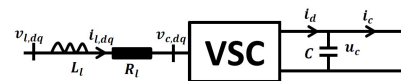


Fig. 1. A simplified configuration of a VSC terminal.

A simplified representation of a VSC terminal is shown in Fig. 1 where a small resistance R_l and a large inductance L_l represent the phase reactor which dominates the dynamics of the converter on the AC side and C is the DC capacitor whose size determines the dynamics on the DC side. $v_{l,dq}$ are the voltages of the AC network, $v_{c,dq}$ are the voltages on the AC side of the converter, $i_{l,dq}$ are the currents through the phase reactor, u_c is the DC voltage across the DC capacitor and i_c is the measurable DC current representing the active power demand or supply from the connected DC grid. By convention, i_c is positive if the power flows from the AC side to the DC side.

A state-space model of the VSC terminal established in a synchronous dq frame is given by [10]:

$$\frac{di_{ld}}{dt} = -\frac{R_l}{L_l}i_{ld} + \omega i_{lq} - \frac{1}{2L_l}M_d u_c + \frac{v_{ld}}{L_l} \quad (1)$$

$$\frac{di_{lq}}{dt} = -\frac{R_l}{L_l}i_{lq} - \omega i_{ld} - \frac{1}{2L_l}M_q u_c + \frac{v_{lq}}{L_l} \quad (2)$$

$$\frac{du_c}{dt} = -\frac{1}{C}i_c + \frac{1}{C}\frac{3}{4}(M_d i_{ld} + M_q i_{lq}) \quad (3)$$

where ω is the angular frequency of the AC network. Pulse-width modulation (PWM) technology is applied to generate the converter voltages $v_{c,dq}$ where M_{dq} are the modulation indices as the control inputs. $v_{c,dq}$ are controlled by M_{dq} :

$$v_{c,dq} = \frac{1}{2}M_{dq}u_c \quad (4)$$

The active and reactive power on the AC side of the converter are expressed as:

$$P_l = \frac{3}{2}v_{ld}i_{ld} \quad (5)$$

$$Q_l = -\frac{3}{2}v_{ld}i_{lq} \quad (6)$$

since the chosen dq reference frame makes the d-axis fixed to the AC area voltage, i.e. $v_{ld} = V_{l,rms}$ and $v_{lq} = 0$.

III. CONTROL STRATEGY

As presented in the state-space model (1)-(3), there are two degrees of freedom. Thus, at least two variables can be controlled. In this paper, u_c and Q_l are chosen to be regulated at their desired values u_c^* and Q_l^* . As seen in (6), controlling Q_l is equivalent to regulating i_{lq} at i_{lq}^* which satisfies

$$i_{lq}^* = -\frac{2Q_l^*}{3v_{ld}} \quad (7)$$

In this paper, the control objective is to make u_c and i_{lq} track their references u_c^* and i_{lq}^* , respectively. An important feature of the VSC terminal is that the system can operate at unity power factor. For simplicity, i_{ld}^* is set to zero.

In this paper, according to the direction of the power flows, two control laws are developed based on feedback linearization theory.

A. Static feedback linearization control

By defining $(x_1 \ x_2 \ x_3) \triangleq (i_{ld} \ i_{lq} \ u_c)$, we rewrite the system (1)-(3) as:

$$\dot{x} = f(x) + g_d M_d + g_q M_q \quad (8)$$

where $f(x)$, g_d and $g_q \in \mathbb{R}^3$ are easily found.

According to the control objective, we first define the outputs as $y_1 = (u_c \ i_{lq})^T$. Consider the square system (8) where the number of control inputs is equal to the number of outputs. It is easy to find that the relative degree of each output is $r = 1$. Let J be an 2×2 matrix such that:

$$J = \begin{pmatrix} L_{g_d}(i_{lq}) & L_{g_q}(i_{lq}) \\ L_{g_d}(u_c) & L_{g_q}(u_c) \end{pmatrix} \quad (9)$$

where the Lie derivatives of each output with respect to g_d , g_q and f are:

$$L_{g_d}(i_{lq}) = 0$$

$$L_{g_d}(u_c) = \frac{3i_{ld}}{4C}$$

$$L_{g_q}(i_{lq}) = -\frac{u_c}{2L_l}$$

$$L_{g_q}(u_c) = \frac{3i_{lq}}{4C}$$

J is non-singular under the condition $u_c i_{ld} \neq 0$. Therefore, a nonlinear feedback control can be developed as:

$$\begin{pmatrix} M_d \\ M_q \end{pmatrix} = J^{-1} \begin{pmatrix} v_q - (-\frac{R_l}{L_l}i_{lq} - \omega i_{ld} + \frac{v_{lq}}{L_l}) \\ v_u - (-\frac{i_c}{C}) \end{pmatrix} \quad (10)$$

with

$$J^{-1} = \begin{pmatrix} \frac{2L_l}{u_c} \frac{i_{lq}}{i_{ld}} & \frac{4C}{3i_{ld}} \\ -\frac{2L_l}{u_c} & 0 \end{pmatrix} \quad (11)$$

where v_q and v_u are the additional control inputs yet to be designed, for instance by using linear technique. The complete system with the controller (10) can be written as:

$$\begin{aligned} \frac{di_{ld}}{dt} &= -\frac{R_l}{L_l}i_{ld} + \omega i_{lq} + \frac{v_{ld}}{L_l} \\ &\quad - \frac{i_{lq}}{i_{ld}}(v_q - (-\frac{R_l}{L_l}i_{lq} - \omega i_{ld} + \frac{v_{lq}}{L_l})) \\ &\quad - \frac{2u_c C}{3i_{ld}L_l}(v_u + \frac{i_c}{C}) \end{aligned} \quad (12)$$

$$\frac{di_{lq}}{dt} = v_q \quad (13)$$

$$\frac{du_c}{dt} = v_u \quad (14)$$

We see that the additional inputs v_q and v_u are linear with respect to the outputs i_{lq} and u_c and hence a linear subspace of dimension two is generated. These new inputs can also transform the system (1)-(3) into a much simpler structure. In addition, (12) represents the internal dynamics of the system. Since i_{lq}^* and u_c^* are the desired trajectories which are given by a higher control level, we design the additional inputs v_q and v_u as:

$$\dot{\phi}_q = i_{lq}^* - i_{lq} \quad (15)$$

$$\dot{\phi}_u = u_c^* - u_c \quad (16)$$

$$v_q = \frac{di_{lq}}{dt} + k_{pq}(i_{lq}^* - i_{lq}) + k_{iq}\phi_q \quad (17)$$

$$v_u = \frac{du_c}{dt} + k_{pu}(u_c^* - u_c) + k_{iu}\phi_u \quad (18)$$

where $k_{p,q}$ and $k_{i,q}$ are positive.

With (17) and (18), the closed-loop system is written as:

$$\dot{\phi}_q = i_{lq}^* - i_{lq} \quad (19)$$

$$\dot{\phi}_u = u_c^* - u_c \quad (20)$$

$$\begin{aligned} \frac{di_{ld}}{dt} = & -\frac{R_l}{L_l}i_{ld} + \omega i_{lq} + \frac{v_{ld}}{L_l} \\ & -\frac{i_{lq}}{i_{ld}}\left(\frac{di_{lq}^*}{dt} + k_{pq}(i_{lq}^* - i_{lq}) + k_{iq}\phi_q \right. \\ & \left. - \left(-\frac{R_l}{L_l}i_{lq} - \omega i_{ld} + \frac{v_{lq}}{L_l}\right)\right) - \frac{2u_c C}{3i_{ld}L_l} \times \\ & \left(\frac{du_c^*}{dt} + k_{pu}(u_c^* - u_c) + k_{iu}\phi_u + \frac{i_c}{C}\right) \end{aligned} \quad (21)$$

$$\frac{di_{lq}}{dt} = \frac{di_{lq}^*}{dt} + k_{pq}(i_{lq}^* - i_{lq}) + k_{iq}\phi_q \quad (22)$$

$$\frac{du_c}{dt} = \frac{du_c^*}{dt} + k_{pu}(u_c^* - u_c) + k_{iu}\phi_u \quad (23)$$

As seen in (22) and (23), the surfaces $i_{lq}^* - i_{lq} = 0$ and $u_c^* - u_c = 0$ define two manifolds for the system, which are globally attractive. When the outputs of the system are identically equal to their reference values, the behavior of the system is governed by:

$$\frac{di_{ld}}{dt} = \frac{1}{L_l i_{ld}} [v_{ld} i_{ld} - R_l i_{ld}^2 + \frac{1}{i_{ld}} (-\frac{2}{3} u_c^* i_c)] \quad (24)$$

which is the zero dynamic relative to the outputs y_1 . Linearizing (24) around the equilibrium point \bar{i}_{ld} , we obtain

$$A = \frac{1}{L_l} \frac{\frac{2}{3} u_c^* i_c - R_l (\bar{i}_{ld})^2}{(\bar{i}_{ld})^2} \quad (25)$$

If $i_c < 0$, the converter operates in inversion mode and we have a negative A , which means that the zero dynamic is locally asymptotically stable. In this case, the system (1)-(3) can be locally stabilized under the proposed nonlinear feedback control law [11]. In fact, from (24), there are two equilibrium points of which one is negative and the other is positive. By taking into account technology limitations, only the negative one can be attained. In addition, it is also found that the attraction region of this negative equilibrium point is very large.

If $i_c > 0$, the converter works as a rectifier. The term $R_l (\bar{i}_{ld})^2$ represents the losses when the power flows through the phase reactor. In practice, the power injected into the HVDC grid represented by $\frac{2}{3} u_c^* i_c$ should be much larger than the losses represented by $R_l (\bar{i}_{ld})^2$. Thus, the term $\frac{2}{3} u_c^* i_c - R_l (\bar{i}_{ld})^2$ should always be positive, which leads to a positive A such that the zero dynamic (24) is unstable. Therefore, we can conclude that the system (1)-(3) is not feedback linearizable in rectification mode. In the next section, we develop a new control law based on a simplified model and feedback linearization technique such that the stability of the converter in rectification mode can be ensured.

B. Feedback linearization control based on the simplified model

As mentioned in Section III-A, a positive i_c results in unstable internal dynamics. One possible approach to tackling this problem is to redefine the outputs as $y_2 = (i_{ld} \quad i_{lq})^T$.

According to (12), v_u can be expressed as:

$$\begin{aligned} v_u = & -\frac{i_c}{C} + \frac{3L_l}{2Cu_c} \{ [v_d - (-\frac{R_l}{L_l}i_{ld} + \omega i_{lq} + \frac{v_{ld}}{L_l})] i_{ld} \\ & + [v_q - (-\frac{R_l}{L_l}i_{lq} - \omega i_{ld} + \frac{v_{lq}}{L_l})] i_{lq} \} \end{aligned} \quad (26)$$

where v_d is an additional control input yet to be determined. With this expression of v_u , the system (12)-(14) becomes:

$$\frac{di_{ld}}{dt} = v_d \quad (27)$$

$$\frac{di_{lq}}{dt} = v_q \quad (28)$$

$$\begin{aligned} \frac{du_c}{dt} = & -\frac{i_c}{C} + \frac{3L_l}{2Cu_c} \{ [v_d - (-\frac{R_l}{L_l}i_{ld} + \omega i_{lq} + \frac{v_{ld}}{L_l})] i_{ld} \\ & + [v_q - (-\frac{R_l}{L_l}i_{lq} - \omega i_{ld} + \frac{v_{lq}}{L_l})] i_{lq} \} \end{aligned} \quad (29)$$

We find that $i_{l,d,q}$ are linearized and decoupled from the rest of the system and (29) describes the internal dynamics of the system relative to the outputs y_2 . Now the actual control inputs M_{dq} are designed as a nonlinear state feedback control by combining (10) and (26):

$$\begin{pmatrix} M_d \\ M_q \end{pmatrix} = \frac{2L_l}{u_c} \begin{pmatrix} (-R_l i_{ld} + \omega L_l i_{lq} + v_{ld}) - v_d \\ (-R_l i_{lq} - \omega L_l i_{ld} + v_{lq}) - v_q \end{pmatrix} \quad (30)$$

If i_{ld}^* represents a reference signal for i_{ld} , we may choose the additional input v_d as:

$$v_d = \frac{di_{ld}^*}{dt} + k_{pd}(i_{ld}^* - i_{ld}) + k_{id}\phi_d \quad (31)$$

where $k_{p,d}$ are positive and ϕ_d satisfies:

$$\dot{\phi}_d = i_{ld}^* - i_{ld} \quad (32)$$

With (17) and (31), the closed-loop system is deduced as:

$$\dot{\phi}_d = i_{ld}^* - i_{ld} \quad (33)$$

$$\frac{di_{ld}}{dt} = \frac{di_{ld}^*}{dt} + k_{pd}(i_{ld}^* - i_{ld}) + k_{id}\phi_d \quad (34)$$

$$\frac{di_{lq}}{dt} = \frac{di_{lq}^*}{dt} + k_{pq}(i_{lq}^* - i_{lq}) + k_{iq}\phi_q \quad (35)$$

$$\begin{aligned} \frac{du_c}{dt} = & \frac{3L_l}{2Cu_c} \{ [(\frac{di_{ld}^*}{dt} + k_{pd}(i_{ld}^* - i_{ld}) + k_{id}\phi_d) \\ & - (-\frac{R_l}{L_l}i_{ld} + \omega i_{lq} + \frac{v_{ld}}{L_l})] i_{ld} \\ & + [(\frac{di_{lq}^*}{dt} + k_{pq}(i_{lq}^* - i_{lq}) + k_{iq}\phi_q) \\ & - (-\frac{R_l}{L_l}i_{lq} - \omega i_{ld} + \frac{v_{lq}}{L_l})] i_{lq} \} - \frac{i_c}{C} \end{aligned} \quad (36)$$

It is seen that the surfaces $i_{ld}^* - i_{ld} = 0$ and $i_{lq}^* - i_{lq} = 0$ give globally attractive manifolds and the zero dynamics relative to the outputs y_2 are:

$$\dot{u}_c = -\frac{i_c}{C} + \frac{3}{2Cu_c} [-R_l (i_{ld}^*)^2 + v_{ld} i_{ld}^*] \quad (37)$$

and then the linearized zero dynamics is:

$$B = \frac{\partial \dot{u}_c}{\partial u_c} \Big|_{u_c^*} = \frac{3[R_l (i_{ld}^*)^2 - v_{ld} i_{ld}^*]}{2C(u_c^*)^2} \quad (38)$$

A positive i_c means that the AC network supplies the power, which equals $v_{ld}i_{ld}^*$, to the HVDC grid. In practice, $v_{ld}i_{ld}^*$ should be much larger than the losses represented by $R_l(i_{ld}^*)^2$. Thus, the term $R_l(i_{ld}^*)^2 - v_{ld}i_{ld}^*$ results in $B < 0$. Therefore, the zero dynamics (37) is well behaved and locally stable. The overall system (34)-(36) is locally asymptotically stable [11] and the converter can operate in rectification mode. Now the crucial step in this procedure is to determine the reference value of i_{ld} which is not directly given but might be deduced from the known u_c^* and i_{lq}^* . In this paper, i_{ld}^* is developed based on a simplified model.

By considering that when i_{ld} and i_{lq} converge to i_{ld}^* and i_{lq}^* , (3) can be replaced by the following simplified model:

$$\frac{du_c}{dt} = -\frac{i_c}{C} + \frac{3}{4C}(M'_d i_{ld}^* + M'_q i_{lq}^*) \quad (39)$$

where

$$\begin{pmatrix} M'_d \\ M'_q \end{pmatrix} = \frac{2}{u_c} \begin{pmatrix} -R_l i_{ld}^* + \omega L_l i_{lq}^* \\ -R_l i_{ld}^* - \omega L_l i_{lq}^* \end{pmatrix} \quad (40)$$

and i_{ld}^* can be viewed as the control input. We would like to develop a strategy for i_{ld}^* such that u_c in (39) is stabilized at u_c^* . Since $(i_{ld}^*)^2$ explicitly appears in (39), we then add an integrator in i_{ld}^* which yields a higher order system as:

$$\frac{du_c}{dt} = -\frac{i_c}{C} + \frac{3}{2C} \frac{(v_{ld}i_{ld}^* - R_l i_{ld}^{*2}) + (v_{lq}i_{lq}^* - R_l i_{lq}^{*2})}{u_c} \quad (41)$$

$$\frac{di_{ld}^*}{dt} = u_d \quad (42)$$

The above system is in a standard form:

$$\dot{x}_0 = f_0(x_0) + g_0 u_d \quad (43)$$

with the trivial expressions for f_0 and $g_0 \in \mathbb{R}^2$ where $x_0 \triangleq (u_c \ i_{ld}^*)^T$. We define u_c as the output and then the relative degree of u_c is 2. Consequently, the following expressions can be deduced:

$$\dot{u}_c = L_{f_0}^1(u_c) \quad (44)$$

$$\ddot{u}_c = L_{f_0}^2(u_c) + L_{g_0} L_{f_0}^1(u_c) u_d \quad (45)$$

where

$$L_{f_0}^1(u_c) = -\frac{i_c}{C} + \frac{3}{2C} \times \frac{(-R_l i_{ld}^{*2} + v_{ld} i_{ld}^*) + (-R_l i_{lq}^{*2} + v_{lq} i_{lq}^*)}{u_c} \quad (46)$$

$$L_{f_0}^2(u_c) = -\frac{3\dot{u}_c}{2C} \frac{(-R_l i_{ld}^{*2} + v_{ld} i_{ld}^* - R_l i_{lq}^{*2} + v_{lq} i_{lq}^*)}{u_c^2} \quad (47)$$

$$L_{g_0} L_{f_0}^1(u_c) = \frac{3}{2C} \frac{1}{u_c} (-2R_l i_{ld}^* + v_{ld}) \quad (48)$$

Hence, we can define the change of coordinates as:

$$z_1 = u_c \quad (49)$$

$$z_2 = L_{f_0}^1(u_c) \quad (50)$$

in which the system (39)-(42) is rewritten as:

$$\dot{z}_1 = z_2 \quad (51)$$

$$\dot{z}_2 = L_{f_0}^2(u_c) + L_{g_0} L_{f_0}^1(u_c) u_d \quad (52)$$

TABLE I
THE VSC TERMINAL PARAMETERS.

R_l	L_l	V_{line}	C	f
10.1 m Ω	3.2 mH	415 V	680 μ F	50 Hz

TABLE II
BASE QUANTITIES APPLIED IN THE PER-UNIT SYSTEM.

AC side	$S_b = 10$ kVA	$V_b = 415\sqrt{2}$ V
DC side	$S_{dc,b} = 10$ kVA	$V_{dc,b} = 730$ V

By introducing a synthetic input θ_d , u_d is designed as

$$u_d = \frac{1}{L_{g_0} L_{f_0}^1(u_c)} (-L_{f_0}^2(u_c) + \theta_d) \quad (53)$$

where θ_d is chosen using linear techniques as:

$$\theta_d = -c_1(u_c - u_c^*) + c_2 z_2 - c_3 \phi_u \quad (54)$$

where c_1 , c_2 and c_3 are positive. Finally, i_{ld}^* is developed as:

$$\frac{di_{ld}^*}{dt} = \frac{-L_{f_0}^2(u_c) - c_1(u_c - u_c^*) - c_2 z_2 + c_3 \phi_u}{L_{g_0} L_{f_0}^1(u_c)} \quad (55)$$

Remark 1: From the above description, a reference trajectory i_{ld}^* is deduced from the simplified model by adding the additional integrator. The dynamics of i_{ld}^* can be controlled by regulating the control gains c_1 , c_2 and c_3 . Thus, when implementing v_d in (31), we usually remove the feedforward term $\frac{di_{ld}^*}{dt}$ which can be regulated to have much slower dynamics compared with i_{ld} . This action makes the controller much easier to be implemented and represents a small additive disturbance in a stable system.

Remark 2: In the case of $i_c = 0$, we can also apply the control strategy (30) under condition that u_c in (30) must be replaced by u_c^* to eliminate the static error.

IV. SIMULATION RESULTS

In order to validate the developed control structure, simulations are performed using MATLAB/Simulink. The parameters of the VSC terminal are listed in Table I. The base quantities used in the per-unit system are in Table II.

As previously described, the control system consists of two parts, i.e. the static feedback linearization control law and the dynamic one based on the simplified model. The direction of i_c is the key to determine which one is applied to the VSC terminal. In order to evaluate the performances of the control structure, several simulation scenarios are considered in this paper and all of them just use the exact feedback linearization, which means that the integral part was simplified in the control system.

A. Performance evaluation of the static feedback linearization control law

In this scenario, the converter operates in inversion mode. At the start of the simulation, the system works in a state with $u_c = 1.0$ p.u., $i_c = -0.219$ p.u., $i_{lq} = 0.01$ p.u. and $i_{ld} = -0.219$ p.u..

Before $t = 6$ s, i_c is subjected to a step change every one second, as shown in Fig. 2(a). New reference value of u_c^* is given $t = 8$ s as illustrated in Figs. 2(d).

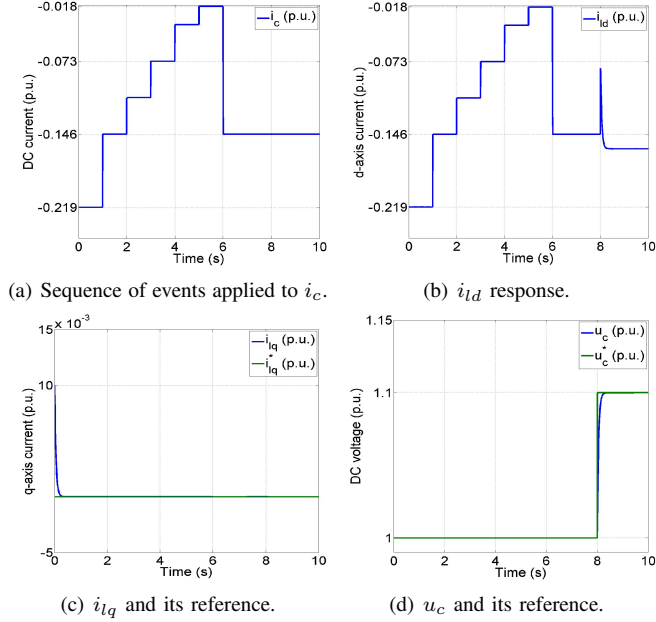


Fig. 2. Simulation results with the static feedback control law.

Since the initial value of i_{lq} is not equal to its reference value, i.e. $i_{lq}^* = 0$ p.u., i_{lq} has a fast response and converges to the origin in seconds with the proposed control law as depicted in Fig. 2(c).

As shown in Fig. 2(d), u_c always remains at its reference value, i.e. 1.0 p.u., before $t = 8$ s, irrespective of the variations in i_c . When u_c^* increases by 10% at $t = 8$ s, u_c starts to increase and then arrives at the new reference value, i.e. 1.1 p.u., after a short transient.

Due to the power balance on both sides of the converter, i_{ld} gives a corresponding response to the change of i_c as shown in Fig. 2(b). During the interval $t \in (8, 10)$ s, keeping i_c unchanged but increasing u_c^* , i_{ld} attains a new steady state around $i_{ld} = -0.1605$ p.u..

From the simulation results of this scenario, it is clarified that the static feedback linearization control law has the ability to regulate the DC voltage and the q -axis current when the converter operates in inversion mode.

B. Performance evaluation of the feedback linearization control law based on the simplified model

In this scenario, keeping i_c non-negative, we evaluate the performance of the designed feedback linearization control law based on the simplified model. In the following, we illustrate the case when the converter operates in rectification mode. The system initially operates at $u_c = 1.0$ p.u., $i_c = 0.219$ p.u., $i_{lq} = 0.01$ p.u. Then, every one second we give a new value to i_c until $t = 4$ s as shown in Fig. 3(a). In the simulation results, we denote the solutions of the simplified model (41)-(42) as $i_{ld,nom}$ and $u_{c,nom}$.

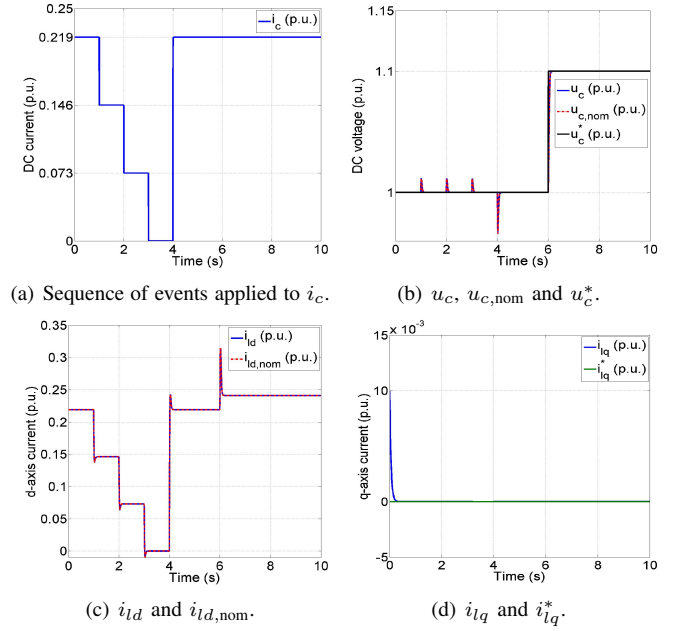


Fig. 3. Simulation results with the feedback linearization control law based on the simplified model.

As presented in Fig. 3(c), i_{ld} reaches the corresponding steady state every time i_c varies. We also find that the trajectories of i_{ld} and $i_{ld,nom}$ almost coincide with each other as seen in Fig. 4(b). Figure 5(b) clearly shows that the discrepancy between i_{ld} and $i_{ld,nom}$ is extremely small.

The response of DC voltage are shown in Fig. 3(b). If u_c^* is kept unchanged, u_c is well controlled at 1 p.u. in spite of the change of i_c . At $t = 6$ s, u_c^* is increased by 10% as in Section IV-A. u_c gives a fast response and converges to the new reference value, i.e. 1.1 p.u. As seen in Fig. 4(a), the response of u_c is almost identical to that of $u_{c,nom}$. The error between the two responses is very small (see Fig. 5(a)).

As shown in Fig. 3(d), although i_{lq} starts from the point $i_{lq} = 0.01$ p.u., it quickly converges to its reference value, i.e. $i_{lq}^* = 0$ p.u..

The above simulation results illustrate that we can use the simplified model (41)-(42) to generate the reference signal for i_{ld} such that u_c can be well regulated at its desired value.

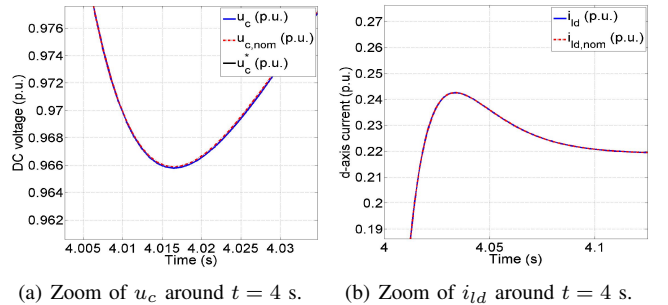


Fig. 4. The responses of u_c and i_{ld} around $t = 4$ s.

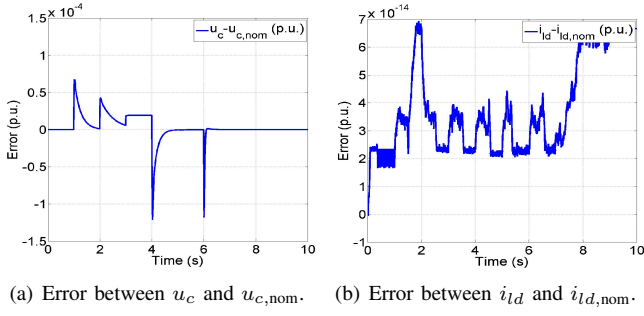


Fig. 5. Error between the physical model and the simplified model.

C. Performance evaluation of the combination of the static and the dynamic feedback linearization control law

In this scenario, we evaluate the system performance when the converter operates from one operation mode to the other mode. As depicted in Fig. 6(a), i_c reverses from the negative to the positive, which requires the converter to operate from inversion mode to rectification mode.

Before $t = 6$ s, since i_c is negative, the static feedback control law is applied to the VSC terminal. As illustrated in Fig. 6(b) and Fig. 6(d), u_c and i_{lq} are well controlled at their respective desired values. During $t \in (6, 8)$ s, $i_c = 0$. As mentioned in Remark 2, the DC voltage can be kept at its desired level under the controller. When i_c reverses, the feedback linearization control law based on the simplified model is used. We see that u_c is still stabilized at its reference value after a short transient and the trajectories of u_c and $u_{c,nom}$ are very close to each other. Figure 6(c) shows the response of i_{ld} . In order to keep the power balance on both sides of the converter, i_{ld} is brought into a new steady state every time i_c or u_c^* changes.

The simulation results clearly shows that the proposed control structure can make the VSC terminal fulfill bi-directional power transfer.

V. CONCLUSION

Focusing on the DC voltage control of a VSC terminal using feedback linearization, this paper shows that the converter is feedback linearizable in inversion mode but not in rectification mode when u_c and i_{lq} are defined as the outputs. In order to overcome this limitation, a simplified model is developed to generate a reference signal for i_{ld} , since the system is feedback linearizable in rectification mode by defining $i_{l,dq}$ as the outputs.

The advantage of this control system is that we can use linear techniques to design the closed-loop system since the partial nonlinearity of the system can be canceled. As seen from the simulation results, when the converter operates in inversion mode, the performance of the system is good enough since there is almost no overshoot. Therefore, the proposed control system is especially suitable for such VSC-HVDC system which is used to transfer the power flow from the unstable energy sources, such as wind farms and solar plants, to the mainland AC network. However, the main drawback is that the control system is complicated which

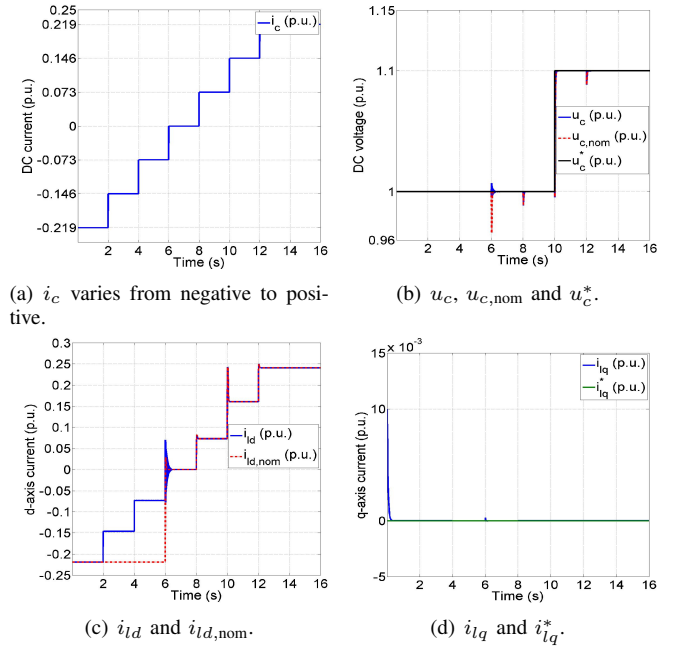


Fig. 6. Simulation results with the proposed control system.

needs to switch between two control laws according to the direction of the power flow, although this case most likely happens only a few times a day.

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