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## FIELD EFFECT TRANSISTOR WITH INTEGRATED MICROFLUIDIC CHANNEL

## AS PH SENSOR

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#### **ABSTRACT**

This paper presents an original design of chemical sensors with an integrated microfluidic channel. Targeted applications are pH-meters devices. The integration of the microfluidic channel allows decreasing the volume required for each measurement. The sensing part of the device consists of a field effect transistor with a suspended gate directly performed above the fluidic channel. Chemicals under test are driven through the sensing area between the electrical channel of the FET and the suspended gate. By this way products that flow in the microfluidic channel directly module the concentration of charges inside the transistor's gap and thus induce changes in the transfer characteristic. This paper describes the fabrication process and the technological choices for materials. Electrical tests, performed in air and in liquid, have shown a good behavior of the transistor, linked to a good mechanical sustain of the fluidic channel. The system is able to detect transition between air and liquid media. Moreover, it has shown a high sensitivity (about 300 mV/pH) to pH measurements.

**Keywords:** pH sensor, microfluidic, Field effect transistor.

## 1. INTRODUCTION

Chemical sensors are important tools used in medicine for diagnostic, in chemistry for pH measurement and also in environmental monitoring for gas detection in air or ionic concentrations in water. Among sensors processed by microtechnologies, different structures of transistors, since the work of Bergveld [1], have shown main interest for chemical sensing, as ISFET (Ion Sensitive Field-Effect Transistor) [2-6], Floating Gate FET [7] or Extended Gate FET (EGFET) [8-10]. For all these devices, the maximum pH sensitivity is limited to the Nerstian response (59.5 mV/pH at room temperature) [11, 12]. SGFETs (Suspended Gate Field Effect Transistor) involving gates suspended at submicron distance have shown good properties in charges detection [13]. They have been used in pH measurement [13-15] and detection of biological species [16-18]. These original structures have shown impressive characteristics, in particular high detection sensitivity. This sensitivity has been demonstrated in the detection of pH, but also biological detection (DNA, proteins) [16-18]. An analytical model devoted to these microsensors and based on the 2D-numerical resolution of Poisson's equation, has been developed [19]. The response of quasi-static C(V) plots versus pH is simulated using both electrolyte charge distribution and site-binding theory considering the influence of sites densities on silicon nitride. It has

allowed to show that this specific geometry with a suspended gate at submicronic distance changes the charges distribution and leads to a high sensitivity to pH, significantly greater than that obtained for structures of the same family as ISFET [11, 12, 19].

Many of the chemical sensors are integrated in a microfluidic system, to improve the flow management and prevent from evaporation. In addition, this integration can reduce the required amount of liquid, which is especially valuable for biological materials that can be quite expensive or not readily available. The fabrication of a microfluidic system molded into PDMS (Polydimethylsiloxane) microchannel is the most used technique [20, 21]. It was also performed with SGFET [14], encapsulated in a PDMS micro-fluidic device to drive chemicals flow nearby the sensor. This integration helps the driving of liquid and is useful in rinsing steps, so that it become easy to define a precise and reproducible protocol of use. However, in the case of SGFET but also of other chemical sensors, the liquid chamber is often oversized compared to the scale of the sensor itself.

One solution is to integrate a microfluidic channel within field effect transistor in order to drive the liquid in the active area of the sensor. This new technology is harder to perform but give the advantage of an integrated microfluidic sensor easy to use and should increase the reliability of the detection.

This paper presents the technology that has been developed to process the structure as well as its applications, in particular for the measurement of pH with high sensitivity.

#### 2. TECHNOLOGY

#### 2.1 General design

The basic structure is a field effect transistor with an integrated microfluidic channel. The floor of the fluidic channel contains the drain, the source and also the electrically active area of the transistor, whereas the top of the fluidic channel corresponds to the gate of the transistor. The fluidic channel is insulated from the contacts of the device. The height of the fluidic channel can be easily fixed by monitoring the thickness of the different layers, especially the sacrificial one. Inlets and outlets, connected to the external fluidic system, can be made with PDMS.

The general structure is shown figure 1. Several transistors can be designed along one fluidic channel.

In this structure, the channel must be mechanically sustained on a long length. It is then necessary to use a layer with good mechanical and electrical properties, as this layer will act as gate and channel cover. Moreover, the sacrificial layer should be easy to etch.

## 2.2 Microchannel fabrication

The microchannel process is based on the use of a sacrificial layer and a structural layer. Two sacrificial materials have been investigated. The first one is a photoresist deposited by spin coating with a thickness of 1µm. The second one is made of silicon dioxide deposited by Radio Frequency sputtering. These two materials have been chosen for their properties of deposition and etching. The first one limits the temperature for the end of the process as well as the temperature of use. The second one can be easily used in classical high temperature processes. For this, the optimization of the oxide layer, in order to obtain a particular high etch rate in 2% HF solution (hydrofluoric acid) has been achieved by changing the pressure of deposition. A high wet etch rate of 1.1

 $\mu$ m / min is obtained. For the structural layer the materials used were either silicon nitride and polycrystalline silicon (50nm/700nm) for substrates with oxide as sacrificial layer, or sputtering RF Silicon oxide deposited at room temperature and aluminium (100nm/800nm) in case of photoresist as sacrificial layer. The process steps are shown figure 2.

The last step consists to remove the sacrificial layer by using the two openings, which also will act as inlet and outlet.

Fig. 3 shows cross section of micro-channel using photoresist (figure 3.a) or silicon oxide (figure 3.b) as sacrificial layer. Etching of 750 µm length by HF 2% for SiOx is very long and reacts with the most materials used. The photoresist is quickly etched by acetone which doesn't react with other materials. Thanks to these properties, the photoresist was chosen for use as sacrificial layer in the fabrication of sensor. As show figure 3, the gate is suspended with good mechanical sustain.

## 2.3 Technological process for the sensors

Sensors have been processed on silicon wafers by surface micromachining associated to classical high temperature MOS process. Substrates used are Si <100> oriented and low doped (N type). The drain and source are doped by boron diffusion at  $1100^{\circ}$ C. The protection layer is a 1,2  $\mu$ m thick SiO<sub>2</sub> film. The channel is then oxidized at  $1100^{\circ}$ C to obtain a 70 nm thick layer. This step is followed by a LPCVD deposit (Low Pressure Chemical Vapor Deposition) of a 100 nm thick layer of silicon nitride, which acts as sensitive layer to the  $H_3O^+$  ions.

Then, a  $1\mu m$  thick photoresist S1818 sacrificial layer is deposited by spin-coating, followed by a 100 nm thick SiO<sub>2</sub> layer deposition by radio frequency sputtering at room temperature, performing thus the insulating layer for the bottom of the suspended gate. Via are opened in this insulating layer (silicon nitride and silicon dioxide) to reach source and drain contacts.

Gate and drain source contacts are then obtained by the deposition of a  $1\mu m$  thick aluminium layer. A final  $100\mu m$  thick layer of SU8 photoresist is deposited by spin coating to protect the aluminium and also, thanks to its elastic properties, to protect the gate from mechanical stresses. A scheme of P-type FET with microfluidic channel is presented in figure 4 at the step before removing the sacrificial layer.

Finally, after removal of the sacrificial layer, PDMS (Polydimethylsiloxane) inlet and outlet can be added on the openings (fig 3).

#### 3. EXPERIMENT AND RESULTS

#### 3.1 Characterization in air

FETs with microfluidic channel have been characterized to show the good behaviour of these devices. Devices are first tested before and after the etching of the sacrificial layer. One example of transfer characteristics is given figure 6.

It is shown in this figure, from the difference between the two characteristics, that the sacrificial layer has been successfully removed. Indeed, the capacitance C<sub>I</sub> between the gate and the electrical channel has changes as well has the conductance and the current between source and drain.

The model of classical MOS transistor can then be used. The current  $I_{DS}$  in the linear regime  $(|V_{DS}| << |V_{GS} - V_T|)$  is expressed by:

$$\left| I_{DS} \right| = \frac{W}{L} \mu C_{I} \left[ \left| \left( V_{GS} - V_{T} \right) V_{DS} \right| - \frac{V_{DS}^{2}}{2} \right]$$

With W and L the width and the length of the transistor,  $\mu$  the carrier mobility and  $V_T$  the threshold voltage. The following figure illustrates the output characteristic of FET with integrated microfluidic channel measured in air. The characteristic shows good modulation of drain current by the gate voltage that as in usual field effect transistors.

# 3.2 Detection of liquids

The change in transfer characteristic of the transistor with air in the channel and water in the channel is clearly shown in figure 8. The conductance increases when the water is injected, and is linked to an increase of the permittivity between the gate and the electrical channel leading to an increase of the insulator capacitance  $C_I$ .

Another measurement used to highlight the water injection is based on sampling test. The transistor is biased with a constant gate voltage of -11 Volts and a drain source voltage of -4 volts. The drain source current ( $I_{DS}$ ) increases widely when liquid reach the transistor channel area. Once the channel is filled with water the drain current  $I_{DS}$  remains stable and constant. It also could be noticed that the gate current  $I_g$  is weak showing the good electrical insulation.

## 3.3 PH sensing

FETs with integrated microchannel have been tested with solutions of different pH values. Basic solutions have been produced with NaOH and acid solution by using boric acid. Drain-source currents  $I_{DS}$  have been measured versus the gate voltage  $V_{GS}$ , with drain-source voltage  $V_{DS}$  fixed at -2V. Figure 10 shows the transfer characteristics with different pH values tested at ambient temperature. The threshold voltage decreases for higher pH values, which is the correct behaviour of a traditional FET pH-meter. This phenomenon is explained by the decrease in the amount of positives ions  $H^+$  and increasing the amount of negative ions OH in the test solution when the pH increases, inducing more negative charges at the surface of the transistor channel. Each measurement is made on static operation mode. The devices are tested in the pH range from 5 to 10 which is the interesting range for targeted applications, as pH variations in physiological liquids, or pH control in water for environment applications. Extreme pH values could be measured by adding a protecting layer on the surface of the sensor thus improving its insulation.

The pH sensitivity is defined as the gate voltage variations per pH unit for fixed drain-source current  $I_{DS}$ . The variation of  $V_{GS}$  with pH is reported for different pH values and different devices in figure 11. This variation of voltage is calculated from the  $V_{G0}$  value, given for a pH equal to 7. This curve is quite linear and the sensitivity

calculated from the slope is 290 mV/pH. This high value that exceeds the Nernst sensitivity obtained with ISFET sensors was also obtained with suspended gate FETs.

The electronic transducer, which is a field effect transistor, has a short response time, less than one second. The response time of the chemical sensor will be higher, and is especially due to the relaxation time of chemical elements, their interaction with the surface and the time for the liquid to reach the sensing area. As the device is more complex than the ISFET structure, the response time will be higher than the one for ISFET but could be improved by optimizing the design of the microchannel. By the way, the major interest of the new sensor is its high sensitivity to pH compared to classical ISFET sensors.

#### 4. CONCLUSION

Microfluidic channels of 1 µm height and 200µm width have been processed by surface micromachining using a simple method of sacrificial layer patterning. The top of this channel acts also as a gate performing by this way a FET with an integrated microfluidic channel. The integration of transistor inside this microchannel has been quite successful. Indeed, first, the structure shown good mechanical sustain after sacrificial layer removal, and thus prove the feasibility of the concept. Second, electrical tests have shown the good functioning of FETs with integrated fluidic channel and especially a good stability after removal of the sacrificial layer.

Tests with liquids have shown a quite promising sensitivity to different pH. In next experiments microfluidic inlet and outlet and the design of microfluidic channels will be improved because it is still a weak point of the device especially to facilitate the liquid injection in the small dimension channel.

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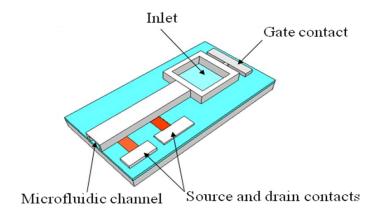


Figure 1.

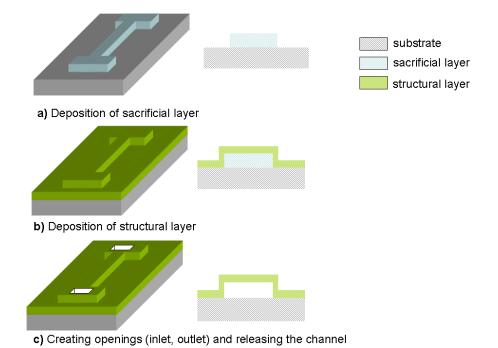
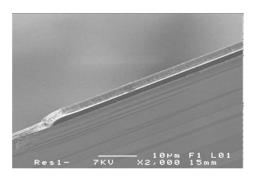


Figure 2.



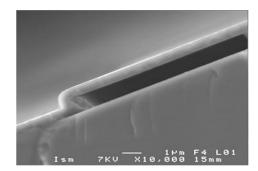


Figure 3

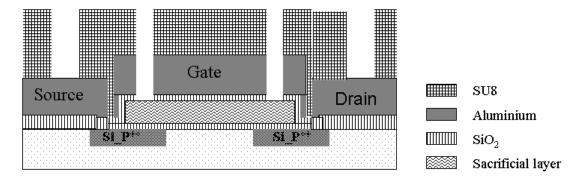


Figure 4

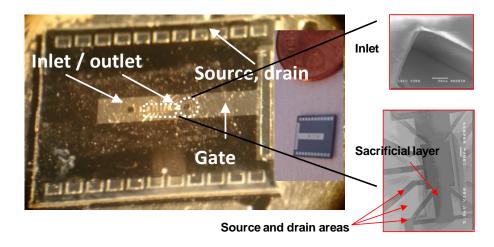


Figure 5

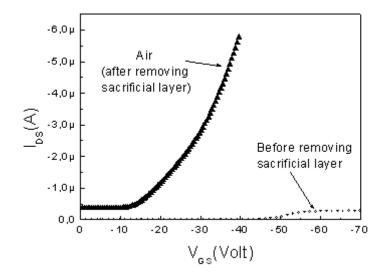


Figure 6.

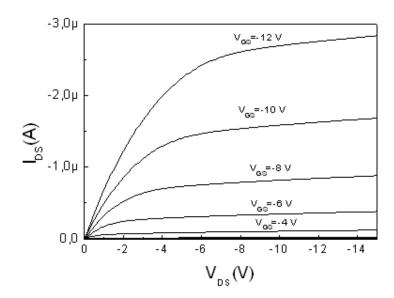


Figure 7

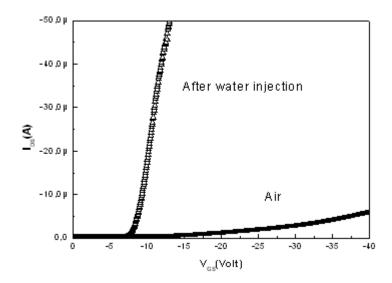


Figure 8

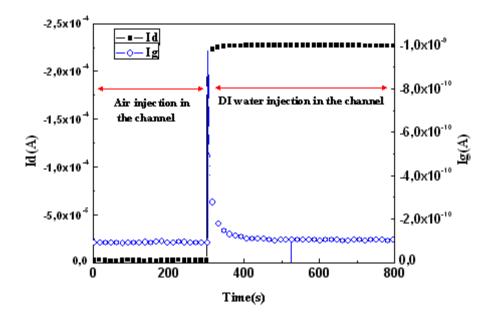


Figure 9

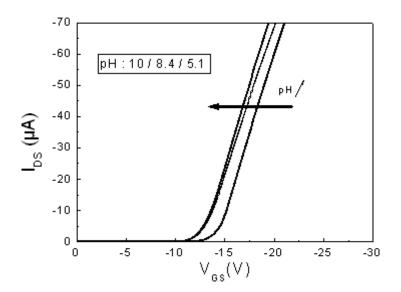


Figure 10

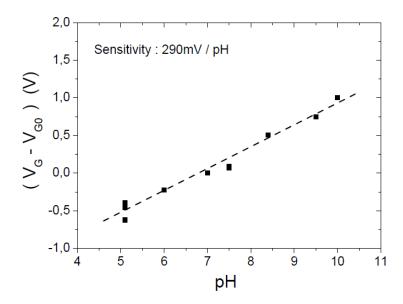


Figure 11

## List of captions:

Figure 1: Schematic structure of FET with fluidic microchannel.

Figure 2: Steps for the manufacturing of microchannel.

Figure 3: SEM cross-section showing the micro-fluidic channel: (a) Using photoresist as sacrificial layer, (b) Using SiOx as sacrificial layer.

Figure 4: Cross section showing the device before removal of sacrificial layer.

Figure 5: (a) Optical picture showing the design of the aluminium gate under a SU8 layer, inlet and outlet are visible on both side of the channel, (b) SEM picture showing the SU8 protection of the device and the inlet, (c) design of doped p-wells crossing the channel area is zoomed.

Figure 6: Transfer characteristics before and after the etching of the sacrificial layer (for  $V_{DS} = -2V$ ).

Figure 7: Output characteristics of FET with integrated microfluidic channelafter the removing of the sacrificial layer.

Figure 8: Transfer characteristic of the FET with air or water in the fluidic channel.

Figure 9: Drain current  $(I_{DS})$  sampling showing the two operating ranges in air and DI water. Transition is direct and level of current to stable.

Figure 10: Electrical transfer characteristic with different pH values.

Figure 11: Plot of the gate voltage variations versus pH at fixed drain source current.