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Loïc Théolier, Hicham Mahfoz-Kotb, Karine Isoird, Frédéric Morancho. A new junction termination technique: The Deep Trench Termination (DT²). 21st International Symposium on Power Semiconductor Devices & IC's (ISPSD), 2009., Jun 2009, Barcelona, Spain. pp.176-179, 10.1109/ISPSD.2009.5158030 . hal-00991573

HAL Id: hal-00991573

<https://hal.science/hal-00991573>

Submitted on 16 May 2014

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A new junction termination technique: the Deep Trench Termination (DT²)

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Abstract — Numerous techniques have been used to improve the voltage handling capability of high voltage power devices with the aim to obtain the breakdown of a plane junction. In this work, a new concept of low cost, low surface and high efficiency junction termination for power devices is presented and experimentally validated. This termination is based on a large and deep trench filled by BCB (BenzoCycloButene) associated to a field plate. Simulation results show the important impact of trench design and field plate width on termination performances. The experimental breakdown voltage of this Deep Trench Termination (DT²) is close to 1300 Volts: this value validates not only the concept of the DT² but also the choice of the BCB as a good dielectric material for this termination.

I. INTRODUCTION

The breakdown voltage in high voltage power semiconductor devices is one of the most important problems encountered. In order to improve the breakdown voltage, an adequate edge termination is necessary to reduce the electric field peak at the devices periphery. To date, several techniques have been proposed to reduce this electric field peak: these include guard rings [1], junction termination extension (JTE) [2], field-limiting rings (FLR) [3], field plate (FP) and guard rings [4], field plate and a SIPOS layer [5] and 3D RESURF (Superjunction) [6], [7]. However for high voltage devices, these techniques have the drawback of consuming large area.

Recently, several junction terminations with a deep trench have been reported [8], [9], [10]. The T³JTE [8], the concave junction employing deep trench [9] and the deep trench junction termination using inductive coupled plasma reactive ion etching (ICP-RIE) [10] required a deep silicon etching (usually several tens or several hundreds microns) in order to decrease the junction termination area and increase the breakdown voltage. The T³JTE was not experimentally demonstrated. The application of the concave termination method is restricted to high voltage devices based on deep junctions such as integrated capacitors on silicon. The deep trench junction termination using ICP RIE is difficult to apply to non-punch through cases.

The purpose of this paper is to report a suitable and relatively low cost junction termination for 1200 V power devices. This new junction termination, called the Deep Trench Termination (DT²), is based on the use of dielectric filled, large and deep trench associated to a field plate. Because of the dimensions of this trench termination, conventional thin film deposition techniques were not applicable. As a result, the possibility of trench filling using Spin-On Dielectrics (SOD) was studied. At the end we present experimental results of the DT² breakdown voltage.

II. DT² CONCEPT AND THEORETICAL RESULTS

Fig. 1 shows the cross-sectional view of the proposed DT2 structure. In the proposed device, the trench needs to contain all the potential. For that, termination includes a field plate on top of a dielectric trench.

Fig. 2 shows the 2D-simulated electrostatic potential distribution of the DT² structure at breakdown. The dielectric in the trench reduces the junction termination area because it sustains all the potential, with a critical electric field higher than that of silicon. In addition, the field plate disables variations of the electrostatic potential in the silicon, guaranteeing a breakdown voltage of the termination near the infinite plane junction breakdown voltage.

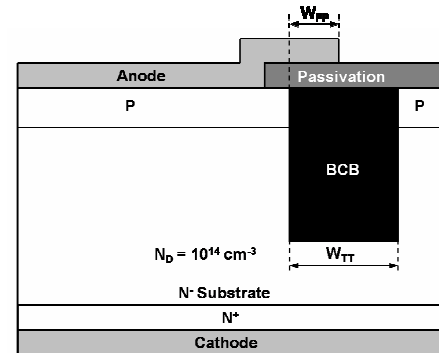


Figure 1. Schematic cross-section of a PN diode with the Deep Trench Termination (DT²).

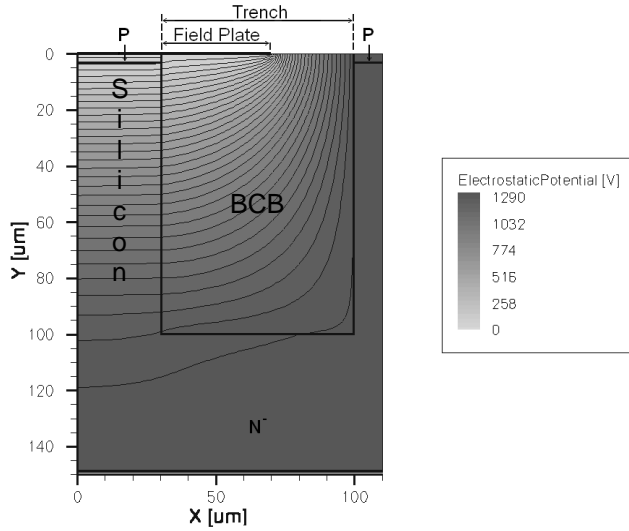


Figure 2. 2D-simulated theoretical potential distribution in the Deep Trench Termination at breakdown.

Fig. 3 shows the 3D-simulated electrostatic potential distribution of the DT² structure at breakdown. This potential distribution is restricted in the trench like in the 2D simulation. Nevertheless, the 3D-simulated voltage handling capability becomes 80% of the 2D-simulated voltage handling capability (1060 V versus 1290 V). This is due to the cylindrical junction which increases the electric field in the BCB like in the silicon. This result is obtained for a field plate width (W_{FP}) equal to 40 μm , a trench termination width (W_{TT}) equal to 70 μm and an internal radius near 0 μm . This choice of internal radius was made for Superjunction devices in order to keep the charge balance everywhere in the device. For conventional devices, it is possible to increase this internal radius in order to reach the voltage handling of the plane junction.

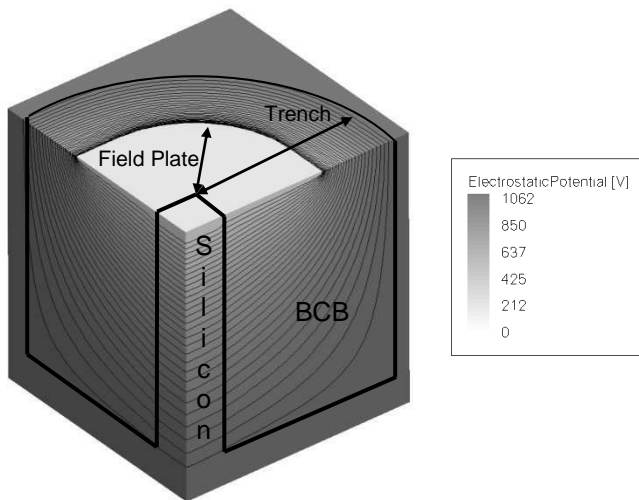


Figure 3. 3D-simulated theoretical potential distribution in the Deep Trench Termination at breakdown.

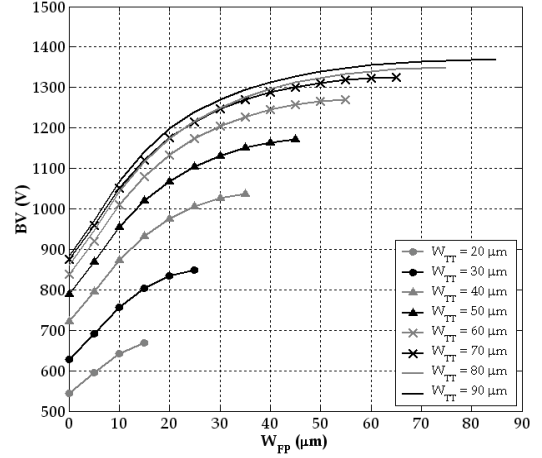


Figure 4. Breakdown voltage variations of the DT² versus W_{FP} and W_{TT} .

Performances of DT² depend not only on trench and field plate dimensions but also on dielectrics characteristics of the filling material. Fig. 4 exhibits the breakdown voltage optimization of the DT² versus the trench termination width (W_{TT}) and the field plate one (W_{FP}) for a 1200 Volts device: in this case, the studied device is a DT-SJMOSFET [11]. The termination without field plate ($W_{FP} = 0$) is not able to sustain more than 900 Volts, even if the trench is very large. In addition, a very low variation of the breakdown voltage can be observed for trench widths higher than 70 μm . In this case, the breakdown voltage of the trench termination reaches the breakdown voltage of the central cells: if the trench width becomes very important ($W_{TT} > 90 \mu\text{m}$), the termination efficiency is then close to 100%. The optimal couple of values for W_{FP} and W_{TT} , in terms of “performance / size” trade-off, is 40 μm and 70 μm respectively: with these dimensions, the termination breakdown voltage is equal to 96% of the base cell one.

Because there are several possibilities of dielectric to fill the deep and wide trench, breakdown voltage of the termination was studied for different types of dielectric, i.e. for different values of critical electric field (E_{Cd}) and permittivity (ϵ_{rd}). Simulation results on the dependence of the breakdown voltage variations of the DT² on E_{Cd} and ϵ_{rd} are given in Fig. 5. A break can be observed near $2.5 \times 10^6 \text{ V}\cdot\text{cm}^{-1}$: below this value of E_{Cd} , the electric field at the end of the field plate is greater than E_{Cd} and the edge cell breaks down before the electric field reaches its critical value in the base cell. For higher values of E_{Cd} , the base cell breaks down first. This minimal value of E_{Cd} is only available for the trench shape of this study (70 μm width). In fact, the maximal electric field in the edge is located at the end of the field plate. Its value depends on the trench width and on the field plate width. In addition, the breakdown voltage of the edge cell is constant for high dielectric permittivities. According to these simulations results, a large variety of dielectric materials is possible to fill the trench termination.

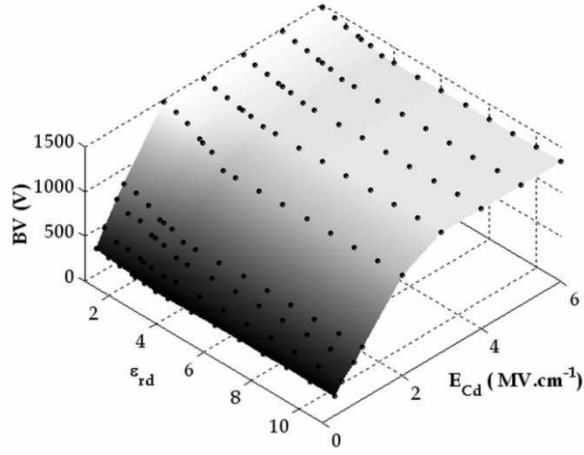


Figure 5. Breakdown voltage variations of the DT² versus E_{cd} and ε_{rd}.

TABLE I. SUMMARY OF THE MAIN PROPERTIES OF BCB COMPARED TO SiO₂ PROPERTIES (BCB PROPERTIES FROM [12])

Property	SiO ₂	BCB
Dielectric constant (ε')	3.9	2.65
Dielectric field strength (MV/cm)	6-11	5.3
CTE (ppm/K)	0.5	42
Glass transition temperature (°C)	> 1200	> 350
Residual stress on Si (MPa)	+100 to +300	-28 ± 2 (tensile)
Index of refraction	1.46	1.54 at 1300-1550 nm
Shrinkage after cure	n/a ^b	< 5 %

^b Not available

A good candidate is the BenzoCycloButene (BCB) polymer. Its good electrical properties are given in Table 1 and compared to those of silicon oxide. Besides BCB is known to have a low moisture uptake (0.24 % at 85 % RH) [12, 13].

III. EXPERIMENTAL RESULTS

In order to evaluate the performance of this novel termination, a PN⁻ diode using the proposed DT² at its periphery (Fig. 1) was fabricated on a N-type substrate with a resistivity and thickness of 50 Ω.cm and 520 μm respectively. This choice of parameters allows avoiding breakdown in the center of the device because the breakdown voltage of the PN⁻ plane junction should be close to 1400 Volts and breakdown would then occur at the device termination. The trench depth and width are 100 μm and 70 μm respectively. Because of its high dimensions, the trench has to be realized by Deep Reactive Ion Etching (DRIE). After DRIE etching of silicon substrates to the needed depth, adhesion promoter AP3000TM (from Dow Chemical) is applied onto the wafer. Then, Cyclotene 4026-46 BCB resin is spun. Afterwards, BCB is hard cured inside a furnace tube in N₂ ambient at 250 °C. A temperature ramp of about 1.5 °C.min⁻¹ was used to arrive at 250 °C. However, for a complete filling of the edge trench, another layer of BCB was necessary. The second layer is applied and cured in the same conditions as previously. After trench filling an excess, uneven BCB layer of about 30 μm is formed on the surface of the substrate. To smooth the surface

TABLE II. CMP PARAMETERS

Slurry	Klebosol STS-2: 30 (wt %) silica abrasives (grain size = 25 nm), pH = 2.5
Slurry flow rate	50 ml/min
Down force pressure	2 psi
Backside pressure	2 psi
Platen rotation speed	60 rpm
Carrier rotation speed	50 rpm

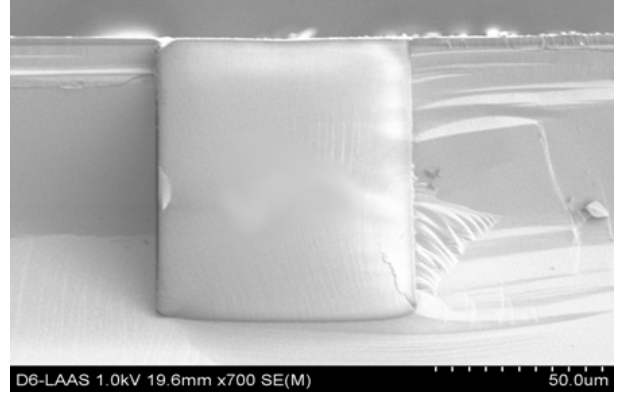


Figure 6. SEM photograph of the BCB-filled trench termination after CMP.

topography of the substrate and remove excess BCB coating after BCB filling, Chemical Mechanical Polishing (CMP) of BCB is realized using a Logitech CDP (CMP) system. The parameters for the CMP process are given in Table 2.

These conditions for the CMP process have enabled an etch rate of BCB of about 0.24 μm/min (± 0.04 μm/min). Polishing is stopped before reaching the surface of the silicon substrate (Fig. 6). In this way, a layer of about 500 nm of BCB is left on the surface to act as a dielectric under the field plate. In average, a polishing time of about 120 min was necessary to prepare the surface of the substrate for next steps. After polishing, openings are realized in BCB by dry etch and metal contacts are deposited. The field plate is made in the same time as metallization. One can notice that the technological process is relatively simple and could be used for the realization of a wide variety of high voltage power devices. The relatively long time of polishing would be reduced by modifying the CMP parameters.

The OFF-state performance of this new structure was electrically verified. Fig. 7 shows the measured and 2D simulated reverse J-V (current density versus voltage) characteristics. The simulation previewed a 1290 Volts breakdown voltage and the measurements exhibit 1310 Volts. These results are very close, showing the good process control of the device. Such performance can be reached thanks to the high critical electric field in the BCB. Furthermore, the measured leakage current of the DT² is equal to 10 μA/cm² just before breakdown, which is a very acceptable and conventional value. Fig. 8 shows that this is the termination which breaks down: this validates not only the DT² concept but also the choice of the BCB as a good dielectric material for this termination, in terms of mechanical and electrical properties.

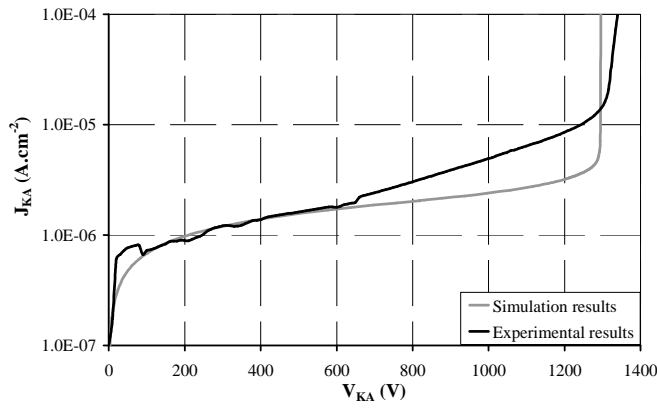


Figure 7. Measured and simulated J-V reverse characteristics of a PN diode using the proposed DT² structure.

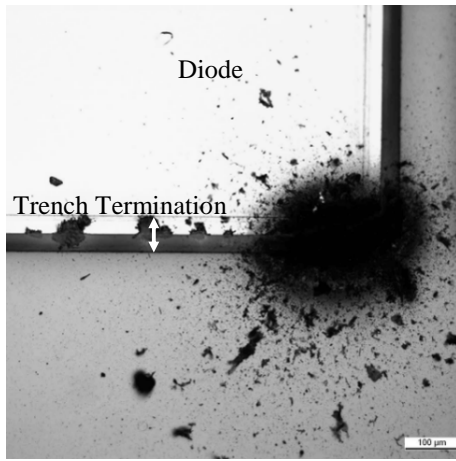


Figure 8. Device damage in the corner of the test diode after breakdown: that confirms that breakdown occurs in the DT².

IV. CONCLUSION

A new junction termination for 1200 V power devices, consisting in a 70 μm width and 100 μm depth trench filled by a dielectric (the BenzoCycloButene), is proposed for the first time. Simulation results showed that the DT² exhibits a blocking voltage capability near the ideal breakdown voltage when using a filling material with a critical electric field of at

least 2.5 MV/cm. A PN diode with the DT² was fabricated; the technological process is relatively simple and could be used for the realization of a wide variety of high voltage power devices. Experimental data show that this new junction termination sustains 1300 V with a leakage current of 10 μA/cm², like predicted by theory. Furthermore, the width of the DT² being only 70 μm, one can notice that the junction termination area is dramatically decreased compared to conventional junction terminations in this voltage range [6, 7].

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