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Paul-Etienne Vidal, Simon Cailhol, Frédéric Rotella, Karima Berkoune, Ana-Maria Llor, et al.. Generalized inverses applied to Pulse Width Modulation for static conversion: a first study. EPE'13 ECCE Europe, Sep 2013, Lille, France. pp.1-10, 10.1109/EPE.2013.6634683 . hal-00952708

**HAL Id: hal-00952708**

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Submitted on 27 Feb 2014

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Vidal, Paul-Etienne and Cailhol, Simon and Rotella, Frédéric and Berkoune, Karima and Llor, Ana-Maria and Fadel, Maurice *Generalized inverses applied to Pulse Width Modulation for static conversion: a first study*. (2013) In: EPE'13 ECCE Europe, 03 September 2013 - 05 September 2013 (Lille, France).

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# Generalized Inverses applied to Pulse Width Modulation for static conversion: a first study

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## Keywords

<<Converter control>> <<Device modeling>> <<Energy efficiency>> <<Modulation Strategy>>  
<<Pulse Width Modulation>> <<Voltage Source Inverter>>

## Abstract

This paper points out the generic matrix approach to design Pulse Width Modulation strategies of three-phase Voltage Source Inverters. This well-known problem has infinitely many solutions, and many modulation methods already exist. This mathematical approach deserves to be explored by its rigor and must identify known but also new solutions.

## Introduction

The static converters are electrical components that can be found in many different applications. In high power devices, such as railway applications, air-planes but also automobiles one can find DC to AC converters (named inverters). Such a device allows to connect, in Voltage Source Inverter (VSI) configurations, DC voltage sources to AC current sources. Current sources are mainly constituted of electrical machines or resistive - inductive loads, (Fig. 1).

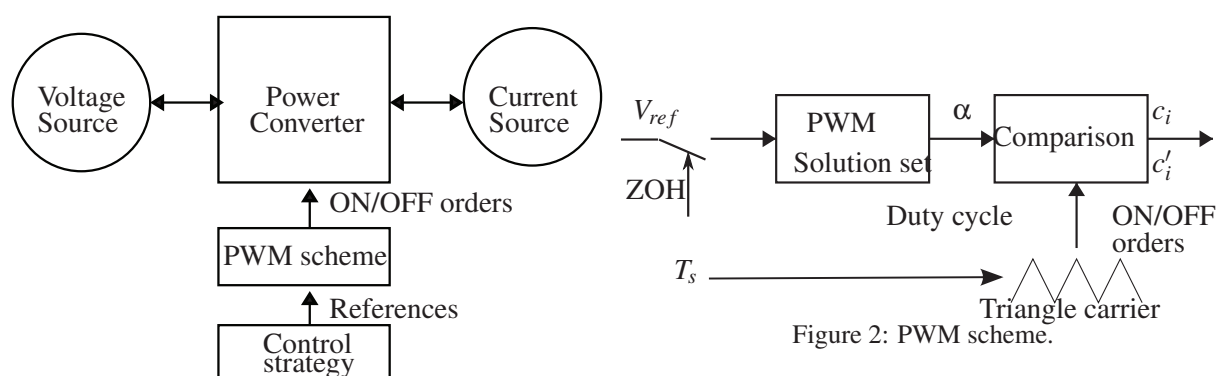


Figure 1: Direct static converter.

Beside the control strategy box, which ensures the closed loop control, a specific stage is distinguished. It is situated between the power converter device and the control board. Indeed this specific stage translates the continuous reference given by the closed loop feedback in binary signals. Finally, they are applied to the power device as illustrated in Fig. 2. In most cases this stage is based on Pulse Width Modulation (PWM) technique. The PWM uses the average value of the voltage applied to the load in order to check the output desired voltage. In most applications, this output voltage will ideally be a sine wave.

In power electronics, the PWM goal is to modulate the magnitude, the frequency and the phase of voltages or currents. These informations are contained in the reference signal issued from the control stage. In the original PWM technique applied to VSI, a sinusoidal reference voltage is compared to a triangle carrier. The output is ON/OFF order applied to the switches. *K. B. Bose* made an overview of several PWM techniques following, the current or voltage controller, the feed-forward or feedback methods, carrier or non-carrier based ones, etc. [1]. Some of them are considered as continuous: the modulation waves are always within the triangle peak boundaries. In such a case, for each carrier period, triangle and modulation waves intersect to produce ON/OFF switching orders. Others PWM methods are said discontinuous: for instance one leg is clamped at the *DC* voltage value for more than one switching period. In some studies, graphical tools are displayed in order to aid the design, the performance evaluation etc. [2]. Other studies detail how to increase the PWM efficiency [3], or what are the link between different PWM methods [4], [5]. Some authors also work on the interaction between the control and the PWM technique in order to improve motor efficiencies [6]. Nowadays the trend is to apply usual PWM to multilevel converters, [7] [8]. It appears to our knowledge that, a generic link between the used VSI model and a formal mathematical approach leading to the PWM solution set has never been described. Let us consider now the carrier based PWM technique. It consists in the comparison of reference waveform  $\alpha$ , linked to the duty cycle, and one triangle carrier. It is assumed that the reference voltage,  $V_{ref}$ , is issued from regulators and that the control strategy is already done. The ON/OFF orders,  $c_i, c'_i$ , are applied to power switches of the three-phase VSI leg, as illustrated in Fig. 2. The goal of this study is to provide a new and generic mathematical solution of PWM - triangle based carrier. From the VSI model, a specific inversion will lead to the PWM solution set. Finally, the study checks that some of the well known PWM strategies are obtained by fixing some free parameters. The boundaries of the freedom parameters are detailed. Moreover, some simulation results are provided.

## Model of 3 $\phi$ Voltage Source Inverter

### VSI average model

Let us consider a three-phase VSI as described in Fig. 3.  $E$ , is a *DC* voltage. Each ideal switch, denoted  $K_i$  or  $K'_i$ , with  $i \in \{1, 2, 3\}$  is considered to be assigned an ON/OFF control signal  $c_i, c'_i \in \{0, 1\}$  respectively. Let us remind that

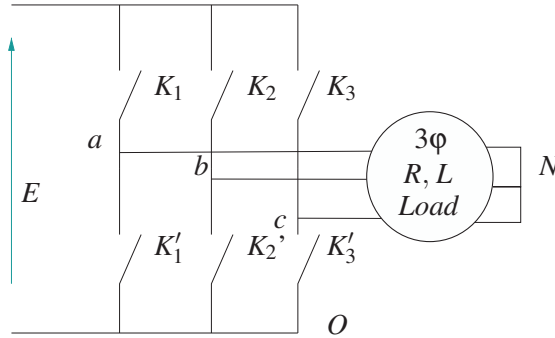


Figure 3: Three-phase voltage source inverter.

$$\begin{cases} c_i = \bar{c}'_i = 1 \Rightarrow K_i \text{ is ON, } K'_i \text{ is OFF,} \\ c_i = \bar{c}'_i = 0 \Rightarrow K_i \text{ is OFF, } K'_i \text{ is ON.} \end{cases} \quad (1)$$

For each ideal switch, the duty cycle applied on a switching period  $T_s$ , is defined as

$$\alpha_i = \frac{t_{iON}}{T_s} \quad \text{with } i \in \{1, 2, 3\}. \quad (2)$$

where  $t_{iON}$  is the time when  $K_i$  is switched on. Then the mean value of the line voltages  $V_{aO}(t), V_{bO}(t), V_{cO}(t)$  is expressed as

$$\langle V_{iO}(t) \rangle_{T_s} = \frac{1}{T_s} \int_0^{T_s} V_{iO}(t) dt = \alpha_i E. \quad (3)$$

For simplicity sake, we assume that the 3 $\phi$   $R-L$  load is balanced,  $Z = Z_1 = Z_2 = Z_3$  with  $Z$  the complex impedance, and star-connected. An average model of the inverter is considered and each average voltage

$\langle V_{iO}(t) \rangle_{T_s}$  is denoted as  $V_{iO} = \alpha_i E$ . The load neutral voltage is given by

$$V_{NO}(t) = \frac{1}{3}(V_{aO}(t) + V_{bO}(t) + V_{cO}(t)) = \frac{1}{3}[1 \quad 1 \quad 1]V_{iO}(t) \quad (4)$$

with  $V_{iO}(t) = [V_{aO}(t) \quad V_{bO}(t) \quad V_{cO}(t)]^T$ . Using the average expression  $V_{iN} = V_{iO} - V_{NO}$ , the load voltages are expressed as

$$V_{iN} = [V_{aN} \quad V_{bN} \quad V_{cN}]^T = V_{iO} - [1 \quad 1 \quad 1]^T V_{NO}. \quad (5)$$

Taking into account (3), in equations (4) and (5), leads to

$$V_{iN} = \frac{E}{3} \begin{bmatrix} 2 & -1 & -1 \\ -1 & 2 & -1 \\ -1 & -1 & 2 \end{bmatrix} \alpha. \quad (6)$$

Eq. (6) relates the load voltage average and the duty cycle  $\alpha$ , if

$$\alpha = [\alpha_1 \quad \alpha_2 \quad \alpha_3]^T. \quad (7)$$

Consequently,

$$V_{iN} = \frac{E}{3} M \alpha \quad \text{with} \quad M = \begin{bmatrix} 2 & -1 & -1 \\ -1 & 2 & -1 \\ -1 & -1 & 2 \end{bmatrix}. \quad (8)$$

## VSI simplified model

Because of

$$V_{aN} + V_{bN} + V_{cN} = 0, \quad (9)$$

thus,

$$\text{rank}(M) = \text{rank}([M V_{iN}]) = 2. \quad (10)$$

Following usual results on linear system [9], the expression (8) is reduced to

$$W = N \alpha, \quad (11)$$

where  $W = \begin{bmatrix} V_{aN} \\ V_{bN} \end{bmatrix}$  and  $N = \frac{E}{3} \begin{bmatrix} 2 & -1 & -1 \\ -1 & 2 & -1 \end{bmatrix}$ . This expression is a compatible system with an infinite number of solutions. Indeed, the VSI simplified model is finally obtained. It links the load voltages and the duty cycles with the non square matrix  $N$ .

The regulation strategy provides the load voltage reference vector,  $V_{ref} = [V_{refaN} \quad V_{refbN} \quad V_{refcN}]^T$ . So that,  $W_{ref}$  is expressed by

$$W_{ref} = \begin{bmatrix} V_{refaN} \\ V_{refbN} \end{bmatrix}. \quad (12)$$

Due to singularity of the  $N$  matrix, we cannot obtain the reference duty cycle by applying the usual inverse. Effectively, it does not exist. The purpose of the next section is to overcome this drawback.

## Generalized Inverse theory, applied to VSI

### Short survey of Generalized Inverse theory

It is well known that each square and non-singular matrix  $A$  has a unique inverse, named  $A^{-1}$ , which satisfies

$$AA^{-1}A = A. \quad (13)$$

At the beginning of the  $XX^{th}$  century, needs for some kind of generalized inverse were pointed out for differential operator [10]. For every matrix, a generalized inverse,  $A^{[1]}$ , is defined such as

$$AA^{[1]}A = A. \quad (14)$$

Some features of the generalized inverse are mentioned [9]: it exists for every matrices; it has some of the properties of the usual inverse; when  $A$  is non-singular,  $A^{[1]} = A^{-1}$ ; it is not unique.

To overcome the last point, based on work of *Moore* [11], [12] and *Penrose* [13], the *Moore-Penrose* inverse or pseudo-inverse of every matrix  $A$ , denoted,  $A^\dagger$  is defined.  $A^\dagger$  satisfies the four *Penrose* equations

$$AA^\dagger A = A \quad A^\dagger AA^\dagger = A^\dagger \quad (15)$$

$$(AA^\dagger)^* = A^\dagger A \quad (A^\dagger A)^* = A^\dagger A \quad (16)$$

where  $A^*$  denotes the conjugate transpose of  $A$ . The main property of  $A^\dagger$  is that for every matrix  $A$ ,  $A^\dagger$  is unique. It is obvious that  $A^\dagger$  is a particular generalized inverse of  $A$ . The main application of generalized inverse theory is to get the solution set of linear systems [14]. Let us consider a linear system described by

$$AX = B \quad (17)$$

where  $A$  is a  $[n * m]$  matrix and  $B$  is a  $[n * p]$  matrix. Then, if (17) is compatible, the general solution of (17) is

$$X = A^{[1]}B + (I_m - A^{[1]}A)Y \quad (18)$$

where  $Y$  is an arbitrary  $(m * p)$  matrix and  $A^{[1]}$  is a generalized inverse of  $A$ . It is obvious that  $Y$  can be chosen in order to satisfy at the outset, some fixed constraints. The  $A^{[1]}B$  part in (18) stands for the basis solution, namely obtained with  $Y = 0$ . It depends on the particular choice for  $A^{[1]}$ . As  $A^\dagger$  is a particular generalized inverse, the solution set (18) is generated as

$$X = A^\dagger B + (I_m - A^\dagger A)Z \quad (19)$$

where  $Z$  is an arbitrary  $(m * p)$  matrix. Despite the fact that the basis solution in (18) and (19) is distinct, the solution sets (18) and (19) are identical. The interest of considering  $A^\dagger$  is to benefit of stable numeric algorithms to obtain this unique matrix [15]. We must insist here that the dimension of the solution space is given by

$$\dim(\text{Ker}A) = \text{rank}(I - A^{[1]}A) = \text{rank}(I - A^\dagger A) \quad (20)$$

### Generalized inverse applied to 3φ VSI model

By using the results of the previous part, the solution set of the duty cycle waveforms is obtained. Thanks to (19), the solution set is expressed as

$$\alpha = N^\dagger W + (I_3 - N^\dagger N)z \quad (21)$$

where  $W = [V_{ref_{aN}} \ V_{ref_{bN}}]^T$  is the output reference voltages given by regulators as illustrated in Fig. 2. Firstly a global  $z$  such as  $z = [z_1 \ z_2 \ z_3]^T$  is considered. Then, as  $\text{rank}(N) = 2$ ,  $N^\dagger$  is given by [9]:

$$N^\dagger = N^T (NN^T)^{-1} = \frac{3}{E} \begin{bmatrix} \frac{1}{3} & 0 \\ 0 & \frac{1}{3} \\ -\frac{1}{3} & -\frac{1}{3} \end{bmatrix}.$$

Finally, this leads to express the duty cycle vector  $\alpha$  as

$$\alpha = \frac{1}{E} \begin{bmatrix} 1 & 0 \\ 0 & 1 \\ -1 & -1 \end{bmatrix} \begin{bmatrix} V_{ref_{aN}} \\ V_{ref_{bN}} \end{bmatrix} + \frac{1}{3} \begin{bmatrix} 1 & 1 & 1 \\ 1 & 1 & 1 \\ 1 & 1 & 1 \end{bmatrix} \begin{bmatrix} z_1 \\ z_2 \\ z_3 \end{bmatrix} = \frac{1}{E} \begin{bmatrix} V_{ref_{aN}} \\ V_{ref_{bN}} \\ V_{ref_{cN}} \end{bmatrix} + \begin{bmatrix} 1 \\ 1 \\ 1 \end{bmatrix} \lambda \quad (22)$$

where

$$\lambda = \frac{z_1 + z_2 + z_3}{3}. \quad (23)$$

Finally, with  $U = [1 \ 1 \ 1]^T$ ,

$$\alpha = \frac{V_{ref}}{E} + \lambda U \quad (24)$$

where  $\lambda$  is the scalar degree of freedom leading to the solution set. It has to be defined by using the compatibility expression given in (9). Then  $\lambda$  is

$$\lambda = \frac{\alpha_1 + \alpha_2 + \alpha_3}{3}. \quad (25)$$

Moreover, for each  $i \in \{1, 2, 3\}$ , the duty cycle is included within  $0 \leq \alpha_i \leq 1$ . Then, Eq. (24) implies

$$-\frac{\min(V_{ref})}{E} \leq \lambda \leq 1 - \frac{\max(V_{ref})}{E}. \quad (26)$$

Nevertheless, the solution obtained in (24) is not completely satisfying because it is not directly linked with most of classical carrier based PWM in use. In next section a method is developed to recover classical solutions.

### Translation of the basis solution

Starting with (24), two new variables  $\mu$  and  $\lambda_b$  are introduced such as

$$\lambda = \lambda_b + \mu. \quad (27)$$

Then the general solution is given by

$$\alpha = \frac{V_{ref}}{E} + \lambda_b U + \mu U = \alpha_b + \mu U \quad (28)$$

with  $\alpha_b$  the basis solution defined as

$$\alpha_b = \frac{V_{ref}}{E} + \lambda_b U. \quad (29)$$

In the following section is detailed that fixing  $\lambda_b$  and  $\mu$  allows to express some well known PWM solutions. According to the given reference voltages  $V_{ref}$ , the values or boundaries of the freedom parameters  $\mu$  and  $\lambda_b$  are expressed.

## Simulation results

### Simulation context

Scilab, which is an open source software for matrix calculus and system simulations, is used [16]. The system described in Fig. 3 is simulated using a 3 $\phi$   $RL$  load. A state space model is introduced in order to obtain the line currents

$$I(\dot{t}) = AI(t) + BV_{IN}(t) \quad \Leftrightarrow \quad I(\dot{t}) = -\frac{R}{L}I(t) + \frac{1}{L}V_{IN}(t) \quad (30)$$

with  $R = 15\Omega$  and  $L = 1mH$ . The line potential  $+E$  or  $0$  is applied to the  $\{a, b, c\}$  line points such as  $V_{IN}(t) = [V_{aN} V_{bN} V_{cN}]^T$ . It is deduced from the switching binary order applied to each ideal switch. In order to obtain these switching signals, the regular PWM is implemented, as given in Fig. 2. In such a scheme, a 3 $\phi$  load reference voltage is applied,

$$V_{ref}(t) = \left[ V_{max} \cos(2\pi f * t) \quad V_{max} \cos\left(2\pi f * t - \frac{2\pi}{3}\right) \quad V_{max} \cos\left(2\pi f * t - \frac{4\pi}{3}\right) \right]^T. \quad (31)$$

Their frequencies are fixed,  $f = 50Hz$ , whereas their peak values can be changed,  $V_{max} \in [0; 325]$ , with  $E = 562V$ . Then the reference voltage is sampled by a zero-order hold. It is noted as  $V_{ref,k}$ . Obviously the PWM solution set is obtained by (28). Finally, the duty cycles to apply are compared to a triangle carrier. In this study the carrier frequency is fixed at  $10kHz$ . Consequently, the duty cycle values are constant during each switching period  $T_s = 0.1ms$ .

## Sinusoidal PWM

To check the accuracy of our model, this basic PWM is simulated. It is obtained with

$$\lambda_b = \frac{1}{2} \quad \text{and} \quad \mu = 0 \quad (32)$$

in the main duty cycle expression given in equation (28). Finally the duty cycle is expressed as

$$\alpha = \frac{V_{ref,k}}{E} + \frac{1}{2}U. \quad (33)$$

The sampled duty cycle  $\alpha$  illustrated in Fig. 4, is compared to the triangle carrier. In such example, two  $V_{max}$  values are applied:

$$V_{max} = \begin{cases} \frac{E}{3} & \text{if } 0 \leq \text{Time} < 0.03s, \\ \frac{E}{2} & \text{if } 0.03s \leq \text{Time} \leq 0.06s, \end{cases} \quad (34)$$

The obtained 3 $\phi$  currents are given in Fig. 5.

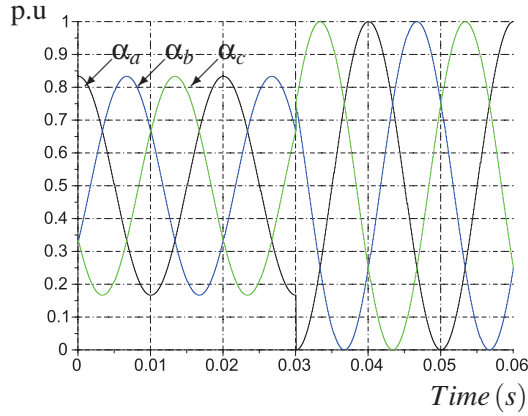


Figure 4: Sampled duty cycle  $\alpha$ .

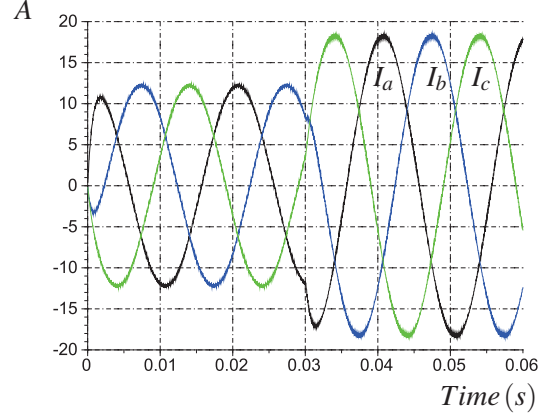


Figure 5: Sinusoidal PWM - 3 $\phi$  currents.

A zoom view of the comparison of the triangle carrier and the sampled duty cycle is provided in Fig. 6. It is noted that for  $V_{ref,k}$  values higher than  $\frac{E}{2}$ , the voltage linearity zone is not ensured. The maximal possible value is obtained for  $V_{max} = \frac{E}{2}$ . Indeed, each duty cycle achieves his maximum  $\alpha_i = 1$ , for appropriate time, as illustrated in Fig. 4.

## Zero Sequence Signal PWM

This technique is used in order to extend the linearity zone of PWM, [17]. Some studies such as [18] reveals that the linearity zone of PWM is linked to the modulation index value. This index allows to quantify the fundamental of the load voltage, and is expressed as

$$m_i = \frac{V_{max}}{V_{six-step}}. \quad (35)$$

$V_{six-step}$  is the maximum fundamental value of a square wave.

To obtain the Zero Sequence Signal PWM, a zero sequence voltage  $\frac{V_{NO}}{E}$  is added to the duty cycle obtained in a SPWM, Eq. (33). It leads to a new expression of duty cycles

$$\alpha = \frac{V_{ref,k}}{E} + \frac{1}{2}U + \frac{V_{NO}}{E}U. \quad (36)$$

Considering (28) and (36), it is obvious that

$$\lambda_b = \frac{1}{2} \quad \text{and} \quad \mu = \frac{V_{NO}}{E} \quad (37)$$



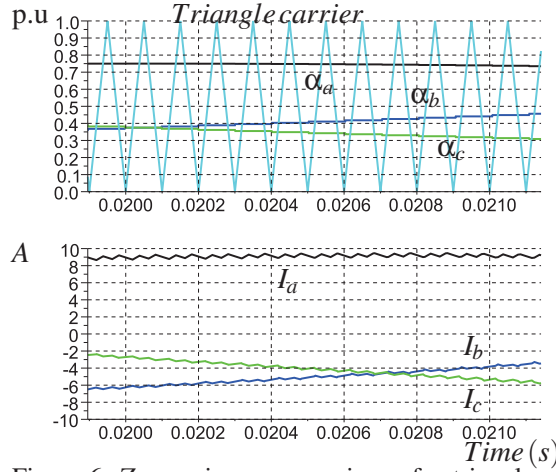


Figure 6: Zoom view - comparison of  $\alpha$  triangle carrier, followed by the 3 $\phi$  currents.

fulfill the degrees of freedom of the solution set to obtain (36). The most common case encountered is

$$V_{NO} = \frac{V_{med}}{2}, \quad (38)$$

with  $V_{med}$  the medium value of the load voltage  $V_{LN}$ . For instance, if  $V_{aN} \leq V_{bN} \leq V_{cN}$ , then  $V_{med} = V_{bN}$ . This is expressed as

$$\frac{V_{med}}{2} = -\frac{\max(V_{ref,k}) + \min(V_{ref,k})}{2} = V_{NO}. \quad (39)$$

Then,  $V_{max}$  can reach the maximal line voltage value,  $\frac{E}{\sqrt{3}}$ . In Fig. 7 the duty cycles applied are illustrated. The  $\mu$  term, which corresponds to  $\frac{V_{NO}}{E}$  is also displayed. The resulted currents point out the good behaviour of the model.

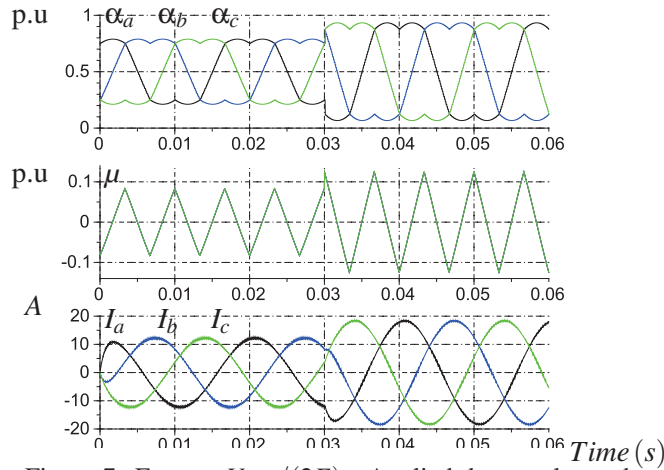


Figure 7: For  $\mu = V_{med}/(2E)$  - Applied duty cycles and obtained currents.

## $\mu$ optimisation

### The linearity voltage zone

To analyze the  $\mu$  optimisation, the linearity voltage zone is defined as the main criterion. It is monitored thanks to the modulation ratio expressed in (35). On the one hand, using a Sinusoidal PWM this ratio is

known to reach  $m_i = \frac{\pi}{4} \approx 0.785$ . This modulation ratio is reached for  $V_{max} = \frac{E}{2}$ . Whereas  $V_{six-step} = \frac{2E}{\pi}$  is the maximum fundamental value of a square wave. Each output voltage allowed  $\in \left\{ \frac{-2E}{3}, \frac{-E}{3}, \frac{E}{3}, \frac{2E}{3} \right\}$ , is maintained during a time period of  $\frac{1}{6f}$ . On the other hand, using a Zero Sequence Signal PWM leads to consider a maximal voltage value  $V_{max} = \frac{E}{\sqrt{3}}$ . Consequently, the modulation index is increased:  $m_i = 0.907$ .

Following this, the sweeping range for  $\mu$  is established using (28) and (26). Indeed the degree of freedom is expressed. Considering  $\lambda_b = \frac{1}{2}$  implies

$$-\frac{\min(V_{ref,k})}{E} - \frac{1}{2} \leq \mu \leq \frac{1}{2} - \frac{\max(V_{ref,k})}{E}. \quad (40)$$

Thanks Eq. (40), the high and low margins,  $\mu_{high}$ ,  $\mu_{low}$ , are expressed

$$\mu_{low} \leq \mu \leq \mu_{high}. \quad (41)$$

Each margin is dependent on the maximal or the minimal values of the desired voltage. It is obvious that (40) is true only if

$$\mu_{low} \leq \mu_{high} \Leftrightarrow \max(V_{ref,k}) - \min(V_{ref,k}) \leq E. \quad (42)$$

Moreover,  $\max(V_{ref,k}) - \min(V_{ref,k})$  is the maximal value of the line voltages  $V_{ab}$ ,  $V_{bc}$  and  $V_{ca}$ . It is established by

$$\max(V_{ref,k}) - \min(V_{ref,k}) = \max(V_{ll}) = \sqrt{3}V_{max}, \quad (43)$$

with  $V_{ll} = [V_{ab} \ V_{bc} \ V_{ca}]^T$ . It is concluded that, for a three-phase VSI as illustrated in Fig. 3, the maximal linearity voltage zone is ensured while the phase voltage involved in the PWM process is

$$V_{max} \leq \frac{E}{\sqrt{3}}. \quad (44)$$

To illustrate such degree of freedom, a first obvious way to ensure (40), is to consider the average point between the two margins such as

$$\mu = \frac{\mu_{high} + \mu_{low}}{2}. \quad (45)$$

This leads to express

$$\mu = -\frac{1}{2E} (\max(V_{ref,k}) + \min(V_{ref,k})) \quad (46)$$

which is exactly the  $\frac{V_{NO}}{E}$  expression given in Eq. (38) and Eq. (39).

Let us now consider the Sinusoidal PWM (cf. section ) and the Zero Sequence Signal PWM (cf. section ) simulations. Following (31), the  $\mu$  values,  $\mu_S$  and  $\mu_{ZSS}$  respectively, are computed. They are related to the 3 reference peak voltages:  $V_{max} \in \left\{ \frac{E}{3}, \frac{E}{2}, \frac{E}{\sqrt{3}} \right\}$ . The result is presented in Fig. 8. This figure is obtained thanks to

$$V_{max} = \begin{cases} \frac{E}{3} & \text{if } 0 \leq \text{Time} < 0.03s, \\ \frac{E}{2} & \text{if } 0.03s \leq \text{Time} < 0.06s, \\ \frac{E}{\sqrt{3}} & \text{if } 0.06s \leq \text{Time} \leq 0.1s. \end{cases} \quad (47)$$

The last case corresponds to the maximal value allowed in order to reach the maximal value of the modulation index. As illustrated in Fig. 8, it becomes obvious that the Sinusoidal PWM do not match anymore the linearity assessment (40) when  $V_{max} > \frac{E}{2}$ . As a consequence, the duty cycles for each PWM reach their maximal value when  $V_{max} = \frac{E}{2}$ ,  $V_{max} = \frac{E}{\sqrt{3}}$  respectively, as shown in Fig. 9.

Fig. 8 also highlights that within the  $\mu_{high}\mu_{low}$  margins, the maximal linearity ratio  $m_i$  is ensured. It is concluded that all the PWM carrier based solutions are included within the solution set (28). Moreover it is observed that the  $\mu$  waveform is not unique. This means that other possible solutions which fulfill other criterions, can be highlight.

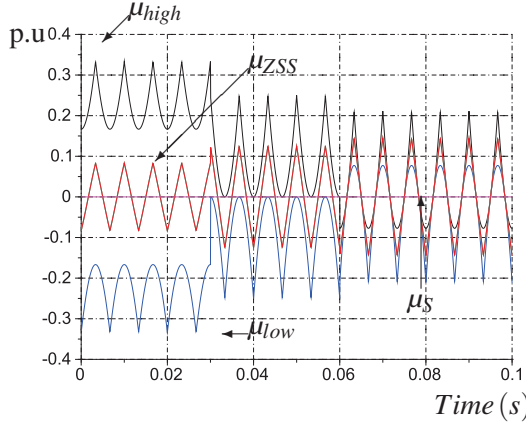


Figure 8:  $\mu$  margins and  $\mu$  waveforms expressed for Si-

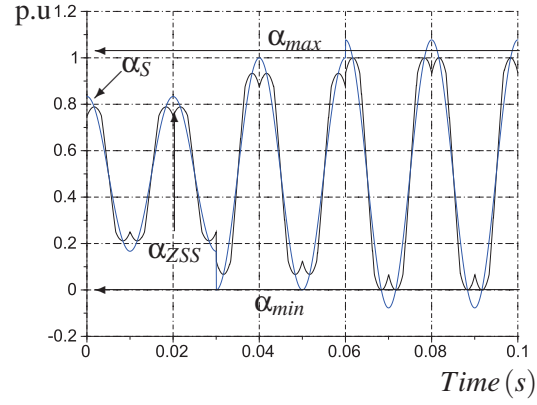


Figure 9: Duty cycle waveforms of the first VSI leg, nusoidal PWM -  $\mu_S$  and Zero Sequence Signal PWM - expressed for Sinusoidal PWM -  $\alpha_S$  and Zero Sequence Signal PWM -  $\alpha_{ZSS}$ .

## Switching losses

For instance, while the linearity ratio is ensured,  $\mu$  is now set in order to reduce the switching losses. Effectively, when the duty cycle  $\alpha$  is set to 1 or 0, the corresponding VSI leg is not switched throughout a switching period  $T_s$ . Then the losses are naturally decreasing. Let us consider  $\mu$  arbitrary fixed as

$$\mu = \mu_{high}. \quad (48)$$

As illustrated in Fig. 10, the duty cylce waveforms change. For some switching period they are clamped to 1. It is demonstrated in Fig. 11 that the general shape of the currents is still correct, whereas the leg

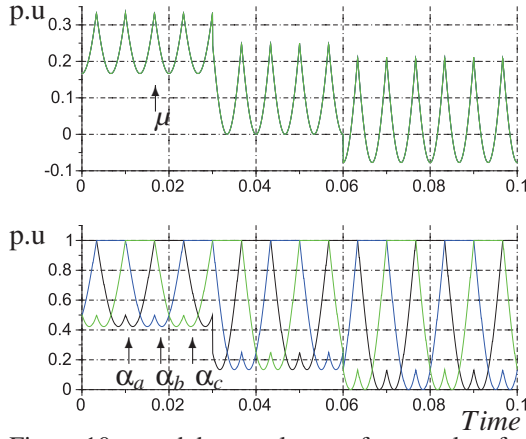


Figure 10:  $\mu$  and duty cycle waveforms, taken for reduc-

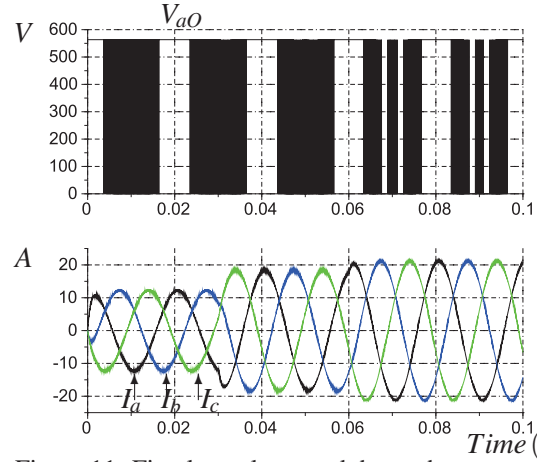


Figure 11: First leg voltage and three-phase currents obtained with  $\mu$  taken for reducing switching losses.

voltage is clamped to  $E$  for more than one switching period. Of course it is linked with  $\alpha_i = 1$ . In such example, if  $\mu$  satisfies,

$$\mu_{low} \leq \mu = \mu_{high}, \quad (49)$$

an optimized fonctionning is ensured as far as the linearity voltage zone and the switching losses are concerned.

## Conclusion

In this paper a mathematical solution set for duty cycles used in carrier based PWM technique applied to Voltage Source Inverters is established. Using the generalized inverse theory, the solution set for the duty cycle is expressed. Two degrees of freedom that allows to describe some PWM published before are

pointed out. Using matrix calculus and simulation software, it is demonstrated that each usual solution is included within admissible margins. This approach permits today to recover the known solutions but our works continue to explore new mathematical solutions that have interesting features as regards to the conversion of energy.

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