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# Study of supercapacitor ageing and lifetime estimation according to voltage, temperature and RMS current

P. KRECZANIK, P. VENET, A. HIJAZI, G. CLERC

**Abstract**—Due to its capacity to store or supply energy with high power, the supercapacitor is becoming an attractive component. Because of the electrostatic nature of energy storage, the endurance of this component toward repetitive charge and discharge cycles is relatively high. The goal of this paper is to demonstrate that the cycling has an impact on the degradation of the supercapacitor and as a result on its lifetime. Based on accelerated cycling tests, some supercapacitors were studied using a dedicated test bench. Temperature, voltage, and current, that are the parameters which accelerate ageing, are monitored. In fact, observations during cycling tests show an important acceleration of the degradation compared to a similar static test having the same voltage and core temperature but without cycling. This paper proposes a method to quantify the acceleration of ageing during cycling phase.

**Keywords**— *Supercapacitor; ultracapacitor; electrochemical double layer capacitor; calendar test; cycling test; lifetime; accelerated ageing; ageing law.*

## I. INTRODUCTION

The lifetime and reliability of storage systems are important factors to determine the feasibility of electromobility solution. In this study, the target application concerns the supply of trolleybuses. It is characterized by the presence of quasi-permanent electric air source, often not reversible providing power to the trolley. The addition of on-board storage system gives the possibility to recover braking energy and fed up trolleybus in case of electrical microcuts which occurs frequently due to different reasons like the presence of insulator, the vibration of trolley's poles, ...

The duration of electrical cuts is relatively small so that the amount of energy to be embarked is low compared to the operating power induced by the equivalent weight of the trolleybus (taking into account the slope of the road). Supercapacitors are potentially interesting to meet these specifications because of their high specific power and good endurance toward cycling. Different works in literature have studied the influence of temperature and voltage on the ageing of supercapacitors [1-5]. However in our paper, we seek to introduce additional factor, which is the effective current, to

represent the effect of cycling on the lifetime of the system [6-10].

This paper is organized as following:

In the first part, we introduce the test bench used and all cycling tests that are performed in this study. After that, a method is proposed to characterize ageing of supercapacitors during cycling. Finally, a simple thermal model is developed to deduce the temperatures of components.

In the second part, we use the results provided by manufacturer to deduce the equation of lifetime as a function of voltage and temperature. From this equation, we deduce what we call “equivalent calendar degradation” that estimate the ageing of components with respect to the evolution of both temperature and voltage.

The third part presents certain results obtained during cycling. The comparison between the degradations observed during experience and those estimated by “equivalent calendar degradation” shows an important acceleration of ageing caused by cycling.

The fourth part makes an overview about the factors that need to be added to the “equivalent calendar degradation” in order to accurately represent the degradation during cycling. New ageing law which includes the effect of RMS current and by consequence of cycling is proposed and discussed.

## II. GENERAL PRESENTATION OF THE TEST BENCH

### A. Test bench presentation

The test bench is composed of a power supply, a data acquisition system and a pack of 4 supercapacitors in series. Fig. 1 shows these different elements.



(a) Test bench (b) supercapacitors of one pack  
Fig. 1: Photo of the test bench

Based on their manufacturer datasheet, each component supports a rated voltage ( $V_r$ ) of 2.7 V for a capacitance ( $C_0$ ) of 3000 F and an ESR (Equivalent Series Resistance;  $R_0$ ) of 0,27m $\Omega$ . No charge balancing device was implemented on purpose to decrease the voltage dispersion between components during cycling tests.

Five packs of 4 serial supercapacitors were cycled (Table I). At least two measurements are provided for each component: terminal voltage and case temperature. For some components, the terminal temperature has also been measured (Fig. 1b).

All packs are charged and discharged with a constant power. Fig. 2 shows the current and voltage profiles for a constant power of 2600 W. The voltage across the stack are limited between 5.4 V ( $= 4 \cdot V_r/2$ ) and 10.8 V ( $= 4 \cdot V_r$ ) (Fig.2).

After each charge and discharge cycle, a break phase is added. In this phase, the current decreases to zero and by consequence the power is null. The durations of this break time are equal to  $t_{break1}$  and  $t_{break2}$  corresponding to the break phase after charge or discharge cycles. These duration, that are chosen to be identical ( $t_{break1} = t_{break2}$ ), determines mainly the RMS current and therefore the heating of supercapacitors. The first four packs were cycled in the ambient air with free convection in an air conditioned room at 24 ° C. The last one took place in a climatic chamber at 10°C under forced convection. **This allows to increase the RMS current without exceeding the maximum core temperature.**

TABLE I. CYCLING CONFIGURATION TABLE

Cycling configuration	Input parameters			Others parameters	
	Charge/Discharge Power	$T_{amb}$	$t_{break}$ (1 or 2)	$\langle I_{RMS} \rangle$	$\langle \theta_m \rangle$
Pack 1	P = 2600 W	24 °C	22.5 s	158 A	63 °C
Pack 2	P = 2600 W	24 °C	40.7 s	122 A	42 °C
Pack 3	P = 2600 W	24 °C	31.1 s	148 A	47 °C
Pack 4	P = 2000 W	24 °C	17.6 s	159 A	45 °C
Pack 5	P = 2600 W	10 °C	12.4 s	197 A	22 °C

$\langle I_{RMS} \rangle$  and  $\langle \theta_m \rangle$  are the mean value respectively of RMS current and case temperature measured during all cycling test.

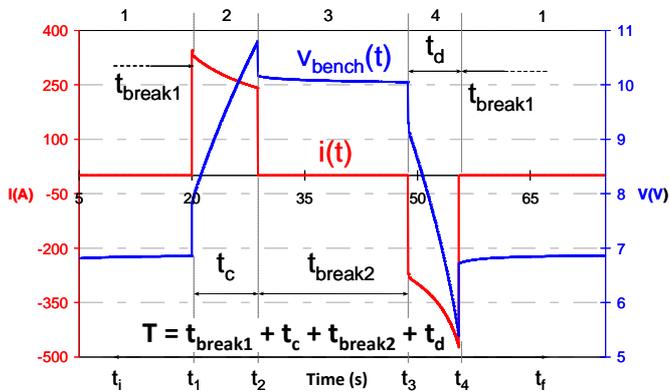


Fig. 2: Power cycling profile (2600 W)

## B. Characterization method

The manufacturer determines the ESR of the components based on the variation of the voltage caused by the extinction of discharge current (2) [7]. As far as the capacity are concerned, it is calculated based on the amount of charge delivered at a constant discharge current and the corresponding voltage variation (4). Our method is the same as the manufacturer method by taking into consideration the parameters that are determined in charge phase of the component (1) (3). Fig.3 illustrates the characterization method.

$$R_c = \left| \frac{V_{c\_max} - V_{c\_f}}{I_{c\_min}} \right| = \left| \frac{\Delta V_{Rc}}{I_{c\_min}} \right| \quad (1)$$

$$R_d = \left| \frac{V_{d\_min} - V_{d\_f}}{I_{d\_max}} \right| = \left| \frac{\Delta V_{Rd}}{I_{d\_max}} \right| \quad (2)$$

$$C_c = \frac{\int_{t_1}^{t_2} i(t) dt}{V(t_2+5) - V(t_1)} = \frac{Q_c}{V_{c\_f} - V_{c\_min}} \quad (3)$$

$$C_d = \frac{\int_{t_3}^{t_4} i(t) dt}{V(t_3) - V(t_4+5)} = \frac{Q_d}{V_{d\_max} - V_{d\_f}} \quad (4)$$

Due to the difficulties to present all the results for each component, we present only the average of the measured parameters as following:

$$R_m = \frac{R_c + R_d}{2} \quad (5)$$

$$C_m = \frac{C_c + C_d}{2} \quad (6)$$

With:  $R_m$  the measured equivalent series resistance [ $\Omega$ ],  $C_m$  the measured capacitance [F].

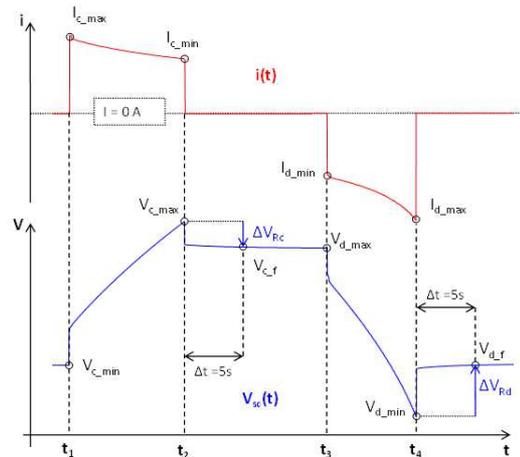


Fig. 3 : Specifics points on cycle charge/discharge with their notations

### C. Thermal model

To estimate the core and case temperatures of components, we have used and characterized a simplified thermal model as presented in fig.4 [11]:

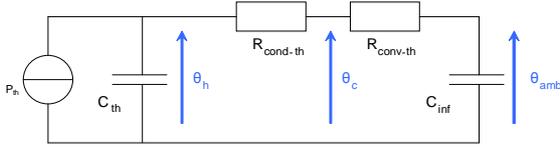


Fig. 4: Thermal model

- $C_{th}$  is the thermal capacity of supercapacitors ( $J.K^{-1}$ ). The voltage ( $\theta_h$ ) across it represents the temperature at the **core** (or heart) of the component.
- $R_{cond-th}$  is the thermal resistor which represents the conduction phenomena ( $K.W^{-1}$ ). It models the heat transfer from the core of the component to its case.
- $R_{conv-th}$  is the thermal convection resistor ( $K.W^{-1}$ ) that models the convection phenomena between case and ambient temperature.
- $C_{inf}$  is the thermal capacity of external environment ( $J.K^{-1}$ ). It is considered as infinite when the ambient temperature is constant.
- $P_{th}$  is the heat dissipation by Joule effect (W). It is supposed to be equal to all losses during charge/discharge cycle, so that:

$$P_{th} = \frac{\int_{t_i}^{t_f} V(t) \times i(t) dt}{(t_f - t_i)} = \frac{\int_{t_c}^{t_d} V(t) \times i(t) dt + \int_{t_i}^{t_d} V(t) \times i(t) dt}{T} = \frac{E_{charge} - E_{discharge}}{T} \quad (7)$$

with  $P_{th}$  the thermal power losses [W],  $E_{charge}$  and  $E_{discharge}$  respectively the **absolute value** of the charging and discharging energy [J],  $t_f$  and  $t_i$  the final and initial time [s] (cf. Fig.2).

For the parameters identification, 150 charge/discharge cycles of 100 A were applied to one component, without break time, and under free convection. The ambient temperature is stabilized to  $24^\circ C$  ( $\theta_{amb} = 24^\circ C$ ) via air conditioner ( $C_{inf}$  equal to infinity). The temperature of the terminal has been assumed to be equal to the temperature inside the supercapacitor due to the good thermal conduction between core and case of the component [11-12]. The following Fig.5 and Table II resume the obtained results:

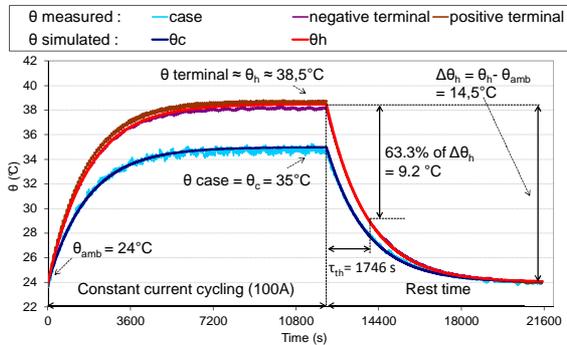


Fig. 5: Measured and simulated temperature.

TABLE II. PARAMETERS OF THERMAL MODEL

Thermal model identification results		
Definitions	Numerical application <sup>a</sup>	Unit
$R_{cond-th} = \frac{\theta_h - \theta_c}{P_{th}}$	$\frac{38,5 - 35}{6,2} = 0,565$	$K.W^{-1}$
$R_{conv-th} = \frac{\theta_c - \theta_{amb}}{P_{th}}$	$\frac{35 - 24}{6,2} = 1,77$	$K.W^{-1}$
$C_{th} = \frac{\tau_{th}}{R_{cond-th} + R_{conv-th}}$	$\frac{1750}{2,34} = 748$	$J.K^{-1}$

<sup>a</sup>. The mean value of  $P_{th}$  is 6.2W (measured for each cycle)

To model the thermal behavior of supercapacitor inside the climatic chamber, only the convection resistor has to be reevaluated. The steady state temperatures measured during tests in the climatic chamber allows us to deduce the convection resistor that is equal to  $0,705 K.W^{-1}$ . The thermal model induces some error due to the fact that it doesn't take into account the position of the supercapacitor inside the packs. However it has been used to estimate the core temperature of component under cycling test.

### III. CALENDAR EQUIVALENT DEGRADATION LAW

To obtain the "calendar equivalent degradation" law, calendar tests were performed. These tests consist of applying different constants voltage and temperature to component and monitoring the electrical parameters variation. This method quantifies the influence of both temperature and voltage on the lifetime.

#### A. Datasheet calendar test result

In calendar test, the climatic chamber temperature ( $\theta$ ) is equal to the case and the core temperature of supercapacitor (respectively  $\theta_c$  and  $\theta_h$ ). Maxwell datasheet for the calendar test gives data presented in table III [13]:

TABLE III. END OF LIFE FOR MANUFACTURER DATA

Datasheet end of life	Voltage across the supercapacitor	
	2.7 V	2.5 V
Chamber temperature:	End of life: <sup>b</sup>	
$\theta = 65^\circ C$	3670 h	7330 h
$\theta = 55^\circ C$	7330 h	14700 h
$\theta = 45^\circ C$	14700 h	29300 h
$\theta = 35^\circ C$	2930 h	58700 h
$\theta = 25^\circ C$	58700 h	117000 h

<sup>b</sup>. End of life  $\Leftrightarrow$  20% reduction of the rated capacitance

We can note that the lifetime is reduced by half when the temperature is increased by  $10^\circ C$  or the applied voltage by

0.2 V [13-14]. Assuming this trend, we can establish a relation between the lifetime and voltage and temperature [9-10]:

$$\tau_s(V; \theta) = \tau_0 \exp\left(-\frac{V}{V_0} - \frac{\theta}{\theta_0}\right) \quad (8)$$

with  $\tau_s$  is the static lifetime [s], V is the constant voltage across the supercapacitor terminals [V] and  $\theta$  the climatic chamber temperature [°C].

Numerical application gives:

$$\tau_0 = 1.4 \times 10^{13} [s]; \quad V_0 = \frac{0.2}{\ln(2)} [V]; \quad \theta_0 = \frac{10}{\ln(2)} [^{\circ}C]$$

The equation (8) is deduced from Arrhenius law generalized to two variables [17].

### B. Extrapolation of calendar results

We suppose that the ageing of supercapacitor is directly depending on the redox reaction rate. So for constant voltage and temperature, the redox reaction rate is considered as constant.

This hypothesis is consistent with the curves presented by the manufacturer which shows a linear variation of the capacitance and ESR during calendar ageing (except at the beginning and at the end of the test) [18]. The inverse of the lifetime is therefore homogeneous to a degradation rate of the component [%·s<sup>-1</sup>]. If the voltage and the temperature were the only parameters that influence the deterioration of the component, this variable, homogeneous to the degradation rate of the component, may be expressed using the following equation:

$$v_d(V; \theta) = \frac{1}{\tau_s(V; \theta)} = \frac{1}{\tau_0 \exp\left(-\frac{V}{V_0} - \frac{\theta}{\theta_0}\right)} = \frac{1}{\tau_0} \exp\left(\frac{V}{V_0} + \frac{\theta}{\theta_0}\right) \quad (9)$$

with  $v_d$  the calendar degradation speed [%·s<sup>-1</sup>]

For a dynamic voltage and/or temperature, the rate of the redox reaction is not constant so the mean value of the reaction rate must be used to estimate the lifetime. Considering that the lifetime is directly proportional to the inverse of reaction rate, the lifetime equation to be used becomes [15-16]:

$$\tau_d(V(t); \theta(t)) = \frac{1}{\frac{1}{t_{end} - t_{init}} \int_{t_{init}}^{t_{end}} v_d(V(t); \theta(t)) dt} = \frac{t_{end} - t_{init}}{\frac{1}{\tau_0} \int_{t_{init}}^{t_{end}} \exp\left(\frac{V(t)}{V_0} + \frac{\theta(t)}{\theta_0}\right) dt} \quad (10)$$

with  $\tau_d$  the dynamic lifetime [s], V(t) dynamic voltage value across the supercapacitor [V] and  $\theta(t)$  dynamic considerate temperature [°C],  $t_{init}$  and  $t_{end}$  are respectively the beginning time and the end time of the entire test [s].

If the cycling test is a succession of n cycles of period T and of index "i" having the same voltage and temperature, the study of a single cycle is sufficient to identify an average speed

of degradation and thus the lifetime of the components is shown in the following equation:

$$\tau_d(V(t); \theta(t)) = \frac{1}{\frac{1}{nT} \sum_{i=1}^{i=n} \int_{(i-1)T}^{iT} v_d(V(t); \theta(t)) dt} = \frac{1}{\frac{n}{nT} \int_{(i-1)T}^{iT} v_d(V(t); \theta(t)) dt} = \frac{1}{\langle v_d \rangle_i} = \langle \tau_d \rangle_i \quad (11)$$

with  $\langle v_d \rangle_i$  a homogenate value of the mean calendar degradation speed during the cycle "i" [%·s<sup>-1</sup>] and  $\langle \tau_d \rangle_i$  the equivalent lifetime [s].

This law is used to quantify the "equivalent calendar degradation" of the capacity or ESR for a given cycle. Indeed, if we consider a linear variation of parameters and supposing that the lifetime (decrease of capacity of 20 % and increase of 100% of ESR) of the component is  $\tau_d$ , the degradation for one cycle could be estimated as following:

$$\Delta C_i = \frac{-0,2 \times C_0}{\langle \tau_d \rangle_i} T \quad (12)$$

$$\Delta R_i = \frac{R_0}{\langle \tau_d \rangle_i} T \quad (13)$$

with  $\Delta C_i$  and  $\Delta R_i$  are respectively the equivalent calendar degradation of capacitance [F] and ESR [ $\Omega$ ] for the cycle "i".

To estimate the capacity and ESR of components solicited by n cycle, we have to make the sum of the degradation as shown in the following equations:

$$C_{deg}(n) = \sum_{i=1}^{i=n} \Delta C_i = \sum_{i=1}^{i=n} -0,2 \times C_0 \times \frac{1}{\tau_0} \int_{(i-1)T}^{iT} \exp\left(\frac{V(t)}{V_0} + \frac{\theta(t)}{\theta_0}\right) dt \quad (14)$$

$$R_{deg}(n) = \sum_{i=1}^{i=n} \Delta R_i = \sum_{i=1}^{i=n} R_0 \times \frac{1}{\tau_0} \int_{(i-1)T}^{iT} \exp\left(\frac{V(t)}{V_0} + \frac{\theta(t)}{\theta_0}\right) dt \quad (15)$$

with  $C_{deg}$  and  $R_{deg}$  are the "calendar equivalent degradation" of "n" cycle respectively for capacitance [F] and ESR [ $\Omega$ ].

If the voltage and temperature were the only parameters that affect the ageing of supercapacitors, using extrapolation, we could predict the lifetime of components with cycling. The extrapolation could predict the life of components when they are solicited by current. Cycling test at given RMS current induce significant heating. Taking into account this temperature rise in the calendar ageing law reduces significantly the estimated lifetime but don't give realistic time.

Our aim is to show that the "equivalent calendar degradation" law underestimates the degradation observed during cycling. To validate this approach, the most critical temperature was used to evaluate the "equivalent calendar degradation". This temperature corresponds to core temperature ( $\theta = \theta_h$  in (14) and (15)). The core temperature was determined by simulating the thermal model in Fig. 4 for all components for a given cycling.

The next paragraph shows that equations (14) and (15) strongly minimize the degradation of supercapacitors when they are subjected to cycling power.

#### IV. RESULTS PRESENTATION FOR THE PACK 5

This paragraph focuses on the results obtained on a single cycling test to explain and illustrate the used method and to show how a speed factor could be extracted from the results. The variables used to estimate the equivalent calendar degradation are presented. The Pack 5 was solicited, in less than 15 days, to approximately 30000 cycles of charge/discharge at constant power of 2600 W with a break time of 12.4 s between each phase in a climatic chamber at 10 °C (Table I).

Fig. 6 shows the individual voltages for 4 supercapacitors for the 200<sup>th</sup> and 30000<sup>th</sup> cycle. Fig. 7 and Fig. 8 indicate the evolution of the effective current and the average terminals temperature (measured) and cores temperature (simulated) of 4 supercapacitors as a function of the number of cycles.

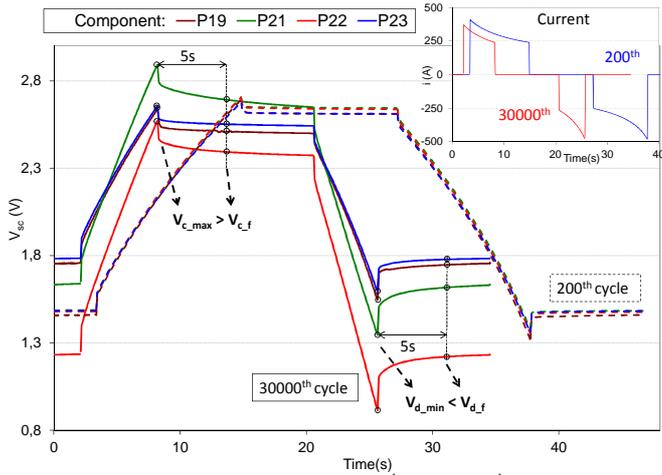


Fig 6: Pack 5 voltage and current for the 200<sup>th</sup> and 30000<sup>th</sup> cycle.

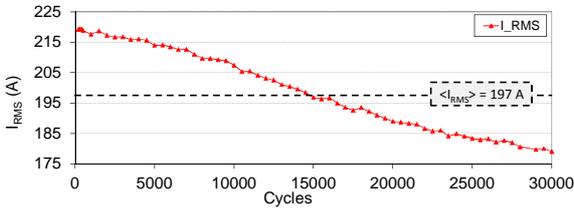


Fig 7: RMS current as a function of the number of cycles

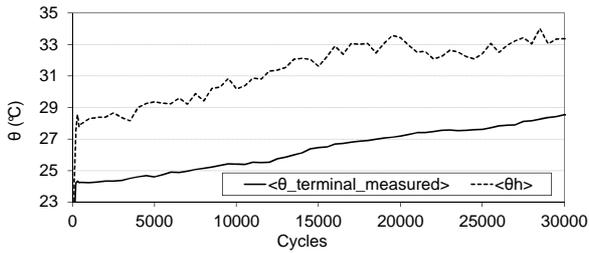


Fig 8: Average value of core components temperature (measured and simulated) as a function of the number of cycles

The loss of capacity and the increase of ESR logically make the charging/discharging time lower. As a result, the effective current decreases during ageing. Its average value over the entire cycling ( $\langle I_{RMS} \rangle$ ) is 197A. In spite of the reduction of the RMS current, the increase of the ESR results in an increase of temperature of the components.

The representative data of a pack is obtained by averaging the results. So the parameters  $\langle C_0 \rangle$ ,  $\langle C_m \rangle$ ,  $\langle C_{deg} \rangle$ ,  $\langle R_0 \rangle$ ,  $\langle R_m \rangle$  and  $\langle R_{deg} \rangle$  are the average of four individual parameters corresponding to 4 supercapacitors constituting the pack. Average degradations observed for the Pack 5 for both capacitance ( $\langle C_m \rangle$ ) and ESR ( $\langle R_m \rangle$ ) with respect to the number of cycle are shown respectively in Fig. 9 and Fig. 10. The "equivalent calendar degradation" law completely underestimates the ageing and must be multiplied by a factor  $\langle K \rangle$  to equalize the estimated degradation to those observed at the end of cycling:

$$\langle Kc \rangle = \frac{\langle C_m(n) \rangle - \langle C_0 \rangle}{\langle C_{deg}(n) \rangle} \quad (16)$$

$$\langle Kr \rangle = \frac{\langle R_m(n) \rangle - \langle R_0 \rangle}{\langle R_{deg}(n) \rangle} \quad (17)$$

with  $\langle Kc \rangle$  and  $\langle Kr \rangle$  respectively the multiplication factor to equalize the "estimated calendar degradation" of capacitance and ESR with the observed degradation during the cycling test.

The observed evolutions of  $\langle C_m \rangle$  and  $\langle R_m \rangle$  during cycling are very close to the following equations "estimated calendar degradation" multiplied by the speed factor ( $\langle Kc \rangle$  or  $\langle Kr \rangle$ ):

$$\langle Kc.Cest(n) \rangle = \langle C_0 \rangle + \langle Kc \rangle \times \langle C_{deg}(n) \rangle \quad (18)$$

$$\langle Kr.Rest(n) \rangle = \langle R_0 \rangle + \langle Kr \rangle \times \langle R_{deg}(n) \rangle \quad (19)$$

with  $\langle Kc.Cest \rangle$  and  $\langle Kr.Rest \rangle$  respectively estimated capacitance and equivalent series resistance with their "calendar equivalent degradation" multiply by their speed factor respectively  $\langle Kc \rangle$  and  $\langle Kr \rangle$ .

The factors  $\langle Kc \rangle$  and  $\langle Kr \rangle$  are obtained by using average values. Similarly,  $\langle Kc.Cest(n) \rangle$  and  $\langle Kr.Rest(n) \rangle$  are defined with the average parameters injected respectively in (18) and (19). However, all these factors are also calculated individually for each component of each Pack (noted without " $\langle \rangle$ "). Fig. 9 and Fig. 10 present these curves with the average parameters of Pack 5:

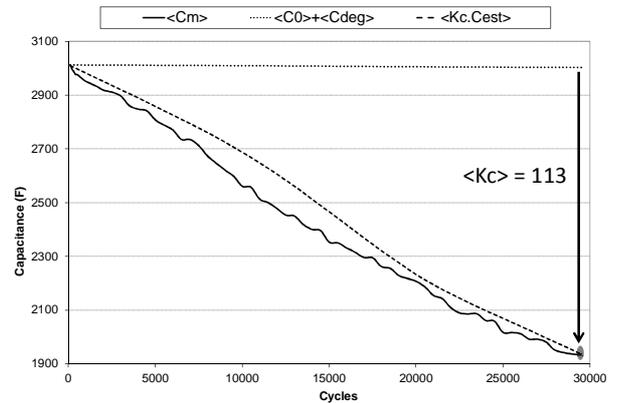


Fig. 9: Comparison of capacitance degradation between measurements ( $\langle C_m \rangle$ ) and estimation of "equivalent calendar degradation" with or without the speed factor  $Kc$  (respectively  $\langle Kc.Cest \rangle$  and  $\langle C_0 \rangle + \langle C_{deg} \rangle$ )

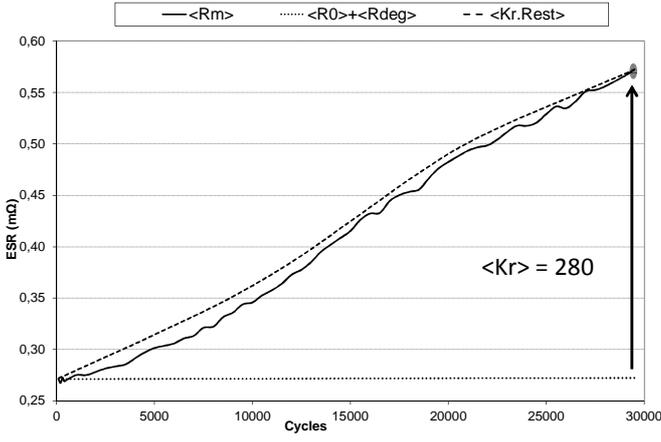


Fig. 10: Comparison of ESR degradation between measurements ( $\langle R_m \rangle$ ) and estimation of "equivalent calendar degradation" with or without the speed factor  $K_r$  (respectively  $\langle K_r \text{ Rest} \rangle$  and  $\langle R_0 \rangle + \langle R_{deg} \rangle$ )

The speed factors  $\langle K_c \rangle$  and  $\langle K_r \rangle$  quantify the increase of the degradation rate of components during cycling. The importance of speed factor demonstrates that the degradation during cycling is greater than one calendar ageing test having equivalent voltage / temperature (Table III). The RMS current is intuitively apparent as the more relevant parameters that identify the magnitude of these factors  $K_r$  and  $K_c$ . Surely the increase in the rate of degradation could dependent on other quantities such as the current waveform, the amplitude of component heat exchange, the depth of charge/discharge...

In this study, the RMS current through cycling is retained to identify speeds factors  $K_c$  and  $K_r$  that must be applied to "equivalent calendar degradation" in order to represent the real degradation.

#### V. DEVELOPMENT OF A NEW AGEING LAW TAKING INTO ACCOUNT THE RMS CURRENT

The ageing by cycling is much faster than that by calendar ageing even though with lower voltage and temperature. This paragraph proposes to identify the speed factors ( $K_c$  and  $K_r$ ) to be applied (multiplied) on the estimate calendar degradation speed to find the estimate cycling degradation (function of the RMS current). The method described above was applied to each package previously described (see Table I) to obtain a multiplicative speed factor  $\langle K_c \rangle$  and  $\langle K_r \rangle$ . Fig. 11 and Fig. 12, in a semi-logarithmic scale, respectively plot the factors  $\langle K_c \rangle$  and  $\langle K_r \rangle$  of each pack according to RMS current measured during different cycling. For a given test (given RMS current), the vertical bar corresponds to the multiplicative factors of various components of the pack (individual  $K_c$  and  $K_r$ ). Exponential shape is identified on six points. One of these points correspond to calendar ageing ( $I_{RMS} = 0$ ,  $K = 1$ ).

From Fig.12, we can deduce that an increase of about 30A to RMS current induce a degradation rate two times greater for ESR. Under these conditions, the lifetime of current cycling is thereby reduced by a factor 2.

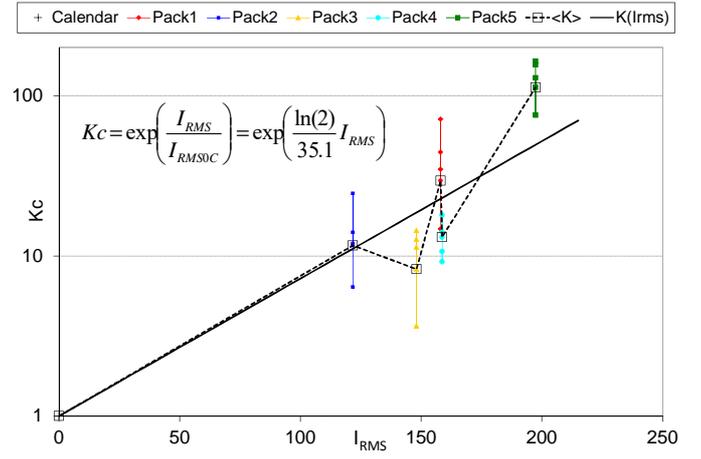


Figure 11: Speed factor of capacitance degradation versus RMS current to applied of the calendar estimation degradation to estimate the degradation during a cycling test

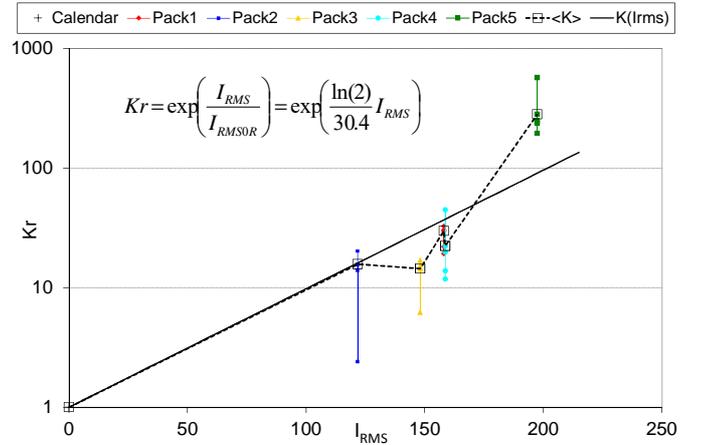


Figure 12: Speed factor of ESR degradation versus RMS current to applied of the calendar estimation degradation to estimate the degradation during a cycling test

In order to integrate the multiplicative factor representing the influence of the RMS current, we propose to use a new equation to estimate the lifetime of supercapacitors:

$$\tau_d(V(t); \theta(t); I_{RMS}) = \frac{t_{end} - t_{init}}{\frac{1}{\tau_0} \int_{t_{init}}^{t_{end}} \exp\left(\frac{V(t)}{V_0} + \frac{\theta(t)}{\theta_0} + \frac{I_{RMS}}{I_{RMS0}}\right) dt} \quad (20)$$

$$\text{with: } I_{RMS0} = \frac{30}{\ln(2)} [A]$$

Equation 20 generalizes Arrhenius law with 3 variables [11]. Figure 13 shows the estimated lifetime versus voltage and RMS current for 3 different temperatures.

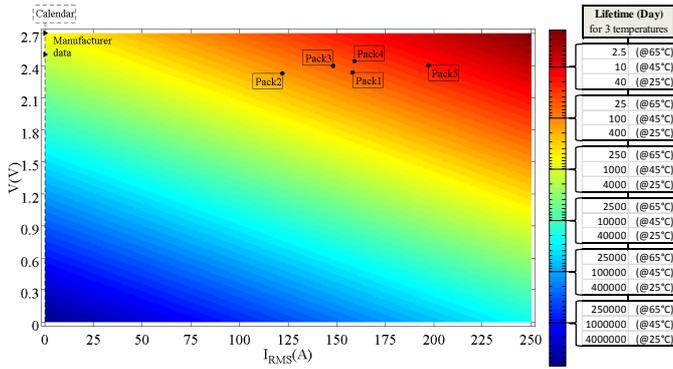


Figure 13: Lifetime estimation according to voltage, RMS current for 3 temperatures

For positioning in Figure 13 the packs studied, their coordinates must be calculated. To do this, the average speed factor due to the voltage  $\langle K_V \rangle$  is calculated by using the following equation:

$$\langle K_V \rangle = \frac{1}{t_{end} - t_{init}} \int_{t_{init}}^{t_{end}} \exp\left(\frac{V(t)}{V_0}\right) dt \quad (21)$$

This factor  $\langle K_V \rangle$  allows the deduction of equivalent voltage  $V_{eq}$  as following:

$$V_{eq} = V_0 \ln(\langle K_V \rangle) \quad (22)$$

This equivalent voltage  $V_{eq}$  generates a voltage speed factor equal to the average observed during the cycling. However, the calculated coordinates are not strictly positioned each pack on its estimated lifetime because the average of a product is not equal to the product of the average (see equation (20)).

Table IV compares estimated lifetime by considering equation (10) and equation (20) with the lifetime observed during the cycling test for each Pack.

TABLE IV. ESTIMATED AND OBSERVED LIFETIME

Lifetime (Day)		Pack 1	Pack 2	Pack 3	Pack 4	Pack 5	Calendar: 2.5V;45°C
Estimated	without $I_{RMS}$	198	863	707	1057	3156	1236
	with $I_{RMS}$	5.13	51.8	23.2	27.1	33.1	1236
Observed during cycling		2.4	59.3	26.6	25.0	8.2	1220 <sup>c</sup>

<sup>c</sup> Manufacturer data

The table IV shows that the estimated lifetime that takes into account the RMS current is more realistic than the other estimation which only uses both voltage and temperature to deduce the lifetime.

## VI. CONCLUSION

The study presented in this paper shows that the degradation rate in cycling ageing test is much higher than that expected by calendar ageing with equivalent voltage and

temperature. To take into account this difference, the law deduced from calendar ageing was modified. An additional term corresponding to the current function is added to make this law more accurate in cycling test.

Regeneration occurs on components after stopping the cycling [10; 19]. This regeneration allows components to recover a significant part of their characteristics. These regeneration characteristics have been the subject of other research studies in our laboratory to [20].

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