

A compact model of precessional spintransfer switching for MTJ with a perpendicular polarizer

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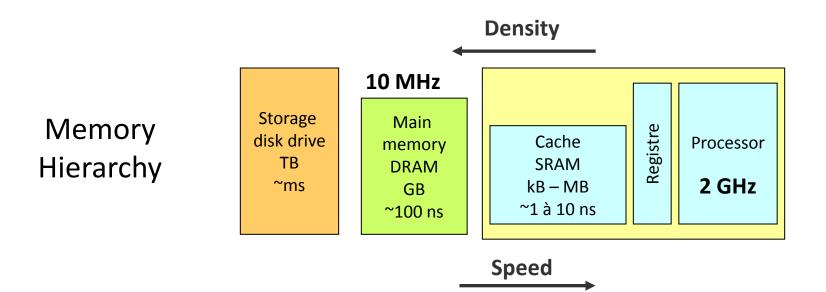




Ultrafast MRAM



Idea: elaborate a **S**pin **T**ransfer **T**orque - **MRAM** with an ultrafast switching to achieve the sub nano second-scale writing time



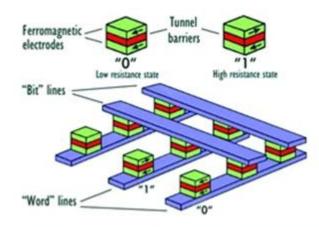
Ultrafast MRAM :

- Replacing SRAM memory
- Building non volatile logic circuits with low-energy consumption

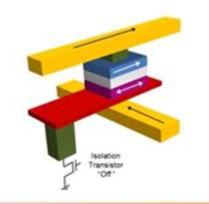


Spin Transfert Torque

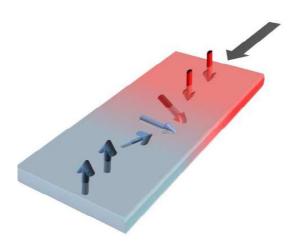
Magnetic Field



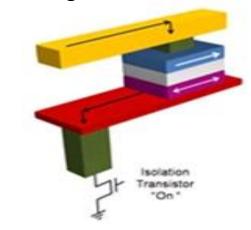
Freescale 2008, Crocus 2011



Spin Transfert



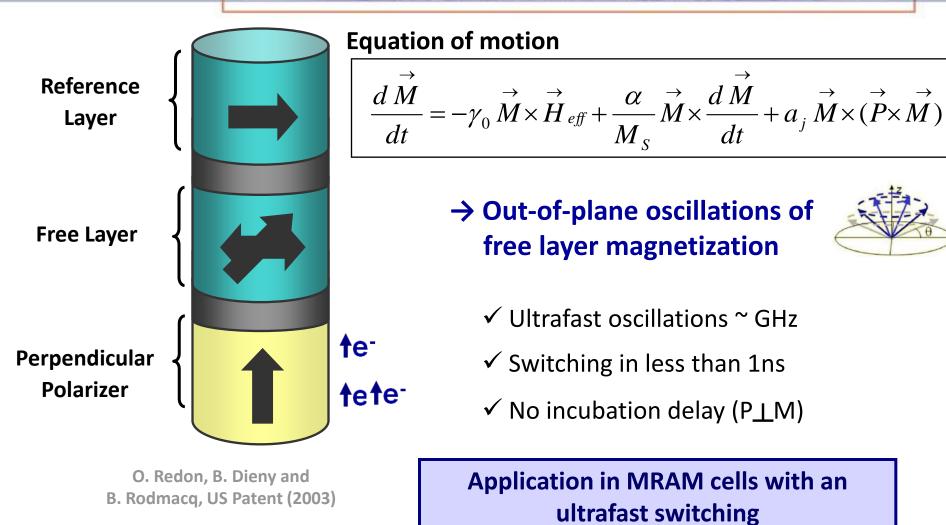
IBM, Samsung, Hitachi





MRAM with perpendicular polarizer

 \rightarrow precessional writing

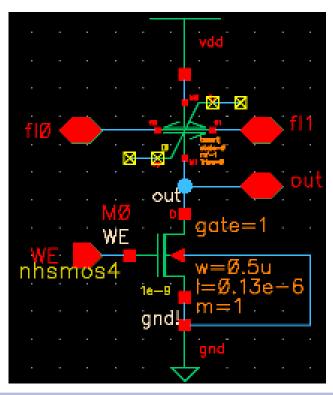


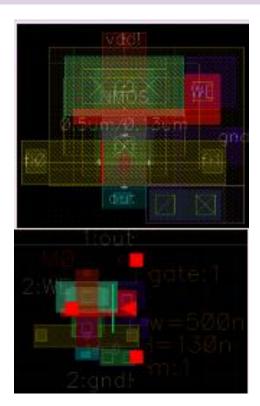


Design

Magnetic technology parameters added to the CMOS technology files:

- Design Rules Checking (DRC)
- Extraction
- Layout Versus Schematic (LVS)







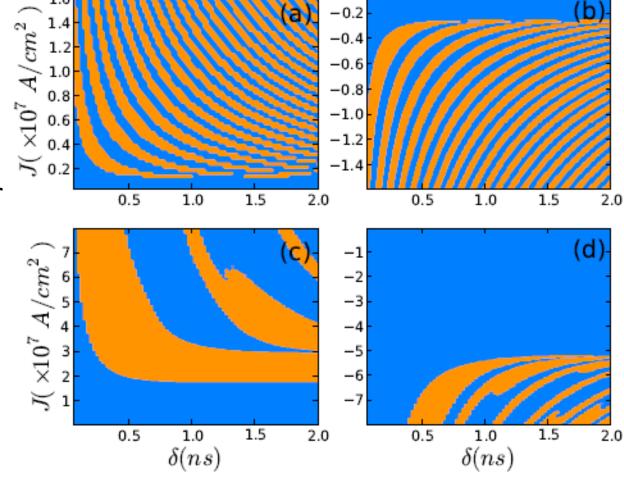
Probability

- Probability of switching

(a) and (b): without analyzer



 $r = \frac{a_{jLONG}}{a_{jPERP}} = 10$

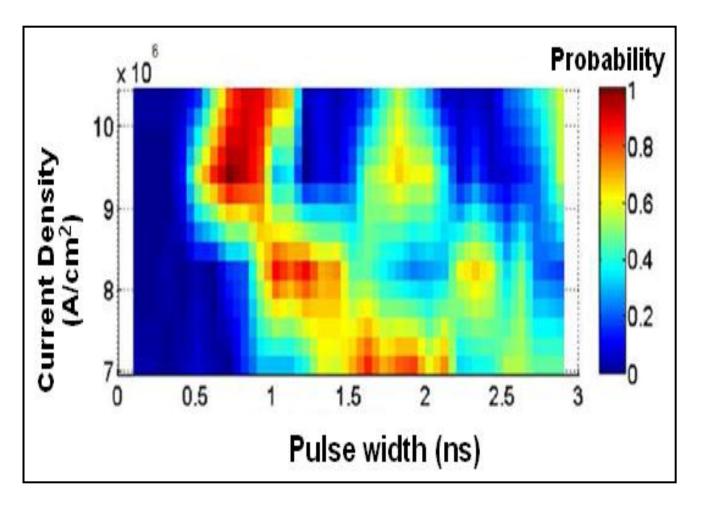




Phase Diagram

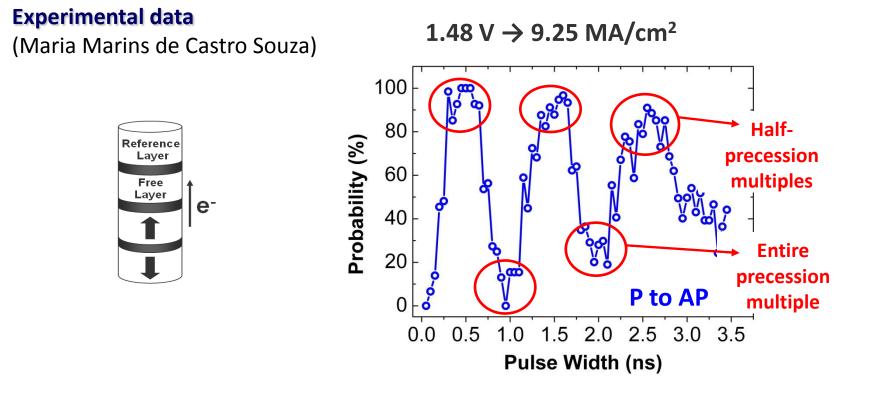
Experimental data

(Maria Marins de Castro Souza)





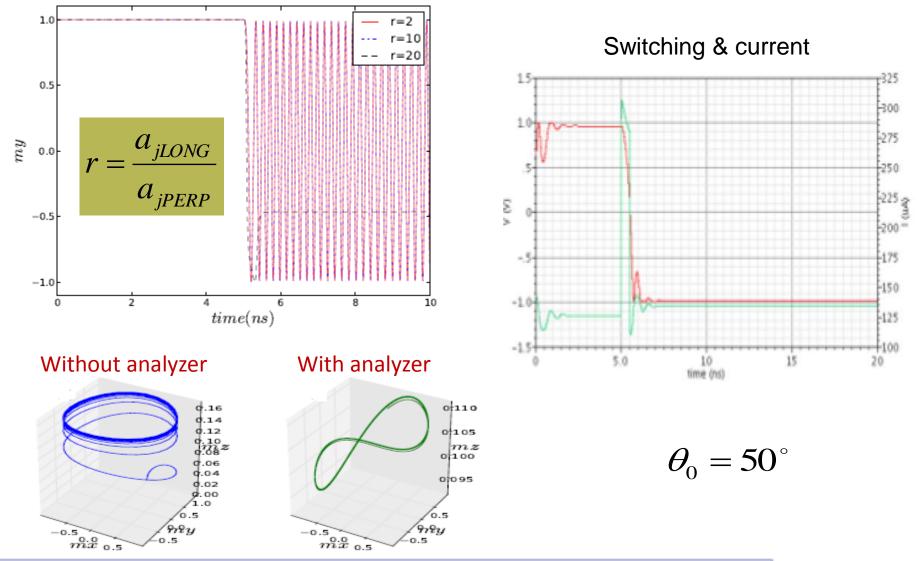
Electrical tests



- Oscillations of the switching probability due to the perpendicular polarizer
- Ultrafast switching of less than 500ps
- As the pulse current density is increased, the precessional motion shows a higher degree of coherence



Magnetization

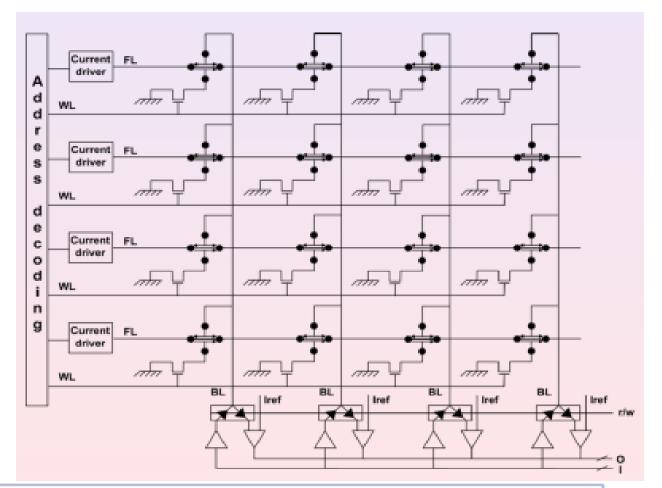




Memory array

1T-1MTJ memory array

- As dense as DRAM





- Integration of a perpendicular polarizer in a magnetic tunnel junction with good TMR signal
- Ultra fast switching of less than 500 ps
- Oscillation of the switching probability as a function of the pulse width
- Validation of precessional switching

Thank you