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# Fault-Tolerant Operation of an Open-End Winding Five-Phase PMSM Drive with Inverter Faults

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**Abstract**—Multi-phase machines are known for their fault-tolerant capability. However, star-connected machines have no fault tolerance to inverter switch short-circuit fault. This paper investigates the fault-tolerant operation of an open-end five-phase drive, i.e. a multi-phase machine fed with a dual-inverter supply. Open-circuit faults and inverter switch short-circuit faults are considered and handled with various degrees of reconfiguration. Theoretical developments and experimental results validate the proposed strategies.

**Keywords**—AC drives, Control reconfiguration, Fault tolerance, Multi-phase machines, Open-end winding, PMSM, Reliability.

## I. INTRODUCTION

Reliability of drives is a key parameter in some critical applications and its improvements can be obtained in various ways. With a conservative design, the system is operated well below its rated value, and therefore its expected lifetime increases. However, in this case, system efficiency may be reduced and the system may not be able to withstand sporadic faults. This is particularly true for power electronics components, of which failures are yet mostly unpredictable [1].

To increase the drive availability, fault tolerance has been introduced in the last decades. Fault tolerance can be applied to the inverter [2-6] and/or to the electric machine [7-10]. Specific configurations of three-phase and multi-phase drives have been investigated for their properties in healthy and faulty conditions.

Multi-phase drives have additional degrees of freedom compared with three-phase machines. These degrees of freedom can be used for different purposes, such as additional torque generation or fault tolerance when a part of the system fails. Fault detection and control reconfiguration of the drive in case of open-circuit faults have been extensively studied in the past years [11-16]. On the other hand, only a few works have addressed the problem of inverter switch short-circuit faults [6], except in topologies with additional components such as fuses and parallel thyristors which are not used in healthy operation [2-5].

In [6], the authors investigate a three-phase drive topology with an open-end winding machine and a dual-inverter supply. The authors show that fault tolerance against open-circuit fault

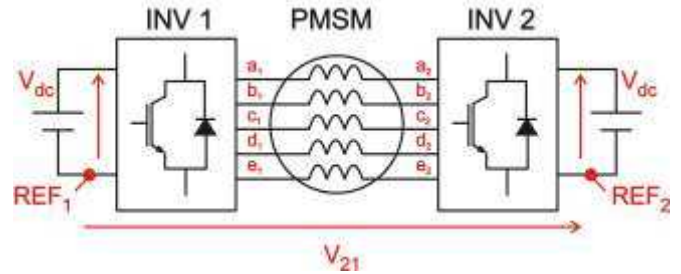


Fig. 1: Drive topology

requires a zero-sequence path for the current (connection between both inverters). On the other hand, inverter switch short-circuit faults can be handled by creating an artificial neutral point through the faulty inverter. In the latter case, this reconfiguration cannot be applied if there is a zero-sequence path. Therefore, a complete fault tolerance cannot be achieved without additional devices which connect or disconnect both inverters depending on the drive condition.

These issues can be solved with an open-end five-phase drive since the machine can be operated with open-circuit faults without zero-sequence path. This topology is illustrated in Fig. 1. This topology has already been investigated in [12] where the authors propose a space vector modulation technique.

In the present paper, the fault-tolerant capability of an open-end winding five-phase PMSM drive is studied. Various degrees of reconfiguration are investigated, starting from no reconfiguration up to a full reconfiguration (control algorithm and gate signals).

It is assumed that the inverter is equipped with smart drivers, which are able to locate a short-circuit fault almost instantaneously and to remove the gate signal of the complementary switch to avoid a short-circuit of the DC source.

In section II, the drive modeling is presented, highlighting the zero-sequence component which is paramount for control reconfiguration. In section III, a simple modulation strategy and the control algorithms are presented for the healthy and faulty conditions. Inverter switch short-circuit faults are addressed in detail, while open-circuit faults are briefly addressed as well. Finally, the test bed is presented in section IV and experimental results validate the theoretical part. Section V ends the paper by a conclusion.

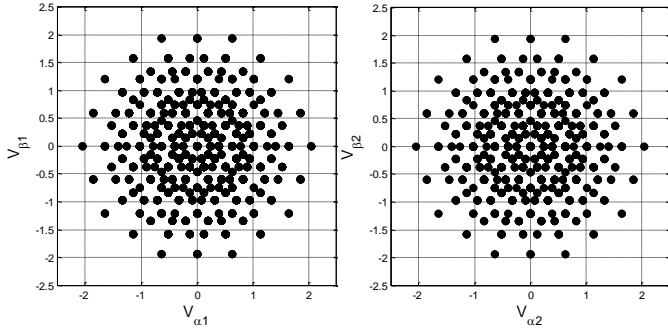


Fig. 2: Voltage vectors in the stator reference frame (normalized by  $V_{dc}$ ).  
Left: Subspace associated with the fundamental component (a). Right:  
Subspace associated with the third-harmonic component (b).

## II. DRIVE MODELING

The topology under study is Fig. 1, which consists of two isolated voltage sources, two five-leg inverters and a five-phase open-end winding PMSM.

Assuming no magnetic saturation and no saliency, the voltage equations are given by:

$$\begin{bmatrix} V_a \\ V_b \\ V_c \\ V_d \\ V_e \end{bmatrix} = R_s \begin{bmatrix} I_a \\ I_b \\ I_c \\ I_d \\ I_e \end{bmatrix} + \begin{bmatrix} L & M_1 & M_2 & M_2 & M_1 \\ M_1 & L & M_1 & M_2 & M_2 \\ M_2 & M_1 & L & M_1 & M_2 \\ M_2 & M_2 & M_1 & L & M_1 \\ M_1 & M_2 & M_2 & M_1 & L \end{bmatrix} \frac{d}{dt} \begin{bmatrix} I_a \\ I_b \\ I_c \\ I_d \\ I_e \end{bmatrix} + \begin{bmatrix} E_a \\ E_b \\ E_c \\ E_d \\ E_e \end{bmatrix} \quad (1)$$

with  $R_s$  the stator resistance,  $L$ ,  $M_1$  and  $M_2$  the stator self and mutual inductances,  $I_x$  the  $x$ -phase current,  $E_x$  the electromotive force (emf) induced by the permanent magnets in the  $x$ -phase and  $V_x$  the voltage at the  $x$ -phase terminals ( $x \in \{a,b,c,d,e\}$ ). The negative DC bus of inverter 1 is chosen as voltage reference for inverter 1, while the negative bus of inverter 2 is chosen as voltage reference for inverter 2. Hence:

$$V_x = V_{x1} - V_{x2} - V_{21} \quad (2)$$

where  $V_{21}$  is the voltage difference between the negative bus of inverter 2 and the negative bus of inverter 1.

As there is no zero-sequence path for the current, the sum of the phase currents is equal to zero:

$$I_a + I_b + I_c + I_d + I_e = 0 \quad (3)$$

Applying the Concordia and Park transformations to the equations (1), the modeling of the system expressed in the stator and rotor reference frame is obtained, with notations  $\alpha_1\beta_1\alpha_2\beta_20$  and  $d_1q_1d_2q_20$  respectively. The zero-sequence voltage component is calculated as follows:

$$V_0 = \frac{1}{\sqrt{5}}(V_a + V_b + V_c + V_d + V_e) \quad (4)$$

Substituting (1), and (3) into (4) yields:

$$V_0 = E_0 \quad (5)$$

where  $E_0$  is the zero-sequence of the back emf and can be calculated as the same way of Eq. (4).

An analytical expression of  $V_{21}$  is obtained by substituting (2) into (1) and calculating the zero-sequence component:

$$V_{21} = \frac{1}{\sqrt{5}}V_{0,eq} - \frac{1}{\sqrt{5}}E_0 \quad (6)$$

with  $V_{0,eq}$  the zero-sequence voltage of an equivalent inverter which would have a connexion between the two negative DC-bus. That is:

$$V_{0,eq} = \frac{1}{\sqrt{5}}(V_{a1} + V_{b1} + V_{c1} + V_{d1} + V_{e1} - V_{a2} - V_{b2} - V_{c2} - V_{d2} - V_{e2}) \quad (7)$$

Eq. (7) shows that  $V_{0,eq}$  depends on the instantaneous output voltage of the inverter. Practically,  $V_{0,eq}$  is thus a control variable, whereas  $V_0$  has no interest for control purpose.

It is worth noticing that (6) is similar to the expression obtained for a star connection [12]. With a star connection, the neutral-point floating voltage is affected by the zero-sequence emf and inverter output voltage, while for the topology under study it is  $V_{21}$ .

As  $V_{21}$  is associated with the zero-sequence component, it has no effect on the  $\alpha\beta$  components and the space vector theory can be applied by omitting  $V_{21}$  in the calculation of these components.

For the topology under study, the inverter has  $2^{10}$  different combinations of states. The corresponding vectors are depicted in Fig. 2 (a) and (b) for the subspaces associated with the fundamental ( $\alpha_1\beta_1$ ) and third-harmonic ( $\alpha_2\beta_2$ ) components respectively.

The electromagnetic torque of the PMSM is given by:

$$T_{em} = e_a I_a + e_b I_b + e_c I_c + e_d I_d + e_e I_e \quad (8)$$

with  $e_x = E_x/\Omega$  and  $\Omega$  the mechanical speed.

## III. CONTROL STRATEGY

This section describes the control strategy used for the experimental tests. First, the modulation strategy is briefly described. This modulation strategy is common to all control schemes (healthy and faulty). No modification of this strategy is required for the post-fault operation.

Next, the control strategy in healthy operation is briefly addressed, followed by the control strategies with an open-circuit fault and an inverter-switch short-circuit fault.

### A. Modulation strategy

A very simple modulation strategy is chosen for this application, i.e. a centered PWM strategy with the duty cycles  $\delta_{x,1}$  and  $\delta_{x,2}$  calculated as follows ( $x \in \{a,b,c,d,e\}$ ):

$$\delta_{x,1} = \frac{1}{2} \left( \frac{V_x^*}{V_{DC}} + 1 \right) \quad (9)$$

$$\delta_{x,2} = \frac{1}{2} \left( -\frac{V_x^*}{V_{DC}} + 1 \right) = 1 - \delta_{x,1} \quad (10)$$

with  $V_x^*$  the voltage reference of the  $x$ -phase ( $-V_{dc} \leq V_x^* \leq V_{dc}$ ).

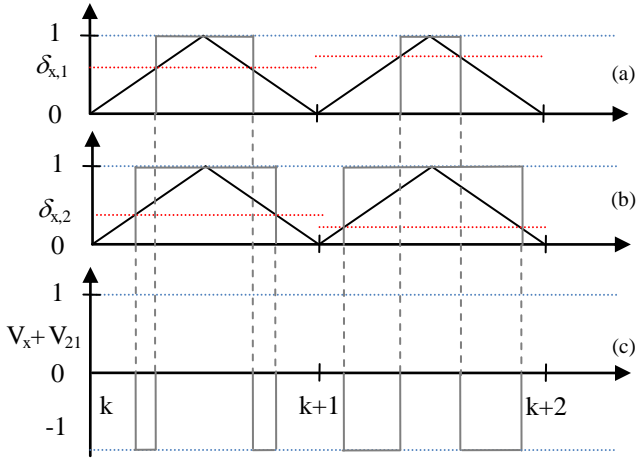


Fig. 3: Carrier-based modulation strategy. (a) Switching function of the x1-leg. (b) Switching function of the x2-leg. (c) Voltage at the x-phase terminals (normalized by  $V_{dc}$ ).

An example is given in Fig. 3, showing the modulation functions, the triangular carrier and the resulting voltage of the x-phase.

### B. Control strategy in healthy operation

Fig. 4 shows the general scheme for torque control. To keep the control scheme simple, the calculation of the current references is obtained from the torque reference and the fundamental component of the back-emf. In the Park reference frame, we have:

$$\begin{cases} I_{d1}^* = 0 \\ I_{q1}^* = \frac{T_{em}^*}{e_{q1}} \\ I_{d2}^* = 0 \\ I_{q2}^* = 0 \end{cases} \quad (11)$$

Proportional integral (PI) controllers are used for the current control, along with an emf feedforward compensation.

The zero-sequence voltage can be chosen in order to increase the DC-link voltage utilization by injecting voltage harmonic components which are multiple of five. However, the choice in this paper is to maintain the average value of  $V_{21}=0$  (healthy operation). Therefore, from (6), the zero-sequence voltage of the equivalent inverter is given by:

$$V_{0,eq,H_0}^* = E_0 \quad (12)$$

where  $H_0$  is a subscript which refers to the healthy operation.

### C. Control strategy with a short-circuit fault (inverter switch)

It is much more interesting to deal with a shorted switch fault with the topology under study. Under the fault condition, it is assumed that the complementary switch is blocked open in order to avoid a short-circuit of the associated DC source.

Under this fault condition, the number of switch combinations decreases to  $2^9=512$ . It is however difficult to develop a post-fault strategy considering a specific subset of

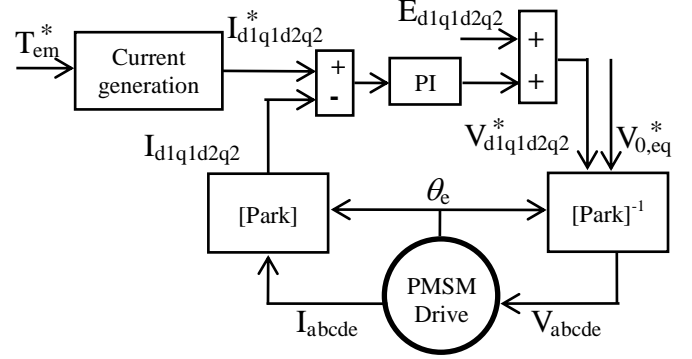


Fig. 4: Control block diagram.

vectors and a simple modulation strategy. Therefore, three reconfiguration techniques are investigated in the following sections.

1) Without reconfiguration: similarly to the open-circuit case, it might be interesting to test the fault-tolerant capability of the drive without any change of the control algorithms and modulation strategies.

The main problem in this case is that the converter output voltage is different from the controller reference voltage. As one leg is connected permanently to the positive or negative bus, a voltage DC component appears at the faulty-phase terminals. The controller will try to reject this component (which is rotating at the electrical frequency in the rotor reference frames) and therefore the response depends on the controller and machine parameters. However, injecting a voltage DC component yields a current that is only limited by the stator resistance. Hence, large currents are expected.

2) With a simple reconfiguration: in order to mitigate the fault effects, a simple reconfiguration is achieved by connecting both inverter legs to the positive or negative bus, as shown in Fig. 5. This can be implemented hardware (error signal and additional logic drive the gate signals of the other leg) or software (duty cycle equal to one or zero).

The advantage of this strategy is that the average value of  $V_x$  is equal to zero and there is thus no DC component to be rejected by the controller. On the other hand, there is still a difference between the reference voltage and the inverter output voltage. The drive response is thus again dependent on the controller and machine parameters.

3) With full reconfiguration: to mitigate the aforementioned problem, the controller has to impose a voltage reference which can be generated by the inverter. To do so, the reconfiguration of Fig. 5 applies as well. However, an additional change in the control scheme is mandatory.

Considering Fig. 5 and (2) and assuming the faulty phase is the a-phase, an important and simple result is:

$$V_a = -V_{21} \quad (13)$$

Eq. (13) shows that the faulty-phase voltage is equal to the voltage difference between the two inverter DC-links.

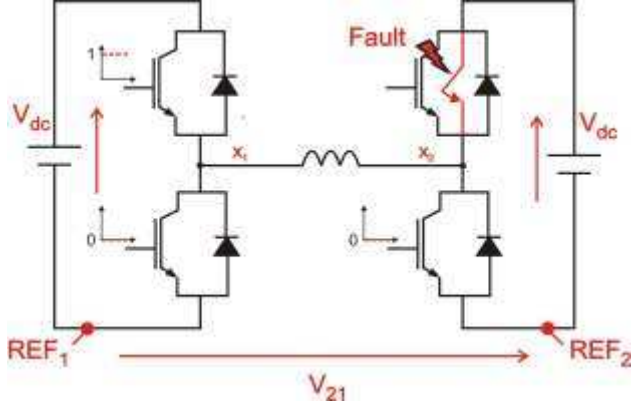


Fig. 5: Reconfiguration of the inverter legs in case of inverter short-circuit fault (top switch of the x2-leg).

From (6), this voltage can be controlled with the zero-sequence voltage of the equivalent inverter.

The Concordia transformation has the following expression:

$$\begin{bmatrix} V_a \\ V_b \\ V_c \\ V_d \\ V_e \end{bmatrix} = \sqrt{\frac{2}{5}} \begin{bmatrix} 1/\sqrt{2} & 1 & 0 & 1 & 0 \\ 1/\sqrt{2} & \cos(2\pi/5) & \sin(2\pi/5) & \cos(4\pi/5) & \sin(4\pi/5) \\ 1/\sqrt{2} & \cos(4\pi/5) & \sin(4\pi/5) & \cos(8\pi/5) & \cos(8\pi/5) \\ 1/\sqrt{2} & \cos(6\pi/5) & \sin(6\pi/5) & \cos(12\pi/5) & \cos(12\pi/5) \\ 1/\sqrt{2} & \cos(8\pi/5) & \sin(8\pi/5) & \cos(16\pi/5) & \cos(16\pi/5) \end{bmatrix} \begin{bmatrix} V_0 \\ V_{\alpha 1} \\ V_{\beta 1} \\ V_{\alpha 2} \\ V_{\beta 2} \end{bmatrix} \quad (14)$$

and substituting (13) and (6) into (14) yields:

$$V_{0,eq,SC_a} = -\sqrt{2}V_{\alpha 1} - \sqrt{2}V_{\alpha 2} \quad (15)$$

As the objective is to maintain a constant torque, i.e. no modifications of the  $d_1q_1d_2q_2$  currents (or equivalently the  $\alpha_1\beta_1\alpha_2\beta_2$  currents), the  $d_1q_1d_2q_2$  voltages (or equivalently the  $\alpha_1\beta_1\alpha_2\beta_2$  voltages) have to be maintained as well. Eq. (15) shows that a modification of the zero-sequence voltage allows this condition to be obtained.

It is worth noticing that the calculation for other phases is immediate.

#### D. Summary of the control algorithms

A summary of the reconfiguration modes for the cases under study is given in TABLE I.

TABLE I. SUMMARY OF THE CONTROL PARAMETERS FOR THE VARIOUS RECONFIGURATION MODES UNDER STUDY (A-PHASE FAULT)

Mode of operation	$V_{x,1}=V_{x,2}$	$V_{0,eq}^*$
Healthy ( $H_0$ )	no	$E_0$
Open-circuit	no	$E_0$
Short-circuit and no reconfiguration	no	$E_0$
Short-circuit and simple reconfiguration	yes	$E_0$

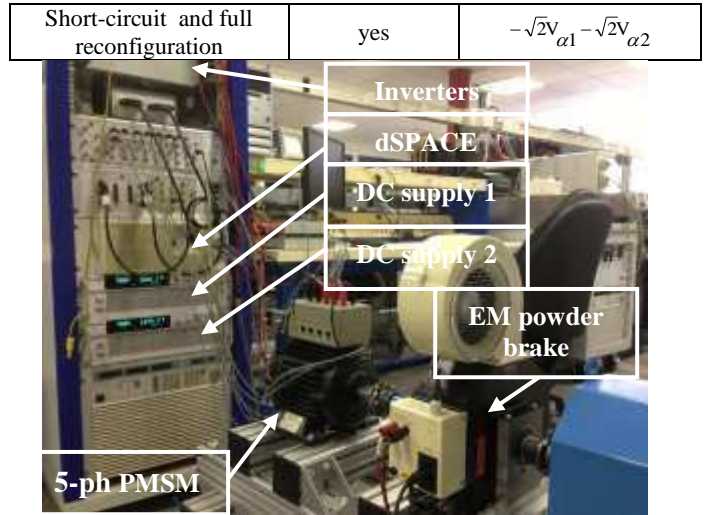


Fig. 6: Experimental platform.

TABLE I. MACHINE PARAMETERS

Parameters	Values
$R_s$	2.24 $\Omega$
$L_{d1} = L_{q1}$	3.2 mH
$L_{d3} = L_{q3}$	0.9 mH
$e_{q1}$	0.51 Vs $\text{rad}^{-1}$
Pole pairs number ( $N_{pp}$ )	2
Nominal speed	1500 rpm
Nominal current	15 A
Maximum torque	20 Nm
Nominal power	3.1 KW

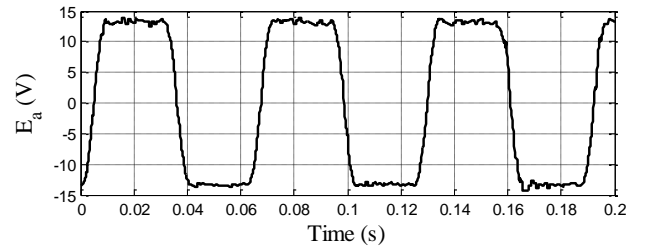


Fig. 7: EMF of the five-phase machine at  $\Omega = 50 \text{ rad s}^{-1}$ .

## IV. EXPERIMENTAL RESULTS

### A. Test-bed description

A picture of the test bed is shown in Fig. 6. The system consists of a five-phase double-ended PMSM, two five-leg inverters and two isolated DC-sources. A dSPACE1005 board is used for control prototyping. The load is an electromagnetic powder brake.

The open-circuit fault is emulated by disconnecting a phase terminal, while a switch short-circuit fault is emulated by setting the corresponding duty cycle to one or zero.



The machine parameters are given in TABLE I. The emf waveform is almost trapezoidal, as shown in Fig. 7.

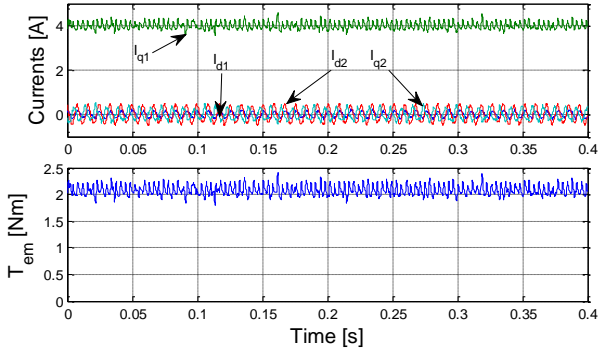


Fig. 8: Healthy operation (experimental results). Top: current expressed in the rotor reference frame. Bottom: Electromagnetic torque (calculated from the model).

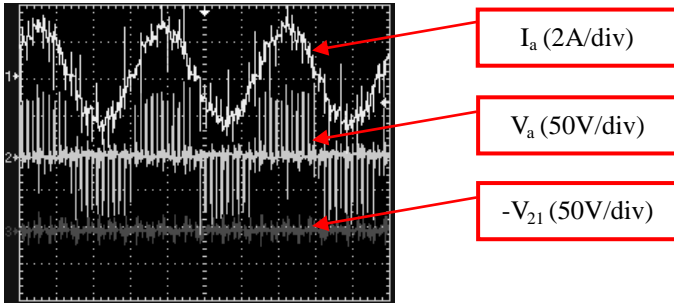


Fig. 9: Healthy operation. Scope results showing the a-phase current  $I_a$ , the a-phase voltage  $V_a$  and the voltage between the two inverter DC-bus  $V_{21}$ .

### B. Control in healthy operation

Fig. 8 and Fig. 9 show experimental results of the control in healthy operation ( $\Omega=38\text{rad/s}$ ). Fig. 8 shows the  $d_1q_1d_2q_2$  currents and the resulting electromagnetic torque (estimated with the model). Fig. 9 shows scope results for the same test presented in Fig. 8. The phase current  $I_a$  is sinusoidal, lagging the phase voltage  $V_a$ . The voltage difference between the two inverter DC-links  $V_{21}$  is also measured. It can be seen that  $V_{21}$  is close to zero (average and instantaneous values), as expected from the control algorithm in healthy operation.

### C. Control with an inverter short-circuit fault

1) No reconfiguration: Fig. 10 and Fig. 11 show experimental results of the control with an inverter switch short-circuit fault and no reconfiguration ( $\Omega=42\text{rad/s}$ ). The fault occurs at time  $t=0.2\text{s}$ .

Fig. 10 shows that the current magnitudes in the  $d_2q_2$  subspace increase a lot upon the fault occurrence. Torque ripple of about 165% appears due to the fault. Fig. 11 shows scope results for the same test. The faulty-phase current  $I_a$  has a positive DC component, while  $V_a$  and  $V_{21}$  are seriously affected by the fault.

2) Simple reconfiguration: Fig. 12 and Fig. 13 show experimental results of the control with an inverter switch short-circuit fault and simple reconfiguration ( $\Omega=40\text{rad/s}$ ). The fault occurs at time  $t=0.2\text{s}$ . Torque ripple of about 75% appears due to the fault. The scope results of Fig. 13 shows

that the faulty-phase current is quite distorted, but its magnitude is rather low. It can be noticed that this fault induces the supply ( $I_{d2}$  and  $I_{q2}$  are different to zero) of the

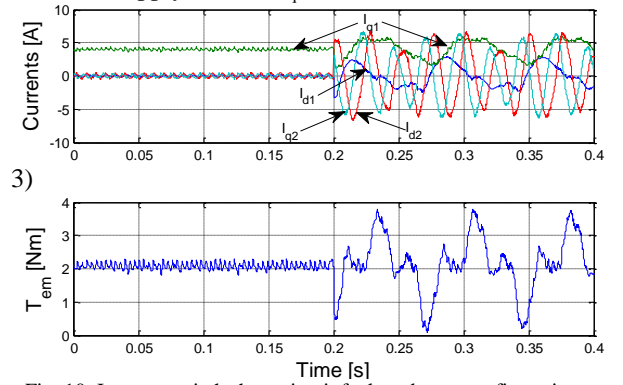


Fig. 10: Inverter switch short-circuit fault and no reconfiguration (experimental results). Top: current expressed in the rotor reference frame. Bottom: Electromagnetic torque (calculated from the model).

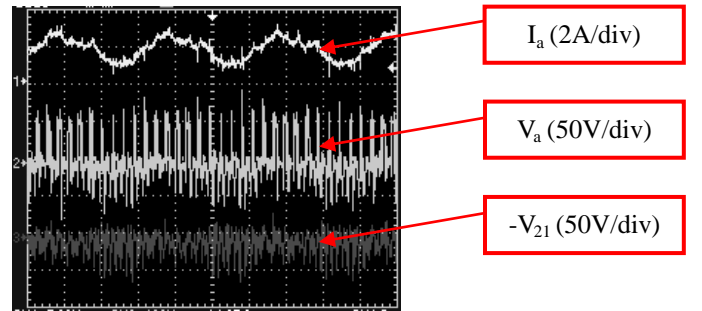


Fig. 11: Inverter switch short-circuit fault and no reconfiguration. Scope results showing the faulty phase current  $I_a$ , the a-phase voltage  $V_a$  and the voltage between the two inverter DC-bus  $V_{21}$ .

secondary machine associated with the subspace  $\alpha_2\beta_2$ . So in order to obtain a constant torque, we have to use a new reference of currents (voltages) for the two fictitious machines in Concordia reference frame. We can see that when a fault occurs in the inverter, a simple reconfiguration of switches is not enough to obtain a good result in torque control. It can be noticed that the torque ripple is reduced by comparing to Fig. 10 but this value of torque pulsation remains important.

3) Full reconfiguration: Fig. 14 and Fig. 15 show experimental results of the control with an inverter switch short-circuit fault and full reconfiguration ( $\Omega=42\text{rad/s}$ ). The fault occurs at time  $t=0.2\text{s}$ . In this case, torque ripple is of the same quantity as in healthy operation. The faulty-phase current is not distorted (see Fig. 15) and Eq. (13) is verified as  $V_a=-V_{21}$ . By imposing this constraint, the new reference of voltages (for 4 healthy phases) are redetermined. Indeed, in this case, the voltage of phase b is a line-line voltage  $V_b-V_a=V_{ba}$ . In the same way, the voltages of three others phases (c, d e) are:  $V_{ca}$ ,  $V_{da}$ ,  $V_{ea}$ . It means that after fault occurrence, the inverter is controlled to obtain the line voltages for compensating the phase a is faulty. Finally, the phase currents of machine are not distorted and maintained as before the fault.

## V. CONCLUSION

The paper has analyzed the behavior of an open-end five-phase PMSM drive under inverter fault conditions. With a dual-inverter supply, the equivalent five-phase topology offers

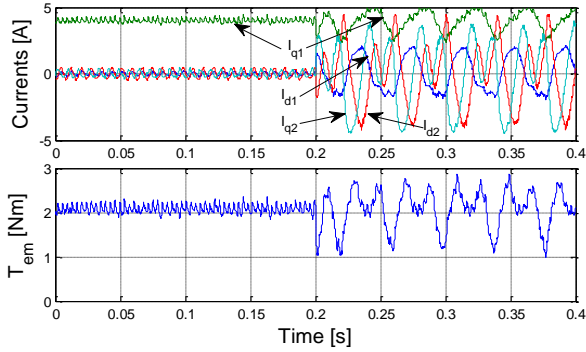


Fig. 12: Inverter switch short-circuit fault and simple reconfiguration (experimental results). Top: current expressed in the rotor reference frame. Bottom: Electromagnetic torque (calculated from the model).

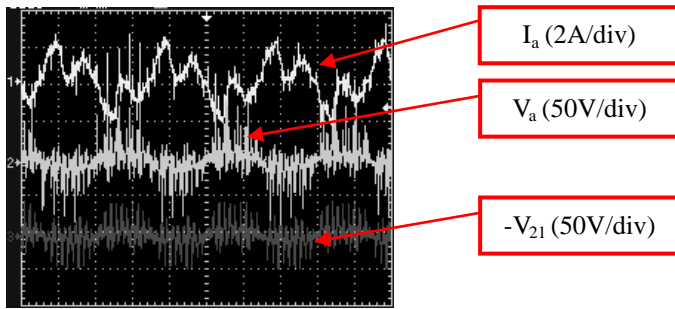


Fig. 13: Inverter switch short-circuit fault and simple reconfiguration. Scope results showing the faulty phase current  $I_a$ , the a-phase voltage  $V_a$  and the voltage between the two inverter DC-bus  $V_{21}$ .

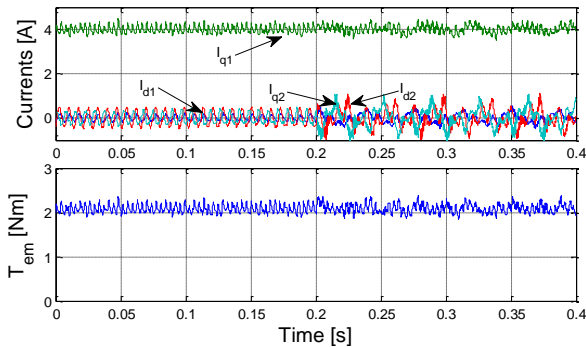


Fig. 14: Inverter switch short-circuit fault and full reconfiguration (experimental results). Top: current expressed in the rotor reference frame. Bottom: Electromagnetic torque (calculated from the model).

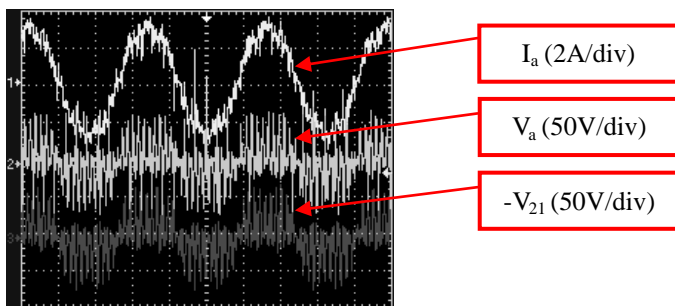


Fig. 15: Inverter switch short-circuit fault and full reconfiguration. Scope results showing the faulty phase current  $I_a$ , the a-phase voltage  $V_a$  and the voltage between the two inverter DC-bus  $V_{21}$ .

the possibility to operate the machine under open-circuit and inverter switch short-circuit faults (without additional component). An inherent fault tolerance has been shown in the study. It has also been shown that a simple reconfiguration allows the drive to be operated with the same torque performance as in healthy operation.

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## REFERENCES

- [1] Shaoyong Yang; Dawei Xiang; A. Bryant, P. Mawby, L. Ran, and P. Tavner, "Condition Monitoring for Device Reliability in Power Electronic Converters: A Review," *IEEE Trans. on Power Electronics*, vol.25, no.11, pp.2734-2752, Nov. 2010.
- [2] B.A. Welchko, T.A. Lipo, T.M. Jahns, and S.E. Schulz, "Fault tolerant three-phase ac motor drive topologies; a comparison of features, cost, and limitations," *IEEE Trans. on Power Electronics*, vol. 19, no. 4, pp. 1108-1116, 2004.
- [3] R.R. Errabelli and P. Mutschler, "Fault-Tolerant Voltage Source Inverter for Permanent Magnet Drives," *IEEE Trans. on Power Electronics*, vol.27, no.2, pp.500-508, Feb. 2012.
- [4] M. Naidu, S. Gopalakrishnan, T.W. Nehl, "Fault-Tolerant Permanent Magnet Motor Drive Topologies for Automotive X-By-Wire Systems," *IEEE Trans. on Industry Applications*, vol.46, no.2, pp.841-848, 2010.
- [5] F. Richardeau, Zhifeng Dou; J.-M. Blaquiere, E. Sarraute, D. Flumian, and F. Mosser, "Complete short-circuit failure mode properties and comparison based on IGBT standard packaging. Application to new fault-tolerant inverter and interleaved chopper with reduced parts count," 14th European Conference on Power Electronics and Applications (EPE 2011), pp.1-9, 2011.
- [6] Y. Wang, T.A. Lipo, and D. Pan, "Robust operation of double-output AC machine drive," *IEEE 8th International Conference on Power Electronics and ECCE Asia (ICPE & ECCE)*, pp.140-144, 2011.
- [7] B.C. Mecrow, A.G. Jack, J.A. Haylock, and J. Coles, "Fault-tolerant permanent magnet machine drives," in *Electric Power Applications*, IEE Proceedings-, 1996, vol. 143, no. 6, pp. 437-442.
- [8] A.M. El-Refaie, "Fault-tolerant permanent magnet machines: a review," *Electric Power Applications*, IET, vol.5, no.1, pp.59-74, January 2011
- [9] Jingwei Zhu; Xiaobing Niu, "Investigation of short-circuit fault in a fault-tolerant brushless permanent magnet AC motor drive with redundancy," *IEEE Conference on Industrial Electronics and Applications (ICIEA)*, pp.1238-1242, 15-17 June 2010.
- [10] L. Parsa, "On advantages of multi-phase machines," in *31st Annual Conference of IEEE Industrial Electronics Society (IECON 2005)*, 2005.
- [11] F. Meinguet, P. Sandulescu; X. Kestelyn and E. Semail, "A Method for Fault Detection and Isolation Based on the Processing of Multiple Diagnostic Indices: Application to Inverter Faults in AC Drives," *IEEE Trans. on Vehicular Technology*, vol.62, no.3, pp.995-1009, 2013.
- [12] F. Meinguet, E. Semail, and J. Gyselinck, "An on-line method for stator fault detection in multi-phase PMSM drives," in *Vehicle Power and Propulsion Conference (VPPC)*, 2010 IEEE, pp. 1-6.
- [13] X. Kestelyn, and E. Semail, "A Vectorial Approach for Generation of Optimal Current References for Multiphase Permanent-Magnet Synchronous Machines in Real Time," *IEEE Trans. on Industrial Electronics*, vol.58, no.11, pp.5057-5065, Nov. 2011.
- [14] N. Bianchi, S. Bolognani; and M.D. Pre, "Design and Tests of a Fault-Tolerant Five-phase Permanent Magnet Motor," in *37th IEEE Power Electronics Specialists Conference (PESC '06)*, pp.1-8, June 2006.
- [15] D. Flieller, N.K. Nguyen, P. Wira, G. Sturtzer, D. Abdeslam, and J. Merckle, "A Self-Learning Solution for Torque Ripple Reduction for Non-Sinusoidal Permanent Magnet Motor Drives Based on Artificial Neural Networks," *IEEE Trans. on Industrial Electronics*, Avril 2013.
- [16] A. Tani, M. Mengoni, M.; L. Zarri, G. Serra and D. Casadei, "Control of Multiphase Induction Motors With an Odd Number of Phases Under Open-Circuit Phase Faults," *IEEE Trans. on Power Electronics*, vol.27, no.2, pp.565-577, Feb. 2012.



- [17] E. Levi, I.N.W. Satiawan; N. Bodo and M. Jones, "A Space-Vector Modulation Scheme for Multilevel Open-End Winding Five-Phase Drives," IEEE Trans. on Energy Conversion, vol.27, no.1, pp.1-10, 2012.