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A 900MHz RF Energy Harvesting Module

TARIS Thierry, VIGNERAS Valérie
IMS lab, University of Bordeaux
33405 Talence Cedex, France

FADEL Ludivine
LAAS, University of Toulouse
31000 Toulouse, France

Abstract — This paper presents a guideline to design and optimize a RF energy harvester operating in ISM Band at 902 MHz. The circuit is implemented on a standard FR4 board with commercially available off-the-shelf devices. The topology of the impedance transformation block is selected to reduce the losses which improves the overall performances of the system. The characterization of the harvesting module shows sensitivity of -22.5 dBm for a dc output voltage of 200 mV up to -11 dBm for 1.08 V. A wireless power transmission in an indoor environment is measured with a radiated source power of 16.8 dBm. The harvester exhibits a DC rectified voltage of 1.25 V at 0.5 meter and still 500 mV at 1.5 meter.

I. INTRODUCTION

The finite life of electrical battery is encouraging the companies and researchers to come up with new ideas and technologies to extend the lifetime of mobile devices. For wireless miniature electronic the answer is available in capturing and storing the energy from external ambient sources, a technology known as energy harvesting. Different types of source are considered among them are: wind, solar, vibration, temperature gradient, thermoelectric, Radio-Frequency (RF) and acoustic [1], [2], [3]. Among the top most power constrained applications are the Wireless Sensor Networks (WSN). Their developments, hardly driven by low cost, low power and compact implementations extensively investigate the energy scavenging. The huge deployment of RF communications over the three last decades makes RF energy available at “anytime and everywhere”. It is a decisive asset to address power saving and energy management challenges in WSN. Furthermore the requirement for low cost solutions increases the interest for RF harvesting since it is supported by microelectronic market. Hence an important issue today is the development of inexpensive, compact and efficient RF energy scavengers for WSN applications.

This paper focuses on the design of a RF harvesting module operating in the 902-928 MHz ISM Band. The demonstrator is exclusively developed with Commercially available Off-The-Shelf (COTS) devices to estimate the performances of a low cost solution. The design procedure of each featuring block is first detailed. The characterization of the module is then reported and compared to simulation results. The measurement of an indoor wireless power transmission (WPT) is presented and discussed. A figure of merit is introduced to compare the performances of the RF harvester with the state of art.

II. BUILDING BLOCK DESIGN AND CHARACTERISTICS

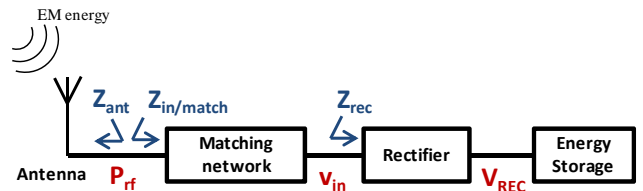


Figure 1. Building blocks of the RF harvesting module

The basic architecture of an RF energy harvester is presented in Fig. 1. The antenna performs the transduction of an electromagnetic energy into an electrical signal. To transfer a maximum of the signal to the rectifier a matching network is introduced. The rectifier converts the AC signal into a DC voltage charging a storage capacitor.

A. Antenna

An antenna is specified by its directivity, G_{ant} , and its impedance, Z_{ant} . The selected element is here a quarter wave length dipole (Pulse Electronics) providing a 3 dBi maximum gain in vertical position in a 60° to 120° aperture.

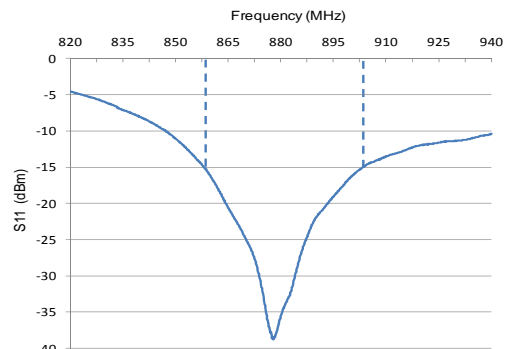


Figure 2. Return loss, S_{11} , of the dipole antenna

The antenna impedance is characterized by its return loss S_{11} , Fig. 2, measured with a HP 8720 network analyzer. Assuming a proper 50Ω matching for S_{11} below -15 dB, the antenna covers a 856 to 903 MHz band, with a best matching at 878 MHz.

B. Rectifier

Since the level of collected power, P_{rf} , is low, the AC to DC converter is based on a voltage multiplier to deliver an acceptable output DC voltage. A Cockcroft-Walton Voltage Doubler [4], Fig. 3(a), composes the elementary stage of the rectifier where C_1 (100 pF) and D_1 form the negative clamp, C_2 (470 μ F) and D_2 achieve peak rectification. C_2 , smoothing the variations of V_{REC} , also acts as storage capacitor.

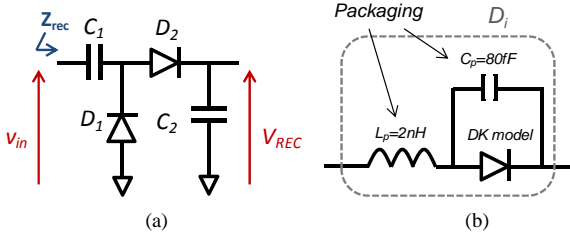


Figure 3. Schematic of a Greinacher Voltage Doubler (a) small signal model of a HSMS2852 zero bias diodes in a SOT323 packaging (b)

The diode skills are critical for the overall performances of the module. Schottky diode, offering low forward voltage, V_F , and high switching speed, C_T , are well suited for RF energy harvesting. The Avago HSMS-2852 Series featuring a V_F of 150 mV, and a C_T of 300 fF, has been selected. To improve the simulation accuracy, the diode model provided by the Design Kit (DK model) has been modified to take into account the packaging parasitic. It includes L_p , 2nH, and C_p , 80 fF as reported in Fig.3 (b). The circuit of Fig.3 (a) is simulated in Advanced Design System (ADS) with Large Signal S-Parameter method. Device accesses and interconnections are modeled by tapers and microstrip lines, respectively. The real and imaginary parts of the input rectifier impedance, Z_{rec} , are depicted in Fig. 4 for an input power of -20 dBm.

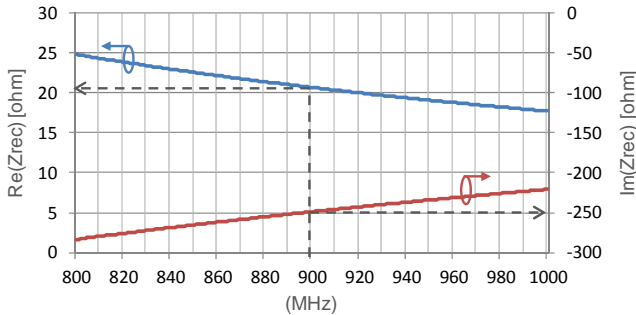


Figure 4. Simulated input impedance, Z_{rec} , of the single stage voltage doubler.

The quality factor, Q_{rf} , exploited by the matching network to boost the voltage at the input of the rectifier, is defined in (1). At 900 MHz, it varies from 7 to 11 with the input power.

$$Q_{rf} = \frac{|\text{Im}(Z_{rec})|}{|\text{Re}(Z_{rec})|} \quad (1)$$

C. Matching Network

A maximum of the available power, P_{rf} , is transferred from the antenna to the rectifier when an impedance equals to the conjugate of Z_{ant} , noted Z_{ant}^* , is presented at the operating frequency. This principle is known as power matching.

Since Z_{ant}^* and Z_{rec} , reported in the smith chart of Fig. 5, do not complete these conditions, a matching network is inserted to transform the rectifier impedance to Z_{ant}^* . To lower the losses and so improve voltage boosting, a solution embedding a reduced number of components is suited. The step by step design of the matching network is illustrated in Fig. 5. It combines a stub, a capacitor and a transmission line (microstrip type).

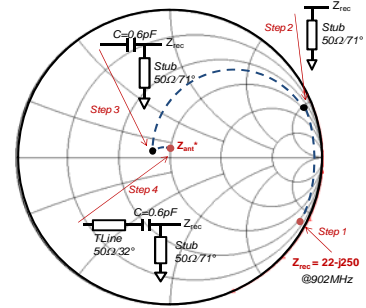


Figure 5. Constructive diagram of the matching network

III. CARACTERISATION OF THE RF ENERGY HARVESTER

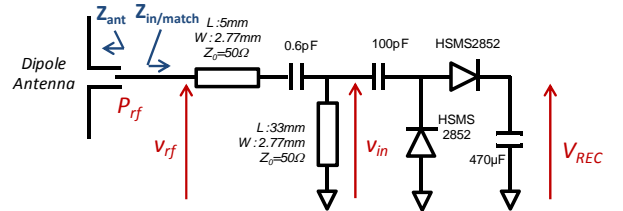


Figure 6. Schematic of the RF Energy harvesting module

The complete schematic of the harvester is presented in Fig. 6. The return loss S_{11} at $Z_{in/match}$, input of the matching network/rectifier combination, is reported in Fig. 7 for an input power, P_{rf} , of -20dBm. In good agreement with simulation results, the minimum of measured S_{11} , -31 dB, yields at 902MHz. This frequency, covered by the antenna bandwidth depicted in Fig. 2(b), will be further considered as the operating frequency of the system.

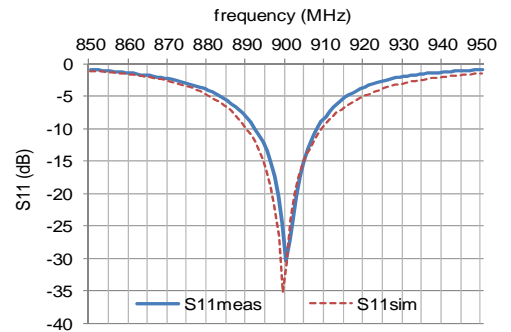


Figure 7. The return loss S_{11} at $Z_{in/match}$ with $P_{rf} = -20$ dBm

The return loss is also measured for various input power P_{rf} , from -25 to -10 dBm, Fig. 8. The minimum of S_{11} shifts from 905 to 898 MHz over the power range because of the non linear behavior of the diodes. Covered by the antenna bandwidth these variations do not alter the system characterization

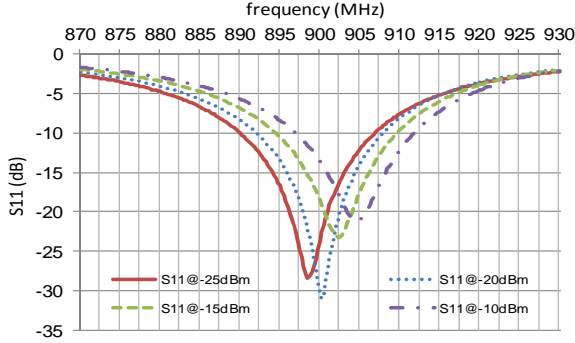


Figure 8. Measured S_{11} at $Z_{in/match}$ for various P_{rf}

The rectified output voltage, V_{REC} , is derived in expression (2) based on [6] :

$$V_{RECestim} = 2\sqrt{2 \cdot \text{Re}[Z_{rec}] \cdot P_{rf} \cdot (1 + Q_{rf}^2) - V_F} \quad (2)$$

Q_{rf} is the quality factor of the input network and V_F is the diode threshold voltage defined in section II. To estimate $V_{RECestim}$, $\text{Re}[Z_{rec}]$ and Q_{rf} are extracted from Fig. 4. It is compared with the ADS simulations and measurement results in Fig. 9. $V_{RECestim}$ exhibits +/- 20% error with measurement results from -21 to -13.5 dB with a best fit occurring at $P_{rf} = -18$ dBm. We can notice the close form of the simulated ($V_{RECsimsim}$) and measured ($V_{RECmeas}$) results over the entire power range.

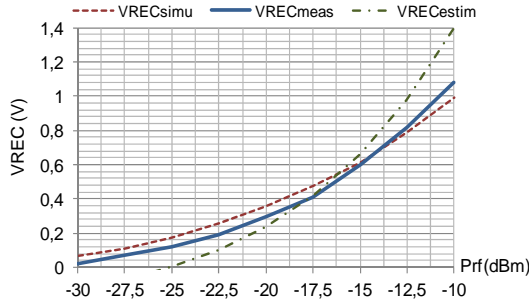


Figure 9. Rectified output voltage V_{REC} versus input power P_{rf} at 902 MHz

IV. WIRELESS POWER TRANSMISSION MEASUREMENTS

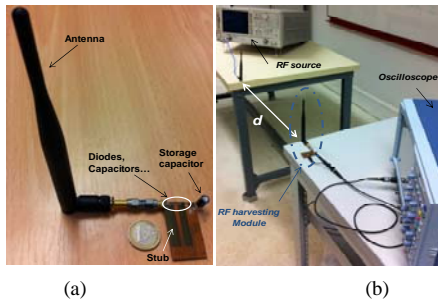


Figure 10. Single stage RF harvesting module (a) indoor WPT scene (b)

A picture of the RF energy harvester is depicted in Fig. 10 (a). The Printed Circuit Board (PCB) integrates the matching network, the rectifier and the storage capacitor on a standard 1.6 mm FR4 support. It is two time larger than a 1 euro coin. The antenna, in vertical position, is 16.5 cm. The indoor measurements of wireless power transmission have been performed in a testing room of the lab, Fig. 10 (b).

A schematic of the WPT scene of Fig. 10(b) is proposed in Fig. 11. The RF source is a HP 8720 network analyzer connected to a dipole antenna. The RF harvester is the one presented in Fig. 10 (a). The voltage delivered by the harvester is measured with a Lecroy Wave pro 960 oscilloscope.

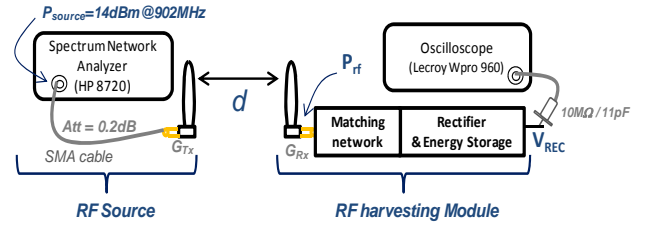


Figure 11. Model of the WPT scene

The relation between the power delivered by the source, P_{source} , and the available power, P_{rf} , at the RF harvester antenna is:

$$P_{rf} = P_{source} \cdot G_{ant/Tx} \cdot A(d) \cdot G_{ant/Rx} \quad (3)$$

With $A(d)$ the attenuation induced by the propagation of the signal in the air. We can find a lot of radio propagation model in the literature. Most of them are based on the free space path loss described by Friis [5] and reported in expression (4).

$$A(d) = \left(\frac{\lambda}{4\pi d}\right)^2 \quad (4)$$

With λ the signal wave length and d the distance between the source and the RF harvester in Fig.10 (b) &11. Combining the expression (2), (3) and (4) we can estimate the rectified voltage $V_{RECcalc}$ at a distance d according (5):

$$V_{RECcalc} = 2\left[G_{ant} \frac{\lambda}{4\pi d} \sqrt{2 \cdot \text{Re}[Z_{rec}] \cdot P_{rf} \cdot (1 + Q_{rf}^2) - V_F}\right] \quad (5)$$

The network analyzer is in Continuous Wave (CW) mode, fixed at 902 MHz. The maximum power, P_{source} , is 14 dBm. The overall radiated power is estimated to 16.8 dBm including the cable loss, 0.2 dB, and the antenna gain, 3 dBi. Fig.12 represents the rectified voltage, V_{REC} , with the distance. $V_{RECcalc}$ is calculated with (5). Likewise $V_{RECestim}$ in Fig. 9, the calculated $V_{RECcalc}$, proposed in (5), overestimates the rectified voltage for a couple of ten centimeters, which corresponds to a large harvested power, and underestimates it above 2 meters, for very low harvested power. The ADS simulations, performed with Ptolemy environment, and measurement results are very close with less than 15% error above 0.5 meter in Fig. 12. The harvester provides more than 500 mV up to 1.5 meter.

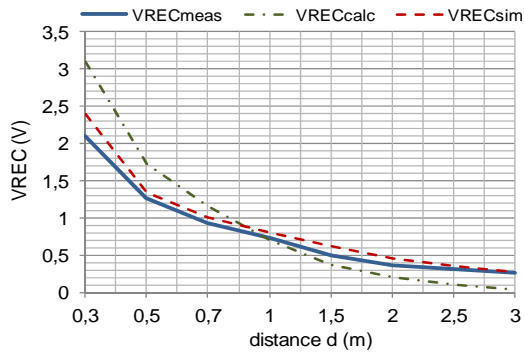


Figure 12; V_{REC} versus distance d in WPT scene at 902 MHz

The regulation authorizes a maximum of 4W/36 dBm EIRP in the 902-928 MHz ISM Band. The 19 dB margin kept from this maximum is here imposed by the equipment - i.e. the network analyzer -. Some ADS simulations figure out the range of 500 mV rectified voltage is extended to 15 meter under EIRP_{max} conditions.

Multiple values of capacitor, type radial, were tested for the energy storage: from 10 μ F to 470 μ F. No variation has been observed for the rectified voltage except the rising time needed to charge the capacitor at a fixed distance. Table I presents the measured rising time for 3.3 μ F, 10 μ F, 220 μ F and 470 μ F. Assuming a typical RC charge, the output resistance of the rectifier, $R_{out/rec}$, is estimated based on expression (6):

$$R_{out/rec} = \frac{0.67 \cdot t_{rise}}{C} \quad (6)$$

TABLE I. Rising time and output resistance of the rectifier for various storage capacitors

Capacitor (μ F)	10	100	220	470
t_{rise} (s)	0.73 \pm 0.03	7.77 \pm 0.4	16.7 \pm 1.5	36 \pm 1.5
$R_{out/rec}$ (k Ω)	50.2	52.1	50.8	51.3

Some realizations of the state of art are reported in Table II. To discuss the performances of these RF energy harvesters we propose a figure of merit, expression (7), rating the voltage efficiency per stage.

$$FOM = \frac{V_{REC}[V] \cdot frequency[GHz]}{P_{rf}[mW] \cdot N[number\ of\ stage]} \quad (7)$$

Passive devices, and so input matching networks, exhibit a low quality factor in CMOS technologies because of the low resistivity of the substrate. In the meantime the threshold voltage of MOS based diodes, ranging from 250 to 350 mV in sub 100nm processes, is high compared to the forward voltage of COTS schottky devices, typical 150 mV for a HSMS2852. As matter of consequences COTS harvesters exhibit a better FOM [this work], [9] than their IC counterparts [6], [7], [8].

This analysis figures out System On a Chip (SOC) implementations pushed by the development of CMOS technologies limit the performances in the context of RF

energy harvesting. Focusing on efficiency and cost issues COTS solutions are better suited.

TABLE II. Comparison with the state of art

Reference	This work	[6]	[7]	[8]	[9]
Freq (GHz)	0.9	0.92	0.9	0.91	2.4
P_{rf} (dBm)	-10	-10	-22	-15	-10
V_{rec} (V)	1.08	3.6	1	2.8	0.6
Number of stage	1	4	17	16	1
Technology	HSMS2852	CMOS 0.18 μ m	CMOS 90nm	CMOS 250nm	HSMS2850
FOM	9.7	8.3	8.8	5.3	14.4

V. CONCLUSION

A low cost RF energy harvesting module has been designed and implemented on a FR4 board with COTS devices. The design procedure including parasitic elements is detailed. It is validated by the close form between the simulated and measured data of the RF harvester. A figure of merit rating the voltage efficiency per stage is proposed. The circuit developed in this work exhibits a larger FOM than CMOS counterparts which confirms the interest in developing RF energy harvester with COTS. Illuminated by a RF source radiating a power of 16.8 dBm in an indoor environment, the harvesting module provides a rectified DC voltage of 1.25 V at 0.5 meter and 500 mV at 1.5 meter over a 470 μ F storage capacitor.

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