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Piezoelectric Response of PZT Nanostructures Obtained by Focused Ion Beam

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ABSTRACT

 $Pb(Zr_x,Ti_{1-x})O_3$ (PZT) microscale island ($1\mu m\sim 100$ nm) was fabricated by Focused Ion Beam (FIB) before and after its crystallization. In the first case the FIB etching is realized on amorphous films and a post annealing treatment, at the crystallization temperature of the PZT films, is necessary to cristallize the film in the perovskite phase. In the second case the etching are made on crystallized films. Local electrical properties were evaluated by piezoresponse force microscopy (PFM) technique and the degradations induce in the films are studies by Raman spectroscopy. Compared to the PZT island fabricated after crystallization, the result shows that there is noticeable enhancement in nanoscale electrical properties of PZT island fabricated before crystallization, especially when the island size decreases.

Keywords: amorphous and crystallized PZT films, PZT island, sputtering, FIB Ga⁺, piezoresponse force microscopy (PFM)

1. INTRODUCTION

Few years ago, ferroelectric films have received a worldwide technological interest for a large number of applications, in particular for Micro (or Nano)-Electro-Mechanical Systems (MEMS-NEMS) [1–3]. Among the various ferroelectric films, PZT (or doped PZT) film is one of the most used materials

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because of its large piezoelectric coefficient. In view of the practical applications point, one of the main objective in the integration of PZT into silicon MEMS/NEMS is developing the ability to develop process compatibility (in terms of thermal budget, chemical products (nature of gaz) for etching these ferroelectric films and associated electrode materials. To this fact, several techniques have been developed [4–8]. Despite the large amount of effort in this field, there are still some problems that need to be overcome. One of the main challenges is to develop the etching process with no degradation as well on the silicon, bottom electrode and PZT material in particular in the electrical properties of the PZT film. Many papers show that the electrical performances of the films decrease after etching process. These degradations are attributed to the physical defects induced by the ion bombardment, the chemical contamination, . . . The etching damage is more severe when the pattern size of PZT film is decreased to nanometer dimensions because of the 'side wall effect'. Recently, much attention has been paid to minimize the etching damage [9–12].

In this study, the submicrometer PZT island is first patterned on amorphous film by FIB method, and then make annealing process at its crystallization temperature. With these etching and annealing conditions, there is great enhancement in nanoscale electrical properties of PZT island. The results are compared with the "classical" etching process: FIB etching on perovskite PZT films.

2. EXPERIMENTAL

We use LaNiO₃ (LNO) as a bottom electode in this work; it is well known that LNO is a favorable bottom electrode for ferroelectric film capacitor applications (in particular for fatigue). The metallic oxide LNO is a pseudo cubic perovskite with a lattice parameter of $a_{\rm LNO}=3.84~{\rm \AA}$; This oxide electrode can also act as a sink for oxygen vacancies and prevent their accumulation at the film/electrode interface. So, a LaNiO₃ film with thickness of 250 nm was firstly deposited by rf magnetron sputtering on SiO₂/Si substrates; its deposition was made by r.f magnetron sputtering (see Table 1). The reaction gas was argon (80%) and oxygen (20%). The films are produced by depositing hot i.e. the substrate is heated during the deposition, and in this case the substrates were heated to a temperature of 450°C. The films grown have a thickness of 200 nm. The LNO was annealed at 700°C in order to improve the conductivity [13, 14].

 $PbZr_{0.53}Ti_{0.47}$ film with thickness of 300 nm was after deposited on the $LaNiO_3$ also by rf magnetron sputtering without substrate heating and no post annealing was made. PZT films sputering conditions are given Table 2.

Then this PZT film was amorphous and it was patterned by FIB method to fabricate the PZT microscale island. FIB patterning here was performed in FEI Dual Beam STRATA 235, with a Ga^{+3} ion-beam, an acceleration voltage of 30 kV, a beam current of 300 pA (corresponding to 25 nm spot size) and normal incident angle. Before etching, a thin $100\sim120$ nm Pt top electrode with the

Table 1 LNO sputtering conditions with stoichiometric target

76.2 mm (3")–stoichiometric target
60 mm
Argon + Oxygen (20%)
10^{-2} mbar
2,36 W/cm ²
Si/SiO ₂
450°C
\sim 30 Å/min average
1 h at 700°C in air

size of $1.7 \times 10^{-2} \text{mm}^2$ was deposited on the PZT film surface by rf sputtering to protect the top PZT island from etching damage. Then this amorphous PZT microscale island was annealed at the crystallization temperature of 625°C for 30 min. For simplicity, this etching process is called amorphous etching; for comparison, microscale PZT islands with same thickness and size were also fabricated by the conventional FIB etching process, which means making the etching on the annealed crystallized PZT samples and in both cases their local electrical properties were examined. Figure 1 (a) and (b) is the flow chart of the two different etching processes and (c) is the schematic view of the FIB fabrication PZT island structure.

For the electrical properties investigation of PZT island, local piezoelectric hysteresis loops produced by a commercial Atomic Force Microscope (AFM) (Multimode, Nanoscope IIIa, Digital Instruments) were recorded. Coercive voltage V_c , phase switch and amplitude of relative piezoresponse signal were

 $\begin{tabular}{ll} Table 2 \\ Typical PZT sputtering conditions and post annealed treatment \\ (PZT target = home made) \end{tabular}$

Target Composition	Stochiometric target (cold pressed powders/home made)
Target Diameter	7.62 mm (3")
Target-Substrate Distance	60 mm
Pressure	10^{-2} mbar
Gas	Ar
r.f. Power	110 W
Temperature	Ambient
Annealing Temperature	625°C
Annealing Time	30 min
Gas	Air

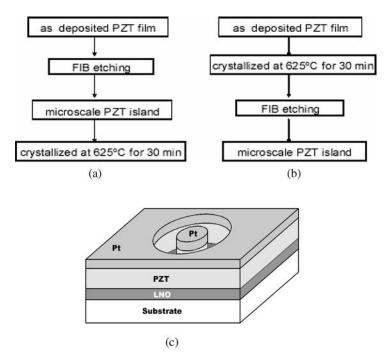


Figure 1. Flow chart of (a) amorphous FIB etching process, (b) conventional FIB etching process and (c) schematic view of FIB fabrication PZT island structure.

measured. The experiments were carried out by considering the so called "infield" mode which presents the particular characteristic, to be very close to the one used for "macroscopic" hysteresis loops recording [15-17]. In brief, PFM is based on the detection of bias-induced surface deformation; in this mode an alternative voltage $V = V_{ac}Cos(2\pi ft)$ is applied between a tip brought into contact with the surface of a piezoelectric sample and grounded bottom electrode. Due to the converse piezoelectric effect, the sample oscillates with the same frequency, causing oscillations of the tip/cantilever. The signal generated by the piezoelectric vibration is extracted from the topographic signal using a lock-in amplifier. The piezoelectric response of the surface (PFM signal which form is $A\cos\varphi$) is detected as the first harmonic component of bias-induced tip deflection. The phase φ of the electromechanical response of the surface yields information on the polarisation direction below the tip. For domains with polarisation vector pointing downward (c – domains), the application of a positive bias on the tip results in the expansion of the sample and the surface oscillations are in phase with tip voltage, $\varphi = 0$. For domains with polarisation vector pointing upward (c+ domains), $\varphi = 180^{\circ}$. The piezoresponse amplitude, PR = A/Vac, defines the local electromechanical activity of the surface. In

fact, both electromechanical response of the surface and electrostatic forces contribute to the PFM signal.

For our experiments, platinum/iridium coated silicon tip with a stiffness of 40 N/m was used. The DC voltage was gradually changed from -10 V to +10 V within a period of 1 second. The voltage step was adjusted to 0.4 V. The frequency of the AC voltage was 2 kHz with a 1.5 V driving voltage.

3. RESULTS AND DISCUSSIONS

Typical morphology of PZT islands of 500 nm and 100 nm in diameter are shown in Fig. (2); it's correspond to Scanning Electron Microscopy (SEM) photographies of PZT nanostrutures. Fig. 3 and Fig. 4 separately show the typical local phase (a) and amplitude (b) piezoelectric hysteresis loops of 500 nm and 100 nm PZT island fabricated by different etching process (amorphous etching process and conventional process). From these figures, it can be clearly seen that the PZT island fabricated by amorphous FIB etching process has much better electrical properties compared to conventional FIB etching, especially when the island size decreases. This means the domain switching of PZT island becomes more difficult after conventional etching process. The post annealing effect after conventional etching is also investigated; with up to 625°C post annealing in atmosphere for 30 minutes, the amplitude increases but still lower than the sample fabricated by amorphous FIB etching process. It implies that the atmosphere post annealing can compensate for the defects induced by the conventional etching process, but can only partially recovered the electrical properties. For the island size of 500 nm, the electrical properties degradation after conventional etching is more serious which is shown in Fig. 3. While the electrical properties of 500 nm PZT island fabricated by amorphous

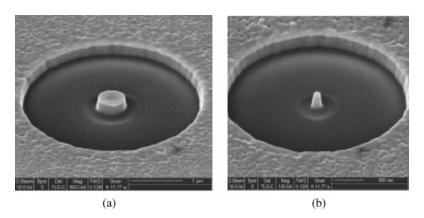


Figure 2. The morphology (SEM) of PZT island with different diameter (a)500 nm and (b)100 nm.

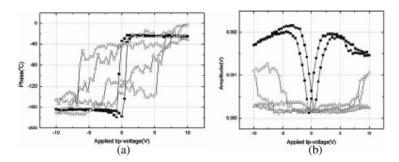


Figure 3. Typical phase (a) and amplitude (b) piezoelectric hysteresis loops of 500 nm PZT island fabricated by ■ amorphous FIB etching process, ○ conventional etching process and □ after post 625°C annealing.

FIB etching is almost unchanged. And there is neither domain switching nor piezoelectric amplitude in 100 nm PZT island fabricated by conventional etching process, even after the post annealing treatment. These notable electrical properties degradation of PZT island fabricated by conventional FIB etching maybe attributed to the side-wall effect, which is more serious when the PZT island size decreases. In Fig. 4(a), the hysteresis loop of 100 nm PZT island is slim and the coercive voltage is smaller compared to 1 μ m or 500 nm PZT island, which means the domain switching is easier with the decrease of PZT island size. Based on the equation 'piezoresponse (a.u.) = $A\cos\varphi$ ', the relative piezoresponse of PZT island fabricated by amorphous etching with different size is shown in Fig. 5.

Whatever the size of the island the piezoresponses of the films are maintained; it is of course very difficult to compare the island piezoresponses since it is necessary to take into account the interface between the film and the probe (presence of water, . . .).

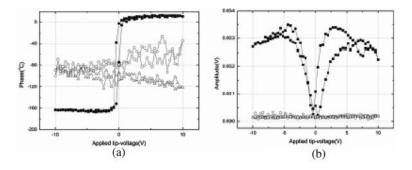


Figure 4. Typical phase (a) and amplitude (b) piezoelectric hysteresis loops of 100 nm PZT island fabricated by ■ amorphous FIB etching process, ○ conventional etching process and □ after post 625°C annealing.

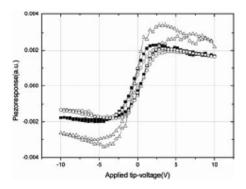


Figure 5. Relative piezoresponse of PZT island fabricated by amorphous etching with different size, $\blacksquare 1$ um, $\Delta 500$ nm and $\Box 100$ nm.

From the above results of local electrical properties of PZT islands, it can be seen that if FIB etching is performed before PZT crystallization, the nanoscale electrical properties of PZT islands can be great improved compared to conventional etching process, especially when the island size decreases. We attribute this phenomenon to the possibility that the side wall damage can be diluted if the PZT island is fabricated by amorphous FIB etching process. Since the FIB etching is performed before PZT crystallization, there is neither grain, grain boundary nor crystalline structure, so etching rate can be almost regarded as isotropic and the roughness will be reduced. For our experiment case, the roughness of the region after amorphous FIB etching is 1.2 nm, while the roughness of the region after conventional FIB etching is about 5 nm. Roughness reduction can improve the electrical properties of PZT film. The second reason is that for amorphous etching process, the damaged layer on amorphous PZT film surface induced by Ga ion bombardment can be almost crystallized into the ferroelectric perovskite phase by the following anneal treatment and there is little degradation on the surface. Charge defect on the PZT surface after etching may also be reduced after following anneal treatment and this will avoid the possibility of "short-circuit" on the side wall. During the island etching, the Ga ions implantation is just on the side wall of island but after annealing, Ga ions can migrate into the crystal lattice of PZT island and there are fewer charge defect left on the side wall. The third reason is that there is no domain in the amorphous PZT film, and it is well known that the charge defects induced by the ion bombardment can limit the domain mobility and increase the coercive voltage which can be proved by the results of figure 3 and figure 4. This effect will not exist in amorphous FIB etching case.

To simulate and analyze the FIB induced damage in the PZT island sidewall layer, another FIB etching experiment was performed, shown in Fig. 6. A large square area ($7 \text{ um} \times 7 \text{ um}$) on the surface of PZT film was FIB exposed with different ion doses to simulate sidewall damage. The ion dose is controlled

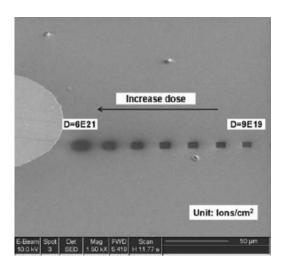


Figure 6. FIB exposed PZT layer with different Ga ion doses.

by the etching time. The lightest exposal is performed with an ion dose of about 9E19 ions/cm2 participating and the hardest is done with 6E21 ions/cm2 participating. Raman spectra in the 200~1000 cm-1 range were carried out with a Dilor XY-800 confocal micro-Raman spectrometer. The raman spectra were recorded using the 514.5 nm line of an Ar/Kr ion laser. The incident laser beam was focused on the FIB exposed area with 1um spot size and 10 mW power.

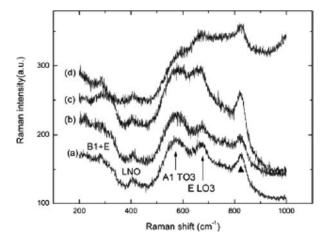


Figure 7. Raman spectra of (a) un-exposed crystallized PZT film, (b) after hardest amorphous FIB etching (6E21 ions/cm2 dose participating), (c) after lightest (9E19 ions/cm2) and (d) hardest conventional FIB etching (6E21 ions/cm2).

To confirm this suppose, a large square area $(7\text{um} \times 7\text{um})$ on the surface of PZT film was FIB exposed to simulate and analyze the FIB induced damage in the PZT island sidewall layer and the details of the experiment is mentioned above. Fig. 7 shows the Raman spectra of (a) un-exposed crystallized PZT film, (b) after hardest amorphous FIB etching (6E21 ions/cm2 dose participating), (c) after lightest (9E19 ions/cm2) and (d) hardest conventional FIB etching (6E21 ions/cm2).

In the spectra, the broadband centered at 280, 572 and 676 cm-1, can be assigned as B1+E, A1(TO3) and E(LO3), according to the modes in the single PZT crystals [17]. The band centered at 820 cm-1 is tentatively assigned to Ti-O bond [18]. The band detected at 400 cm-1 is attributed to LNO. After hardest amorphous etching process, the spectra profile of PZT surface is almost unchanged compared to un-etched PZT film surface. For the region after lightest conventional FIB etching, the spectrum is nearly not changed, except that the intensity of the Raman peak at 676 cm-1 increases. While for hardest conventional etching case, Raman spectra recorded in the region exposed to the FIB shows noticeable change. The sharp increase in the intensity of the Raman band at 676cm-1(E(LO3) mode) maybe connected with the distortion of the tetragonal symmetry of the PZT crystalline lattice due to the ion implantation and the modification of the stress state [19–21]. The broadening of Raman scattering in 550-900 cm-1 region may be attributed to the existence of the amorphous material which is usually expected during the conventional FIB etching process. Whatever the FIB etching process is, the peak position of Raman shift does not show any change, which means that the crystalline structure still exhibits perovskite.

CONCLUSION

We can get the conclusion that there is great enhancement of the nanoscale electrical properties on PZT island fabricated by amorphous FIB etching process Roughness reduction, little degradation of the surface and no domain etching can effectively reduce the side wall effect and make this etching process a very promising etching way with no electrical properties degradation, especially when the island size decreases. The Raman spectrscopy studies confirm these results. This amorphous etching process is very beneficial to the development of the piezoelectric film applications in the field of MEMS/NEMS.

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