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# Modelling Constrained Dynamic Software Architecture with Attributed Graph Rewriting Systems

Cédric Eichler<sup>123</sup>, Thierry Monteil<sup>23</sup>, and Patricia Stolf<sup>13</sup>

IRIT; 118 Route de Narbonne, F-31062 Toulouse, France
 CNRS, LAAS, 7 avenue du colonel Roche, F-31400 Toulouse, France
 Univ de Toulouse, UPS, INSA, F-31400, UTM, F-31100 Toulouse, France

Abstract. Dynamic software architectures are studied for handling adaptation in distributed systems, coping with new requirements, new environments, and failures. Graph rewriting systems have shown their appropriateness to model such architectures, particularly while considering the consistency of theirs reconfigurations. They provide generic formal means to specify structural properties, but imply a poor description of specific issues like behavioural properties. This paper lifts this limitation by proposing a formal approach for integrating the consideration of constraints, non-trivial attributes, and their propagation within the framework of graph rewriting systems.

**Keywords:** Dynamic software architectures, Graph rewriting systems, Constrained and attributed rewriting systems

#### 1 Introduction

Dynamic software architectures are studied for handling adaptation in autonomic distributed systems, coping with new requirements, new environments, and failures. The description of evolving architectures cannot be limited to the specification of a unique static topology but must cover the scope of all the correct configurations. This scope characterize an architectural style, qualifying what is correct and what is not. Naturally, once this distinction made, the question of the specification of the modifications themselves arises, alongside with theirs properties with regard to consistency preservation. These concerns motivate the need for suitable description languages and formalisms avoiding ambiguities for correct architectural design, management and analysis.

Several approaches have been proposed in the past adopting a language-based view of this problem. Many architecture description languages (ADLs) were introduced providing rigorous syntax and semantic to define architectural entities and relations [26, 22, 2, 13, 23]. Such languages are adequate to describe the structural and behavioural properties and constraints of a system. However, ADLs

suffer from several insufficiencies for modelling and analysing dynamic software architecture [16]. The majority of ADLs are concentrated on the structural description of architecture whereas their dynamic aspects are not well supported. Darwin [23] only allows component replication whereas ACME [13] only allows optional components and connections. Dynamic-Wright [1] describes dynamic architecture in Wright [2], limiting itself to predefined dynamics, meaning that the system should have a finite number of configurations and reconfiguration policies known in advance.

Model-based approaches, proposing general-purpose modelling languages, allows to handle dynamism and particularly the definition of reconfiguration rules managing the evolution of an application in run-time. They provide very intuitive and visual formal or semi-formal description of structural properties [4]. For example, designing and describing software models using UML [25] is a common practice in the software industry, providing a standardized definition of system structure and terminology, as well as facilitating a more consistent and broader understanding of the architecture [31]. Nevertheless the generic fitness of model-based approaches implies poor means of describing specific issues like behavioural properties. Therefore they are often coupled with description using ADLs [29, 24, 5], mapping the concepts of ADLs into the visual notation of UML, or other formalisms [21, 17]. In spite of its wide acceptance, UML-based descriptions appears to lack expressiveness and formal tools for guaranteeing consistency. In fact, consistency checking in run-time may lead to combinatorial explosions. To tackle efficiently consistency preservation in the scope of dynamic reconfiguration, correct by design formal approaches have emerged. Based on formal proofs and reasoning in design-time, they guarantee the correctness of a dynamic application, requiring little or no verifications in run-time.

Graph-based methods for software modelling are appropriate for conceiving correct by design frameworks, as theoretical work on this field provides formal means to specify and check structural constraints and properties [30, 11]. Within this kind of approaches, some methods are restricted to the use of type graphs alone [33] and suffer from the same lack of expressiveness as UML-based methods. Other works [14, 20] are based on graph grammar, or graph rewriting system, techniques. Graph grammars are appropriate for formal modelling dynamic structures and software architectures, and are used to specify architectural style where a graph represents a configuration. Graph rewriting rules have two distinct values. They intervene in both the characterisation of an architectural style as part of a rewriting system and in the specification of consistency preserving reconfiguration rules [15]. The very first thing to consider is the definition of attributes for which two approaches prevail. The most simple one is to assign to each elements, vertices and edges, of a graph a list of couples representing constant or variable attributes along with theirs domains of definition [7]. The main drawback is that it does not allow to conduct any operation on said attributes or to fully propagate them. The second one is to define domains of definitions

and operators in the form of a many sorted algebraic signature [12] SIG and to integrate attributes as vertices of the graph, elements of a SIG-algebra [3, 10]. A direct implication is a natural manipulation of attributes using operators and their addition or deletion as regular vertices of the graph. This modularity does not come without drawbacks. Graph rewriting rules relies on finding graph homomorphisms, a NP-complete problem. As a consequence, increasing the size of the input graphs seems inefficient, as well as multiplying the application of graph rewriting rules to modify attributes whenever a domino effect is implied.

The motivation of this paper comes from two simple statements. First, classical grammar theory allows complex propagation of attributes and the specification of elaborated semantic predicates on production rules. Propagation of attributes in graph grammars is very limited and often restricted to inheritance [7], restraining the means to model the impact of an event, such as a reconfiguration, on the attributes of an application. Semantic predicate are mostly present in the form of implicit system of simple equalities between attributes [7, 3, 10]. Secondly, these approaches focused on attributes and structural transformation, but disregard the appropriateness of a configuration. Yet constraints linked to such consideration are closely related to the architecture and its components. As a consequence, we wish, while constructing, deploying, or reconfiguring a configuration, to construct a set of constraints easy to evaluate. These lasts model some basic, more or less important, requirements of the application. As a result, their violation could be detected and automatically handled by a manager without requiring complex decision and without analysing the actual performances of the whole application.

A motivating example further enlightening the problems tackled by this article is presented in the next section. Section 3 introduces our formal model overcoming said obstacles. The characterisation of the motivating example is presented in Section 4, before discussing optimisation and evaluation issues. Finally, Section 5 is dedicated to conclusion and perspectives.

#### 2 Problem Statement throughout a Motivating Example.

#### 2.1 Motivating Example

In order to illustrate the issues addressed in this article, we introduce DIET<sup>4</sup> [6], a hierarchical load balancer for dispatching computational jobs over a distributed infrastructure, like a grid or a cloud. Its architecture consists of a set of agents: some Master Agents (MA) manage pools of computational SErver Deamons (SED) through none, one or several layers of Layer Agents (LA). These servers can achieve specialized computational services. Communications between agents are driven by the omniORB system (OMNI). MAs listen to client requests and dispatch them through the architecture to the best SED that can carry out

<sup>&</sup>lt;sup>4</sup> Distributed Interactive Engineering Toolbox

the required service.

We stated previously that UML-based modelling suffers from a lake of expressiveness. This application has been described using class diagrams [32], but the fact that a LA can manage another LA could not be taken into consideration. It will be shown later that graph grammars do not exhibit such limitations.

A simplified architecture with a single MA and a single OMNI will be considered here. The main characteristics of the application are as follow :

- 1. While being deployed, each component record itself to the OMNI.
- 2. Each LA and each SED has a hierarchical superior.
- 3. The MA and each LA manage at least one LA or one SED and at maximum ten of them these conditions could be trivially extended to any number of minimum and maximum managed entities, as seen later in this paper.

All instances of an architectural style are NOT created equals. At a given time, even though a configuration meet all the requirements of the application, another configuration may meet them in a "better way". In particular, we considered the following criteria: the energy consumption, the robustness, the fault-tolerance w.r.t. the breakdown of machine or a software component, and the quality of service. A very restricted view will be adopted here, with simple - simplistic, even - models as they still put the addressed problem under the spotlight.

Assume that the energy consumption depends only on the number of used machines and of the software components deployed on them.

Robustness is a little bit trickier. The first intuition is to exploit redundancy by deploying SEDs not having a disjoint set of carried out services. However, if every SED offering a particular service are deployed on the same machine, the application is still vulnerable to a machine breakdown. In a similar fashion, SEDs offering very close sets of services should not be managed by the same LA or LAs deployed on the same machine.

A single aspect of quality of service is studied: the balance of the managed entities over the set of LAs sharing the same depth, i.e. the number of intermediate LAs between a LA and the MA. Let LA(d) be the set of LA of depth d, and M(c) be the number of entities managed by the component c. An entity can be deployed and directly managed by a LA  $\in$  LA(d) if it does not make the standard deviation of  $\bigcup_{la\in LA(d)}$  M(la) become greater than an arbitrary value. An interesting point here is that robustness and energy consumption are concurrent, in the sense that deploying more software components or using more machines will, while ameliorating the first, impact badly on the second.

To value these three conditions, it is crucial to keep track of some attributes of the software components :

- 1. the machine on which each entity is deployed,
- 2. the number of entities managed by each LA and the MA,
- 3. the depth of each LA,

4. the set of services carried out by each SED or by at least a SED managed directly or indirectly by each LA.

By misuse of language the set of services carried out by at least a SED managed directly or indirectly by an LA will sometimes be referred to as the set of services carried out by the LA.

#### 2.2 Problem Statement

How is modelling such an architecture an issue? This seemingly simple problem still raises some interesting points underlining limitations of classical attributed graph grammars.

- Interdependency of attributes; the attributes of an entity depends on attributes of other entities. In classical attributed grammars, attributes are classified according to the fact that those other entities are parents or siblings of the first one, it is then said to be inherited, or not, in the case of a synthesized attribute. Similarly, those dependencies have to be handled in a very different way whether they rely on existing attributes or attributes of entities that has yet to be deployed. The first scenario, including the evaluation of the depth of an LA, which depends on the depth of the entity managing it, have been addressed with attributes inheritance in graph grammars. On the other hand, evaluating the set of services offered by at least a SED managed directly or indirectly by an LA can not be treated in the same fashion.
- Modification of an existing attribute; the option of considering attributes as nodes has been discarded due to its high implementation complexity. Consequently, graph rewriting rules cannot be used to modify attributes, and their modification are therefore not possible within the framework of classical graph grammars.
- Conditional deployment; whenever trying to deploy a LA or a SED, we should verify that it does not violate the maximal cardinality condition and the balancing condition. The first one could be handled in a structural, syntactic way using negative application conditions, implying other search for homomorphisms and thus a high computational complexity. The balancing condition, however, is too complex to be reasonably managed using pattern matching.
- Evaluating a configuration; we have seen that the constraints reflecting the appropriateness of a configuration are intimately related to attributes of the graph and do not weight on the same levels or components. Particularly, components of the same nature have close requirements that arise or evolve as components are deployed or as the context evolve, so that integrating such constraints in the model as any attributes is relevant. In addition, the entity managing the evolution of the architecture could then react quickly to their violation, without requiring much reasoning.

These four points put under the spotlight the limits of the formalism presented in [28] and the need for its expansion described in this paper.

#### 3 Introducing Constraints and Mutators within Graph Rewriting Systems

#### 3.1 Attributes, Constraints and Attributes Rewriting

Before introducing graph rewriting systems and how they may be used to model a dynamic software architecture, a certain number of concepts have to be defined. First of all, let us consider what an attribute shall be. We adopt an hybrid formalism conserving the simplicity and the computational efficiency of "listing" attributes as labels while granting the possibility of applying any operator. Informally, an attribute is represented as a constant, a variable or the result of any function on other attributes, as long as the signature of the function is respected, coupled with its domain of definition. The expression of the function may includes quantifiers. We assume the canonic notation where  $\mathbf{Y}^X$  is the set of function from X to Y.

#### **Definition 1.** (Attribute) An attribute is a couple $Att = (Att_A, Att_D)$ where

- Att<sub>A</sub> is called value and is either
  - a variable,
  - a constant or
  - an expression, a regular combination of attributes :  $\forall n \in \mathbb{N}, \forall$  sequence of sets  $(D_u)_{u \in [[1,n]]}, \forall f \in D^{D_1 \times D_2 \times \cdots \times D_n}$ , a couple  $Att = (f(A_1,...,A_n), Att_D)$  is a regular combination of attributes of  $(A_i, D_{A_i})_{i \in [[1,n]]}$  if and only if  $Att_D \subseteq D$  and  $\forall i \in [[1,n]], (A_i, D_{A_i})$  is a (defined) attribute and  $D_{A_i} \subseteq D_i$ .
- and Att<sub>D</sub> its domain of definition.

An attributed structure is a couple composed of the structure and a set of indexed attributes or sequence of attributes. The first member of an attribute will be noted within quotation marks if and only if it is constant. The fact that the first member of an attribute is constant does not implies that it can not change any further, but rather that at a given time the attribute is equals to a member of its domain of definition.

Remark 1. In the approach presented here the attributes can be a regular combination of any other attributes, not necessary the attributes of the same structure. For example, considering an attributed structure (A, ATT<sub>A</sub>) of attributed structures (B<sub>i</sub>, ATT<sub>B<sub>i</sub></sub>) of attributed elements (C<sup>i</sup><sub>j</sub>, ATT<sub>C<sup>i</sup><sub>j</sub></sub>). Let ATT = ATT<sub>A</sub>  $\cup$   $\bigcup_i ATT_{B_i} \cup \bigcup_{(i,j)} ATT_{C^i_j}$ .  $\forall$  Att  $\in$  ATT,  $\forall N \in \mathbb{N}$ ,  $\forall$  (Att<sub>k</sub>)<sub>k \in [|1,N||</sub>  $\in$  ATT<sup>N</sup>, Att can be a regular combination of (Att<sub>k</sub>)<sub>k \in [|1,N||</sub>. From now on, we adopt the definition

Remark 2. Note that this notation is related to the approach considering many-sorted signatures and algebras as elements are attributed over an implicit SIG-algebra. Informally, the sorts of SIG are every defined domains of definition and its operations are every functions from any combination of sorts to any sort. For any attributed structure  $(str, ATT_{str})$ , let ATT be the set of attributes resulting of the union of any attribute defined in the framework of str, which is not empty as at least  $ATT_{str} \subseteq ATT$ . Let  $S = \{s \mid \exists Att, (Att, s) \in ATT\}$  and  $OP = \{f \mid \exists N \in \mathbb{N}, \exists (Att_i)_{i \in [|0,N|]}, f \in Att_0^{Att_1 \times Att_2 \times \cdots \times Att_N}\}$ . str is attributed over a (S,OP)-algebra.

Attributes are entirely aimed at providing informations on an algebraic structure. It is very natural to desire to exploit these bits of information by expressing any property over a structure or its attributes. Those having been defined in a very generic way, constraints can be seen as a specific kind of attributes taking values in a ternary predicate logic system.

**Definition 2.** (Constraint) A constraint Cons = (C, D) is an attribute (C, D) and  $D = \{ \text{"true"}, \text{"false"}, \text{"unknown"} \}.$ 

To simplify the notation, considering that constraints share the same domain of definition, a constraint Cons = (C, D) may be referred to as C, and the domain of definition be implicit. In the following we adopt the principles of Kleene's strong logic [18] [19], in particular its basic logic operations  $(\lor, \land, \neg, \Longrightarrow)$  and the fact that the only truth value is "true". The uniqueness of this truth value means that evaluations are pessimistic, i.e. "unknown" is supposed to be false.

Remark 3. A constraint, as a regular combination of attributes, can be seen as a classical expression of a predicate ternary logic. Considering a ternary logic rather than a binary one implies that unlike attributes, constraints can be evaluated, i.e. associated with a constant, at any time. The idea here is to associate any minimal logic expression that can not be evaluated, due for example to an attribute implied in its expression being un-evaluable or variable, with "unknown".

Example 1. Let S be any mathematical structure along with the set of attributes ATT = {Att<sub>1</sub>, Att<sub>2</sub>, Att<sub>3</sub>} = {(A<sub>1</sub>, Mach), (A<sub>2</sub>, Mach), (A<sub>3</sub>, {"true", "false", "unknown"})} where  $A_1 = m$ ,  $A_2 = "\bar{m}"$ , and  $A_3 = "true"$  and the set of constraints CONS = {C<sub>1</sub>, C<sub>2</sub>} = { (A<sub>1</sub>  $\neq$  A<sub>2</sub>), (C<sub>1</sub>  $\vee$  A<sub>3</sub>) }. Even though Att<sub>1</sub> can not be evaluated to a constant, C<sub>1</sub> can be evaluated to "unknown". C<sub>2</sub> is then equals to "unknown"  $\vee$  "true", which is "true".

In order to lighten the notation, an attributed system with constraints, i.e. a triple composed by the system, a set of indexed attributes or sequence of attributes, and a set of indexed constraints or sequence of constraints, is noted an AC-system. Whenever defining an AC-system with AC-elements, rather than separating each sets of attributes (resp. constraints), a single family of sequence of attributes indexed by the sets of attributed elements is considered.

One of the issues evoked in section 2 is the fact that attributes are prone to evolve. A reconfiguration may thus impact the attribute of the system. Classical string rewriting theory [27] tackles this issue by using mutators. A similar approach is adopted here.

**Definition 3.** (A mutator on an AC-system) A mutator on an AC-system is an arbitrary algorithm updating the value(s) of none, one or some attributes of the AC-system.

According to this definition, the scope of mutators remains limited to the modification of values. They can not be used neither to add or suppress an attribute nor to modify the domain of definition of an attribute.

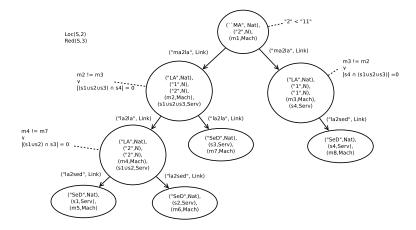
#### 3.2 Attributed Constrained Graph Modelling a Configuration

Tools to manipulate and analyse attributes having been introduced, the structures of interest can be presented. An AC-graph, modelling a software snapshot or configuration at a given time, consists in an AC-couple of two AC-sets of vertices and edges where an edge is a couple of vertices (source, destination). Following the commonly used conventions for standard graphical descriptions, one considers that vertices represent services or architectural components and edges correspond to their related interdependencies. Note that vertices, edges and the graph itself are AC-structures. According to remark 1, elements are attributed over the same algebra, i.e. an attribute or a constraint of the graph, a vertex or an edge may consist in any regular combination of attributes of the graph, any vertex and any edge. For any set S, the cardinality of S is represented as |S|.

**Definition 4.** (AC-graph) An AC-graph is defined by the system G = (V, E, ATT, CONS) where

- V and  $E \subseteq V^2$  correspond respectively to the set of vertices and edges of the graph,
- ATT (resp. CONS) is a family of sets  $ATT_{el}$  (resp.  $CONS_{el}$ ), indexed by a subset of  $V \cup E \cup \{G\}$ .  $ATT_{el}$  is a set of attributes (resp. constraints) of arbitrary length and containing the sequence of attributes  $(ATT_{el}^i = (A_{el}^i, D_{el}^i))_{i \in [|1, |ATT_{el}||}$  (resp.  $(CONS_{el}^i = (C_{el}^i, D_{el}^i))_{i \in [|1, |CONS_{el}||})$  of the element el.

ATT and CONS are indexed by a subset of  $V \cup E \cup \{G\}$ , so as not to impose an empty set of attributes or constraint if an element is wished not to be attributed or constrained. Consequently,  $0 \le |\text{ATT}| \le |V| + |E| + 1$  and  $0 \le |\text{CONS}| \le |V| + |E| + 1$ . Now that AC-graphs are defined, it is possible to represent a configuration of DIET as presented in section 2.



 ${\bf Fig.\,1.}$  An AC-graph modelling a configuration of DIET

Example 2. Figure 1 represents an AC-graph modelling some configuration of DIET, where the naming server has been omitted for clarity concerns. The graph is formally defined in Appendix A.

The notations used in Fig. 1 are as follow; Mach is the set of available machines, on which a software component may be deployed, Nat the set of possible nature of a software component, {"OMNI", "MA", "LA", "SED"} in the case of a DIET architecture, Link the set of possible relationships {"ma2la", "ma2sed", "la2sed", "registered", and Serv the power set of S, the set of services that could be carried out by a SED. At this time, the software architecture is composed by eight components symbolized by eight vertices and theirs corresponding relations modelled by some edges both with theirs attributes. A notable fact is that components of the same nature have the same number of attributes, theirs attributes being the one identified in Sect. 2. This stem from the definition of the rewriting system that will be presented later in this paper. Attributes have similar meanings, be it the number of managed entities, the depth of a LA... In addition, some components as well as the graph itself are constrained to reflect the concerns stated in the same section. Graphically, a dot line between a constraint and a vertex illustrates the target of said constraint. The MA should manage strictly less than eleven entities, underlining a fundamental property of the architectural style. Even though the maximum number of entities managed by the LAs could be expressed in the same way, an other technique will be used and presented later. To cope with robustness, it is specified that a LA should not be deployed on the same machine as a component managed by the same entity as itself or that they should have disjoint set of carried services. The graph is constraints by two clauses Loc and Red, described in Appendix A, taking into account the needs for redundancy and multiple localisations over the offered services.

This is how a DIET configuration should look like. From now on, our aim is to introduce the notions allowing to characterize the corresponding architectural style, ensuring in particular that attributes are correctly updated and that components have the required constraints.

#### 3.3 Graph Rewriting Rules and Systems

As stated in the introduction, an architectural style can be formalised using a graph rewriting system. The production rules of such systems require to identify sub-structures by the means of homomorphisms. An unattributed graph homomorphism between two graphs is defined as an homomorphism from the set of vertices of the first one to the set of vertices of the second graph so that if there is an edge between two vertices of the first one there is an edge between their image in the second one. Based on [28], we define an attributed graph homomorphism as follow.

**Definition 5.** (AC-graph homomorphism) Two AC-graphs G = (V, E, ATT, CONS) and G' = (V', E', ATT', CONS') are homomorph -noted  $G \rightarrow G'$ - if and only if there is a graph-homomorphism h from (V, E) to (V', E') such as

```
1. \forall v \in V \text{ (resp. } \forall e = (\bar{v}, \tilde{v}) \in E), |ATT_v| = |ATT_{h(v)}| \text{ (resp. } |ATT_e| = |ATT_{(h(\bar{v}),h(\tilde{v})}|),
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2.  $\forall v \in V \text{ (resp. } \forall e = (\bar{v}, \tilde{v}) \in E), \forall i \in [1, |ATT_v|], D_v^i = D_{h(v)}^i,$ 

- 3. The system of equations  $S = \{A = A' \mid (\exists v \in V, \exists i \in [1, |ATT_v|], A = A_v^i \land A' = A_{h(v)}^i) \lor (\exists e = (\bar{v}, \tilde{v}) \in E, \exists i \in [1, |ATT_e|], A = A_e^i \land A' = A_{h(\bar{v}, h(\bar{v}))}^i) \}$  has at least one solution.
- Remark 4. Constraints do not impact on the definition of an homomorphism. It will be shown that they intervene in the rewriting process in a different way. Similarly, attributes on vertices and edges are the only one that are considered whereas attributes on the graph itself are not.
- We underline the fact that the existence of an AC-homomorphism is conditioned by the resolvability of a system of equations on attributes. As stated in the introduction, in [3, 21, 14, 20] and most of the research on attributed graphs, the existence of a morphism is also conditioned by equalities between attributes, potentially through morphism between attributes spaces. However, this is often the only clause relying on attributes that impact the applicability of a graph rewriting rule.
- AC-graph isomorphism and AC-vertex morphism can be trivially deduced from the definition of AC-graph homomorphism.

Solving the system of equation S result in identifying the value of some attributes with some constants in theirs domains of definition and/or with some other attributes. Integrating the affectation obtained by solving this system refers to the update of the value of the attribute to reflect these identifications. Intuitively, such an integration is nothing more than a dimension reduction of

the space formed by the potential values of the attributes. For more information see [28].

There exists a vast number of approaches handling graph rewriting based on attributed graphs [30, 9]. Their applicability depends on various factors, suspension condition, negative application condition(s), always including the existence of an homomorphism between an element of the graph rewriting rule and the graph to rewrite. Inspired by string grammar theory [27], we include in these factors the satisfaction of a, potentially empty, set of constraints on attributes, namely the set of constraints of the AC-rewriting rule. Consequently, the set of constraints on a graph rewriting rule can be seen as a set of semantic predicates.

Applying a rewriting rule on a graph consists in suppressing a part of the graph and extending it by adding some vertices and edges. In addition to classical modifications induced by the application of a rule, a set of actions is performed at the end of said application.

Virtually, any attributed graph rewriting formalism could be extended to include semantic predicates, constraints and mutators. In order to fix the idea, we chose the classical double push out formalism defined in [30] alongside with the attribute management considered previously and used in [28].

**Definition 6.** (AC-graph AC-rewriting rule) An AC-graph AC-rewriting rule is a 5-tuple (L, K, R, ATT, CONS, ACT) where

- $ATT = ATT_L \cup ATT_R$  is a set of attributes,  $ATT_{rule}$  being the set of attributes of the graph rewriting rule itself,
- $CONS = CONS_{rule} \cup CONS_{R \setminus K}$  is a set of constraints,  $CONS_{rule}$  being the set of constraints of the graph rewriting rule itself and  $CONS_{R \setminus K}$  the set of constraints of  $R \setminus K$ ,
- $(L = (V_L, E_L), ATT_L, \emptyset)$  and  $(R = (V_R, E_R), ATT_R, CONS_{R\setminus K})$  are AC-graphs,
- $-K = (V_K, E_K)$  is a sub-graph of both L and R,
- ACT is a set of actions.

 $L\backslash K$  is called the Del zone and  $R\backslash K$  is called the Add zone, while K is called the Inv zone.

Furthermore, graph rewriting rules are illustrated here using the delta representation, where only one graph is considered. This graph is visually partitioned into three zones, from left to right the Del, Inv and Add zones. This graphical representation is illustrated in Fig. 2.

A rule is applicable on a AC-graph G if:

1. there is an AC-homomorphism h : (L, ATT<sub>L</sub>, CONS<sub>L</sub>)  $\rightarrow$  G, implying in particular that the system of equations S = { A = A' | ( $\exists$  v  $\in$  V<sub>L</sub>,  $\exists$  i  $\in$  [|1, |ATT<sub>v</sub>||], A = A<sup>i</sup><sub>v</sub>  $\land$  A' = A<sup>i</sup><sub>h(v)</sub>)  $\lor$  ( $\exists$  e = ( $\bar{v}$ ,  $\tilde{v}$ )  $\in$  E,  $\exists$  i  $\in$  [|1, |ATT<sub>e</sub>||], A = A<sup>i</sup><sub>e</sub>  $\land$  A' = A<sup>i</sup><sub>(h( $\bar{v}$ ,h( $\bar{v}$ ))) } has at least a solution,</sub>

- 2. the application of the rule would not lead to the apparition of any dangling edge.
- 3. each  $\operatorname{Cons} \in \operatorname{CONS}_{rule}$  is evaluated to "true" by integrating the affectations obtained by solving S and by evaluating each elementary logic expression containing variable attributes to "unknown" as stated in remark 3.

Its application consists in:

- 1. erasing  $h(L\backslash K)$ ,
- 2. integrating the affectations obtained by solving S to the remaining graph,
- 3. adding an isomorph copy of R\K integrating the affectations obtained by solving S.
- 4. performing each action  $Act \in ACT$ .

Inspired from Chomsky's generative grammars [8], graph grammars are defined as a classical rewriting system and formally characterize an architectural style.

**Definition 7.** (Graph Grammar) A graph grammar is defined by the 4-tuple (AX, NT, T, P) where

- AX is the axiom, an AC-graph with a single vertex AX
- NT is the sets of non-terminal archetypes of AC-vertices,
- T is the set of terminal archetypes of AC-vertices,
- P is the set of AC-graph AC-rewriting rules, or production rules, belonging to the graph grammar.

Each vertex occurring in a graph rewriting rule in P or in a graph obtained by applying a sequence of productions  $\in P$  to the axiom is then isomorph to at least one arch-vertex in NT or T.

*Remark 5.* Semantic predicates allow to easily guarantee the finition of such rewriting systems, by imposing, for example, a limited number of deployed components.

Definition 8. (Instance belonging to the graph grammar) An instance belonging to the graph grammar (AX, NT, T, P) is a graph whose vertices and edges have only constant attributes and obtained by applying a sequence of productions in P to AX. If an instance does not contain any vertex isomorph to an arch-vertex from NT it is said to be consistent.

Graph rewriting systems are illustrated in the next section, where they are used to characterize the example presented in Sect. 2.

#### 4 Characterisation of the Motivating Example.

By characterising the motivating example using the formalism presented in the previous section, it appears undoubtedly that it addresses the problems identified in Sect. 2.

#### 4.1 Components arch-types: Terms of the Grammar

Firstly, let us consider what the axiom shall be. Considering the definition of graph rewriting rules and systems, instances of the such systems are graphs that inherit the attributes and constraints of the axiomatic graph. We wish to expressed the redundancy and localisation constraints on instances of the architectural style, as described in appendix A, as well as the maximum number of entities that a MA or a LA may manage and the arbitrary value intervening in the balancing condition. Therefore, let  $AX_{DIET}$  be  $(v_{AX}, ATT_{AX} = ((maxSon-sLA, \mathbb{N}), (maxSonsMA, \mathbb{N}), (max\sigma, \mathbb{R}^+))$ ,  $CONS_{AX} = (L(S,2),R(S,3))$ , where here maxSonsMA = maxSonsLA = 10. Throughout this section the graph, on which production rules will be attempted to be applied to, is noted G = (V, E, ATT, CONS). Note that as stated in this paragraph, if G is an instance of the architectural style defined here,  $ATT = ATT_{AX}$  and  $CONS_G = CONS_{AX}$ .

Secondly, terminal terms of the graph rewriting system characterizing DIET have to be defined. As they are AC-vertices, defining their attributes and constraints is sufficient. Remember that these term are arch-vertices and defined a pattern of attributes and constraints for the vertices of each instance. The first ones have already been listed in Sect. 2, while constraints stem from considerations exposed in the same section. The naming system itself is not constrained and is quite simple, let  $T_{Omni}$  be  $(v_{Omni}, ATT_{Omni} = (("Omni", Nat), (m,$ Mach)),  $\emptyset$ ). Similarly, let  $T_{SeD} = (v_{SeD}, ATT_{SeD} = (("SeD", Nat), (s, Serv),$ (m, Mach)), ∅). The MA shall not manage more than 10 entities. Accordingly, let  $T_{MA}$  be  $(v_{MA}, ATT_{MA} = (("MA", Nat), (Nsons, N), (m, Mach)), CONS_{MA})$  $=((A_2 < ATT_{AX}^2))$ ). Finally, we stated before that LAs of the same layer should not be deployed on the same machine or have disjoint set of carried out services. Hence, let  $T_{LA}$  be  $(v_{LA}, ATT_{LA} = (("LA", Nat), (depth, N), (Nsons, N), (m,$ Mach), (s, Serv)),  $CONS_{LA} = c(v_{LA})$ ), where  $c(v) = (c(v)_i)_{i \in [|1,|LA((ATT_G)_v^2)|-1|]}$ and  $\forall \tilde{v} \in LA((ATT_G)_v^2) \setminus \{v\}, \exists i \in [|1, |LA((ATT_G)_v^2)| - 1|], c(v)_i = ((ATT_G)_v^4)$  $\neq (ATT_G)_{\tilde{v}}^4 \vee ((ATT_G)_{\tilde{v}}^5 \cap (ATT_G)_{\tilde{v}}^5 = \emptyset).$ 

#### 4.2 Productions of the Rewriting System

Production rules of the graph grammar formalize the construction of instances by defining when and how an entity may be deployed and the consequences of such a deployment. The first rule to define is the initialisation consuming the axiomatic vertex (Del). The naming service and the MA are deployed, as well as a non-terminal vertex granting that the MA manages at least an entity (Add). This vertex will be later on instantiated into a LA or a SeD. Finally, the MA register to the naming service through the action register. Let  $p_1 = (L_{p_1}, K_{p_1}, R_{p_1}, \emptyset, \mu_{registering}(pv2))$ , where  $L_{p_1}, K_{p_1}, R_{p_1}$  and pv2 are defined in Fig. 2 and  $\mu_{registering}(v)$  is the action of registering the object represented by the vertex v to the naming service.

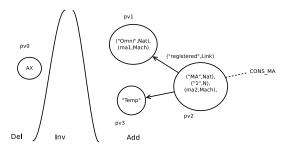


Fig. 2. Initialisation

Productions rules  $p_2$  and  $p_3$  model the addition of a non-terminal vertex, that will later on be instantiated into a LA or a SeD, and manage respectively by the MA or a LA. To deploy a new entity under a LA, the balancing condition and the maximal number of managed entities should be respected. The application of one of these productions leads to the incrementation of the number of sons of the entity on which the terminal vertex is added. Let  $p_2 = (L_{p_2}, K_{p_2}, R_{p_2}, \emptyset, \mu_{inc}(\text{pv2MA},2))$  and  $p_3 = (L_{p_3}, K_{p_3}, R_{p_3}, \text{ (balancing}(\text{pv2LA}), \text{ Nsons} < \text{ATT}_{AX}^1), \mu_{inc}(\text{pv2LA},3))$  where  $L_{p_2}, K_{p_2}, R_{p_2}$ , pv2LA,  $L_{p_3}, K_{p_3}, R_{p_3}$ , and pv2MA are defined in Fig. 3, while  $\mu_{inc}(v, i)$  is described below and balancing(v) =  $\sigma(((\text{ATT}_G)_{la}^3)_{la\in LA(d)\setminus\{v\}}, (\text{ATT}_G)_v^3+1) < \text{ATT}_{AX}^3$ , where  $\sigma(s)$  is the standard deviation of the sequence s.

$$\mu_{inc}(v, i)$$

$$Att_v^i \leftarrow Att_v^i + 1$$

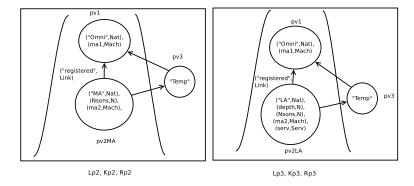


Fig. 3. Addition of a non-terminal term

The instantiation of a temporary vertex into a SeD is described by  $p_4$  and  $p_5$  if it is managed by respectively the MA or a LA. After deploying the SeD, it has to

register to the naming service and, if it is managed by a LA, an update of carrying out services must be conducted. Let  $p_4 = (L_{p_4}, K_{p_4}, R_{p_4}, \emptyset, \mu_{registering}(pv4))$  and  $p_5 = (L_{p_5}, K_{p_5}, R_{p_5}, \emptyset, (\mu_{registering}(pv4), \mu_{updateServ}(pv2,pv4))$ , where  $L_{p_4}$ ,  $K_{p_4}$ ,  $R_{p_4}$ ,  $L_{p_5}$ ,  $K_{p_5}$ ,  $R_{p_5}$ , pv2, and pv4 are defined in Fig. 4, and  $\mu_{updateServ}(v, \tilde{v})$  is as follows.

```
\mu_{updateServ}(v, \tilde{v})
oldServ \leftarrow (Att_G)_v^5
ind \leftarrow 5
if (A_G)_{\tilde{v}}^1 = \text{"SeD"}
ind \leftarrow 2
(Att_G)_v^5 \leftarrow oldServ \cup (Att_G)_{\tilde{v}}^{ind}
if (Att_G)_v^5 \neq oldServ
\bar{v} \leftarrow v' \in V_G, (v', \bar{v}) \in V_E
\mu_{updateServ}(\bar{v}, v)
```

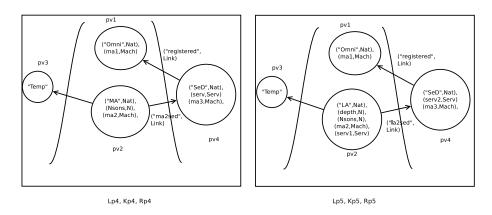


Fig. 4. Instantiation of a non-terminal term into a SeD

The two last productions of the grammar,  $p_6$  and  $p_7$ , describe the instantiation of non-terminal term into a LA managed respectively by the MA or a LA. Let  $p_6 = (L_{p_6}, K_{p_6}, R_{p_6}, \emptyset, \mu_{registering}(pv4))$  and  $p_7 = (L_{p_7}, K_{p_7}, R_{p_7}, \emptyset, \mu_{registering}(pv4))$ , where  $L_{p_6}$ ,  $K_{p_6}$ ,  $R_{p_6}$ ,  $L_{p_7}$ ,  $K_{p_7}$ ,  $R_{p_7}$ , and pv4 are defined in Fig. 5.

## 4.3 The Constrained Attributed Graph Rewriting System Characterizing the Motivating Example

Considering the definition introduced in this section,  $GRS_{DIET}$ , the graph rewriting system formally characterizing the motivating example introduced in Sect. 2, is defined as  $GRS_{DIET} = (AX_DIET, NT_{DIET}, T_{DIET}, P_{DIET})$ , where  $NT_{DIET} = (v_{temp}, ATT_{temp} = (\text{"temp"}, \{\text{"temp"}\}), CONS_{temp} = \emptyset)$ ,

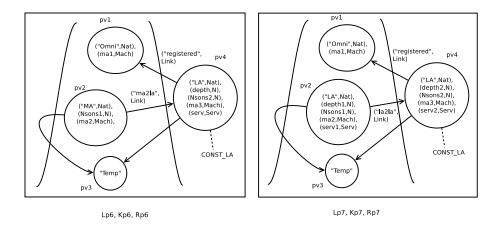


Fig. 5. Instantiation of a non-terminal term into a LA

$$T_{DIET} = \{ T_{Omni}, T_{MA}, T_{LA}, T_{SeD} \}, \text{ and } P_{DIET} = \{ p_1, p_2, p_3, p_4, p_5, p_6, p_7 \}.$$

Remark 6. Efficiently representing evolving attributes. An interesting feature of this formalism is that it offers several options for the characterization of problematic attributes or constraints. Let us consider the set of services that may be carried out by at least a SeD managed directly or indirectly by a LA, for example. Intuitively, this attribute depends on other ones and could be expressed as a regular combination of the graph attributes. However, this combination should have been evaluated whenever its value is required, for example to evaluate the constraints of a LA. To avoid frequent evaluations, the graph representing the configuration can be seen as an augmented data structure and the attribute be updated whenever it has to be using mutators. The choice between these two options rely on the complexity and frequency of updates and evaluations.

This model allows to express each limitation identified in Sect. 2, interdependency between attributes, modification of existing attributes and non-trivial requirements for conditional deployments. Furthermore, components of the architecture are constrained to reflect the appropriateness of a configuration. These constraints are inherited from arch-vertices as a component is deployed, and may evolve like any attribute to reflect context changes.

Evaluation of a configuration can be done in several ways. The most straightforward would be to assign a, potentially infinite, weight to the violation of constraints to define management policies through heuristics. However, constraints on an architecture and their satisfaction can be seen as a way of granting some desirable properties rather than looking for an optimum. Considering that finding a "good" configuration as a classical multi-criteria optimisation problem, it could be solved by restraining it to a single-criteria, here energy consumption,

problem over the space of consistent configurations that does not violate any constraint.

#### 5 Conclusion

This article lift limitations of formal approaches for the characterisation of dynamic software architectures using graph rewriting systems. Said limitations are put under the spotlight using an example of hierarchical dynamic software architecture that previous approaches have failed to characterize, and that later on serve as an illustration for our model. In particular, interdependency of attributes is handled using regular combination of attributes, and mutators are introduced to modify existing attributes. Consequently, non-trivial attributes related to structural and behavioural properties of the architecture may be handily represented, as shown in the example. Based on such attributes, dynamic constraints are included in the formal model, reflecting the appropriateness of a configuration, arising or evolving as components are deployed or as the context change. Moreover, further properties can be guaranteed as the formalism presented in this paper tackles non-trivial conditional deployments using semantic predicates.

Constraints and attributes facilitate the evaluation of a configuration, a basis for the definition and the enforcement of reconfiguration policies. We plan on developing the mechanisms evoked in this paper to achieve these aims, as well as extending existing tools to realise management using this formalism and validate its time efficiency with regard to other graph-based approaches.

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#### A Formal definition of the graph presented in Fig 1

The graph in the Fig. 1 is defined as follow.

```
\begin{split} &G = (\text{V, E, ATT, CONS}) \text{ where} \\ &V = \{\text{v}_1, \text{v}_2, \text{v}_3, \text{v}_4, \text{v}_5, \text{v}_6, \text{v}_7, \text{v}_8\}, \\ &E = \{\text{ e}_1 = (\text{v}_1, \text{v}_2), \text{ e}_2 = (\text{v}_1, \text{v}_3), \text{ e}_3 = (\text{v}_2, \text{v}_4), \text{ e}_4 = (\text{v}_2, \text{v}_7), \text{ e}_5 = (\text{v}_3, \text{v}_8), \text{ e}_6 \\ &= (\text{v}_4, \text{v}_5), \text{ e}_7 = (\text{v}_4, \text{v}_6)\}, \\ &ATT = \{\text{ATT}_G, \text{ATT}_{v_1}, \text{ATT}_{v_2}, \text{ATT}_{v_3}, \text{ATT}_{v_4}, \text{ATT}_{v_5}, \text{ATT}_{v_6}, \text{ATT}_{v_7}, \text{ATT}_{v_8}, \\ &ATT_{e_1}, \text{ATT}_{e_2}, \text{ATT}_{e_3}, \text{ATT}_{e_4}, \text{ATT}_{e_5}, \text{ATT}_{e_6}, \text{ATT}_{e_7}, \text{ATT}_{e_8}\}, \\ &\text{The architecture is composed by some software component, the first one being } \\ &v_1, \text{ a MA managing 2 entities and deployed on a machine noted } \\ &\{(\text{"MA", Nat}), (\text{"2", N}), (\text{m}_1, \text{Mach})\}. \end{split}
```

Three LAs are deployed, respectively of depth 1, 1 and 2, managing 2, 1 and 2 entities, placed on machine  $m_2$ ,  $m_3$  and  $m_4$ . The first one managed directly or indirectly SEDs providing the set of services  $s_1 \cup s_2 \cup s_3$ , the second one  $s_4$  and the third one  $s_1 \cup s_2$ .

```
\begin{array}{l} \text{ATT}_{v_2} = \{(\text{"LA", Nat}), (\text{"1", N}), (\text{"2", N}), (\text{m}_2, \text{Mach}), (\text{s}_1 \cup \text{s}_2 \cup \text{s}_3, \text{Serv})\}, \\ \text{ATT}_{v_3} = \{(\text{"LA", Nat}), (\text{"1", N}), (\text{"1", N}), (\text{m}_3, \text{Mach}), (\text{s}_4, \text{Serv})\}, \\ \text{ATT}_{v_4} = \{(\text{"LA", Nat}), (\text{"2", N}), (\text{"2", N}), (\text{m}_4, \text{Mach}), (\text{s}_1 \cup \text{s}_2, \text{Serv})\}. \end{array}
```

Finally, four SEDs deployed on  $m_5$ ,  $m_6$ ,  $m_7$  and  $m_8$  carry out the services  $s_1$ ,  $s_2$ ,  $s_3$  and  $s_4$ .

```
ATT_{v_5} = \{(\text{"SED",Nat}), (s_1, Serv), (m_5, Mach)\}, ATT_{v_6} = \{(\text{"SED",Nat}), (s_2, Serv), (m_6, Mach)\}, ATT_{v_7} = \{(\text{"SED",Nat}), (s_3, Serv), (m_7, Mach)\}, ATT_{v_8} = \{(\text{"SED",Nat}), (s_4, Serv), (m_8, Mach)\}.
```

The graph itself is constraints by L(S,2) and R(S,3), where R and L are as follows.  $\forall S' \subseteq S, \forall x_s \in \mathcal{N}$ , let the redundancy constraint R(S',x<sub>s</sub>) be "There are at least x<sub>s</sub> SeDs carrying each service s in S'".

 $\forall s \in S, \forall x_s \in \mathcal{N}$ , let the localisation constraint L(S',x<sub>s</sub>) be "For each service in S', there are at least x<sub>s</sub> different machines on which at least a SeD carrying out the service s is deployed".

$$Loc(S',x_s) = \forall s \in S', \exists (V_i)_{i \in [|1,x_s|]} \in V_A^{x_s}, (\forall (i,j) \in [|1,x_s|]^2, i \neq j \implies (V_i \neq V_j \land ATT_{V_i}^3 \neq ATT_{V_i}^3) \land \forall k \in [|1,x_s|], s \in ATT_{V_k}^2.$$

This means that each service should be carried out by at least 3 SEDs located on at least two different machine. The MA has a constraints on the number of entities it manages. The two LA of depth 1 are constrained not to be deployed on the same machine or to offer disjoint sets of services. The last LA has a similar constraint.

```
\begin{aligned} & \text{CONS} = \{ \text{CONS}_G, \, \text{CONS}_{v_1}, \, \text{CONS}_{v_2}, \, \text{CONS}_{v_3}, \, \text{CONS}_{v_4} \}, \\ & \text{CONS}_G = \{ \text{L(S,2)}, \, \text{R(S,3)} \}, \\ & \text{CONS}_{v_1} = \{ \text{ATT}_{v_1}^2 \leq 10 \}, \\ & \text{CONS}_{v_2} = \{ \text{ATT}_{v_2}^4 \neq \text{ATT}_{v_3}^4 \vee (\text{ATT}_{v_2}^5 \cap \text{ATT}_{v_3}^5 = \emptyset) \, \}, \\ & \text{CONS}_{v_3} = \{ \text{ATT}_{v_3}^4 \neq \text{ATT}_{v_2}^4 \vee (\text{ATT}_{v_3}^5 \cap \text{ATT}_{v_2}^5 = \emptyset) \, \} \text{ and } \\ & \text{CONS}_{v_4} = \{ \text{ATT}_{v_4}^4 \neq \text{ATT}_{v_7}^3 \vee (\text{ATT}_{v_4}^5 \cap \text{ATT}_{v_7}^2 = \emptyset) \, \} \end{aligned}
```

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