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Mohamed Boucherit, Ali Soltani, Eva Monroy, Michel Rousseau, D. Deresmes, et al.. Investigation of the negative differential resistance reproducibility in AlN/GaN double-barrier resonant tunnelling diodes. Applied Physics Letters, 2011, 99, pp.182109-1-3. 10.1063/1.3659468. hal-00783409

HAL Id: hal-00783409 https://hal.science/hal-00783409

Submitted on 27 May 2022 $\,$

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Cite as: Appl. Phys. Lett. **99**, 182109 (2011); https://doi.org/10.1063/1.3659468 Submitted: 21 September 2011 • Accepted: 16 October 2011 • Published Online: 04 November 2011

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Appl. Phys. Lett. **99**, 182109 (2011); https://doi.org/10.1063/1.3659468 © 2011 American Institute of Physics.

Investigation of the negative differential resistance reproducibility in AIN/GaN double-barrier resonant tunnelling diodes

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(Received 21 September 2011; accepted 16 October 2011; published online 4 November 2011)

AlN/GaN double-barrier resonant tunnelling diodes were grown by molecular beam epitaxy on GaN/sapphire template and processed into mesa diameters from 2 μ m to 4 μ m. The current-voltage characteristics were carried out in direct current operation and under-high vacuum. A sharp negative differential resistance (NDR) was detected in the forward bias at 120 K. The NDR was observed for the mesa size of 2 μ m at 4 V with a peak-to-valley current ratio of 3.5. The measurement conditions were chosen to make NDR reproducible more than 50 times and apparent in both scan voltage directions after electrical treatment. © 2011 American Institute of Physics. [doi:10.1063/1.3659468]

Thanks to their negative differential resistance (NDR), resonant tunneling diodes (RTDs) are promising quantum devices for high-speed switching and high-frequency applications. Sub-terahertz RTDs based on GaAs/AlAs and InAs/AlSb structures were demonstrated in the last decade.^{1,2} The implementation of such devices using III-nitride (III-N) semiconductors should make it possible to obtain higher peak-to-valley current ratio (PVR) thanks to their large conduction band offset (1.75 eV).³

III-N materials present high structural defect densities in comparison with conventional III-V semiconductors, which result in leakage current.⁴ The first AlN/GaN double-barrier (DB) RTD was achieved by Kishino *et al.*⁵ However, the NDR was not reproducible in forward bias and was not observed in reverse bias. Since, many other groups have faced the similar difficulties. Indeed, the development of III-N RTDs still remains a challenging task due to the current instability and to the presence of trap-like defects in the heterostructures.⁶ These traps are thermally activated, and therefore, their contribution to the tunneling current is minimized when the temperature decreases,⁷ which should result in a larger PVR. A recent work demonstrated NDR reproducibility in a dislocation-free AlN/GaN nanowires, motivating our study at low temperature.⁸

The aim of this work is to investigate the electronic transport through AlN/GaN RTDs at 1ow temperature, with the purpose to obtain the current-voltage (I-V) characteristic when traps are frozen in their filled state. For this purpose, we chose a top-bottom approach keeping the AlN barriers below the critical thickness on GaN.⁹ The heterostructure consists of a 1 nm thick AlN double-barrier, with a 1 nm thick GaN quantum well, embedded in an n-GaN matrix. The sample is grown by plasma-assisted molecular beam epitaxy on a 10 μ m thick GaN-on-sapphire template. A 250 nm thick n-GaN layer is first deposited on the template to improve the bottom ohmic contact and reduce the access resistance. The Si-doping of the n-GaN layer is estimated to

 5×10^{19} cm⁻³. In order to avoid Si diffusion into the barriers, 1 nm i-GaN spacers are grown before and after the AlN/ GaN/AlN DB structure. On top of the active area, a 200 nm thick n-GaN cap layer is deposited (Fig. 1(a)). The dislocations density is estimated to 6×10^8 cm⁻².

The number of dislocations per mesa can be reduced by down scaling the RTDs. In this work, mesas with $2 \mu m$, $3 \mu m$, and $4 \mu m$ diameter were fabricated using microelectronic processing techniques. First, a 150 nm thick silicon nitride (Si₃N₄) mask was deposited by plasma-enhanced chemical vapor. Next, the RTDs mesas were defined by lithography and patterned in two steps of dry-etching: first the Si₃N₄ mask etching using CH₄-based reactive-ion-etching (RIE) and second the etching of the active region corresponding to mesa using Cl₂-based inductive-coupled-plasma RIE. After removal of the Si₃N₄ mask, a Ti/Al/Ni/Au multilayer was deposited by electronbeam evaporation followed by lift-off and a rapid-thermalannealing at 750 °C, to form the bottom Ohmic contact.

The electrical characterization of the RTDs was performed in ultra-high vacuum with an Omicron Nanoprobe (Fig. 1(b)). The bottom Ohmic contact is connected to the ground via a macro-pad linked to the sample holder. The voltage bias is applied by the tungsten nano-tip which is connected to the top diode. This electrode acts as a rectifier contact, so the NDR can only be observed in forward bias (Fig. 2(b)). Electrons are injected from the substrate side to the top electrode for forward bias and from the top electrode to the substrate in reverse bias.

At room temperature, the I-V characteristic of the diodes does not exhibit NDR but sometimes a resonance peak is observed followed by current instabilities. In contrast, at 120 K, a well-resolved NDR is observed for diodes with a diameter smaller than 4 μ m. When scanning from zero bias to a positive voltage, the I-V characteristics exhibits a resonance peak at the first measurement that disappears in the following measurements. The diode of 2 μ m shows a PVR of about 3.5 (Fig. 2(a)), whereas the diode of 3 μ m shows a PVR around 2.5 (Fig. 3(b)). The current hysteresis is related to charge trapping associated to structural defects, particularly treading

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FIG. 1. (Color) (a) Schematic view of the investigated RTD heterostructure; (b) SEM top view of the fabricated RTDs mesa; (c) the conduction band profile and the electrons density of states of the RTD at 0 V bias.

dislocations. In agreement with previous reports, the NDR appears only under the forward bias and disappears in the backward scan. This behavior could be related to the activation of electron traps due to the high voltage applied to the structure.⁷

The simulated electron density of states for the investigated AlN/GaN DB-RTD was calculated using the nonequilibrium Green's functions (Fig. 1(c)). The internal fields produced by spontaneous and piezoelectric polarization lead to the formation of a two dimensional electron gas and depletion region near the AlN/GaN interfaces, in the side of the cathode and anode, respectively. The expected peak voltage (V_{NDR}) should appear at \approx 1.1 V. The relatively large differences between the predicted and the measured V_{NDR} in our devices are attributed to the series resistance and to selfheating due to the power dissipation at high current density.^{10,11}

Thanks to the rectifier contact, important features are revealed after applying a negative voltage of -6 V during 1 min. Subject to this treatment, the 2 μ m RTD exhibits NDR in both scan directions at around 3.7–3.9 V (Fig. 4(b)). In the case of the 3 μ m RTD, the NDR is not apparent in the back-

ward scan. For both diameters, the measurement is not reproducible on the consecutive scans. In fact, the electric field, in the high-field regions and particularly in AlN barriers, strongly changes the trap energy level and the curvature of the triangular barriers band bending. This behavior permits to empty efficiently the traps by tunneling effect assisted by field effect (Poole-Frenkel effect).⁷

In view of the curing effect of reverse bias, we have analyzed the effect of scanning the voltage from negative to positive bias. The measurements show a remarkable improvement in the stability of the NDR. Under these conditions, the NDR is reproducible more than 50 scans for the $2 \mu m$ diodes and more than 20 times for the $3 \mu m$ diodes (Figs. 3(a) and 4(a)). These results are in agreement with previous reports, which reported a bistable behavior in RTD structures requiring negative bias to observe NDR.¹² Such reproducibility can be explained by the traps discharging located in the barriers under sufficient electric field. During consecutive measurements, the peak voltage shifts to lower voltage followed by current instabilities in the backward scan, as observed by other groups.^{13,14} This behavior can be qualitatively explained by the accumulation of charge in



FIG. 2. (Color) (a) The current-voltage characteristics of the $2 \mu m$ diameter RTD measured at 120 K without electrical treatment, the voltage is scanned from 0 V to a positive voltage; (b) the current-voltage curve measurement of the rectifier contact on the top of the RTD at 120 K.



FIG. 3. (Color) The current-voltage characteristics of the $3 \mu m$ diameter RTD measured at 120K without electrical treatment; (a) the voltage is scanned from -19 V to a positive voltage; inset (b): the voltage is scanned from 0 V to a positive voltage.



FIG. 4. (Color) The current-voltage characteristics at 120 K for the $2 \mu m$ RTD without electrical treatment; (a) the voltage is scanned from -13 V to a positive voltage; (b) the voltage is scanned from 0 V to a positive voltage; inset (T) with electrical treatment.

traps located in the near-barrier region, which masks the quantum level and hence shifts V_{NDR} . The NDR slowly decays after repeated measurements for the 2 μ m diode. This behavior is more pronounced for the 3 μ m diode, where the peak current decreases while the valley current increases. This feature suggests that trapped charges are released only partially under application of reverse bias.

After electrical treatment, the diode of $2 \mu m$ exhibits a NDR in both scan directions at 4.2 V-2.2 V, as previously (inset T of Fig. 4). In the backward scan, the peak voltage shifts to lower voltage and the peak current drastically decays. In the following measurements, the resonance peak totally disappears. This behavior is ascribed to the electric field distribution in the structure and to the kinetic of (de)trapping under high-bias voltage.

In summary, we have demonstrated that the NDR can appear in both scan directions after electrical treatment. The NDR reproducibility can be drastically improved when scanning the voltage from a sufficient negative starting point bias. We have also shown that the rectifier contact on the top electrode is responsible of detrapped charges by Poole-Frenkel effect in DB-RTDs. Furthermore, the I-V characteristics are very sensitive to the mesa size. These promising results constitute a motivation in the future for the fabrication of nano-diodes.

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