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► **To cite this version:**

Isabelle Bord-Majek, Philippe Kertesz, Julie Mazeau, Daniel Caban-Chastas, Bruno Levrier, et al.. OPERATIONAL PERFORMANCES DEMONSTRATION OF POLYMER-CERAMIC EMBEDDED CAPACITORS FOR MMIC APPLICATIONS. IEEE Transactions on Components, Packaging and Manufacturing Technology, 2011, 1 (9), pp.1473-1479. hal-00641070

HAL Id: hal-00641070

<https://hal.science/hal-00641070v1>

Submitted on 14 Nov 2011

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OPERATIONAL PERFORMANCES DEMONSTRATION OF POLYMER-CERAMIC EMBEDDED CAPACITORS FOR MMIC APPLICATIONS

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Abstract

Embedded passives are becoming increasingly important for the manufacture of highly integrated electronic boards and packages. The need for embedded passives emerges from the growing consumer demand for product miniaturization thus requiring smaller components and space efficient packaging. This can be realized by replacing discrete components that demands a higher volume than embedded passives. Embedded passives have already been investigated in the last few years. However nowadays used dielectric materials have to be optimized in order to improve the performances of the passive components in terms of dielectric behaviour and reliability. The most promising candidates for dielectric materials applied for embedded passives are polymer-ceramic nanocomposites. In this paper the robustness of embedded capacitors using such commercial dielectrics regarding the environmental conditions (temperature and humidity) is studied. Microwave measurements performed on an embedded structure design for Low Noise Amplifier (LNA) MMIC decoupling allow to demonstrate clearly the capacity of this new technology.

Keywords

Embedded capacitors, polymer-ceramic dielectrics, robustness, operational microwave performance

1. INTRODUCTION

Electronic board and package miniaturization imply the size reduction of bulky and numerous passive components. It is reasonable to expect passive component densities of 20–30 passives/cm² [1]. Although surface mounted devices (SMD) components can be provided as very small passives, their reflow process onto the printed circuit board (PCB) is very tricky limiting thus their operational reliability [2].

Many studies have been performed on the recurring problem of the reliability of ceramic capacitors in all shapes and sizes. It is now widely known that the increase of the stress is caused by the mismatch of the thermal expansion coefficient and the thermal conductivity between the ceramic dielectric, the terminations, the solder, the copper land and the substrate. During cooling, the difference of dissipation due to heat flow between these elements creates important contraction or compression constraints on the different materials. Any concentration of stress can induce cracks (fracture of the leads, shear and fracture of the solder joint, lifting of the copper lands) or cause failure of the assembly during operational use of the components [3].

In order to overcome the risk of early failures related to ceramic capacitors, an interesting approach consists in their replacement with passive layers embedded in a PCB substrate (as FR4 or KAPTON FLEX) during the PCB manufacturing process. The integration of passive layers into the PCB might lead to both increase the integration density and improve the reliability. Moreover the embedded capacitor can be located close to the active component reducing the associated parasitic inductances that are linked to its interconnects.

To be used as dielectric layer for embedded capacitors, the material must:

- present the following dielectric properties: high dielectric constant and low dielectric losses over a broad frequency range,
- be deposited as thin layer (roughly around 10 μ m),
- be in accordance with PCB temperature process (~ 200°C, high pressure processes),
- provide a thermal (high glass transition temperature T_g) and mechanical stability, and
- be low-cost.

In this way polymer-ceramic composites are promising materials for embedded capacitors because they combine the high dielectric permittivity of ceramic powders and the processability and flexibility of polymers

(in particular low temperature processing) [4]. In this paper we focus only on such commercial dielectric materials.

2. EMBEDDED CAPACITORS BASED ON COMMERCIAL POLYMER-CERAMIC DIELECTRICS

Embedded capacitors are made of a dielectric material layer deposited between two metal layers (usually copper foils) that are buried in the PCB. Inorganic dielectric materials commonly used for discrete capacitors are partly replaced by organic polymers. Polymer-ceramic composites are expected to be the major candidate as dielectric layer for embedded capacitors because such composite materials present both characteristics of the processibility of polymers and a high dielectric constant of inorganic materials as barium titanate [5, 6].

The polymer (usually epoxy or polyimide) is filled with high dielectric constant particles (usually BaTiO_3 according to EDX analysis presented Figure 1) modifying the organic film. Such dielectric composites commercially available as pre-fab laminates are studied later [7, 8, 9].

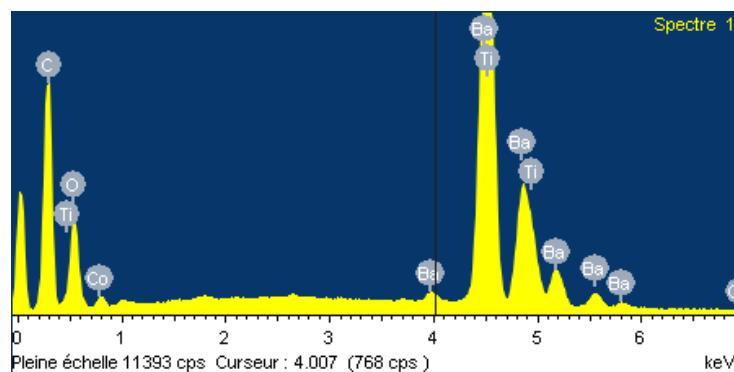


Figure 1: EDX analysis of a commercial dielectric layer based on BaTiO_3 particles (BC8T-FaradFlex[®] material from Oak-Mitsui)

Embedded capacitors based on such dielectrics are partly made with plastic materials subjected to both moisture and temperature. In order to perform a robustness study, the impact of temperature and humidity on embedded capacitors based on commercial pre-fab laminates is studied.

3. ENVIRONMENTAL CONDITIONS INFLUENCE

The pre-fab laminates were buried in a conventional PCB by a PCB manufacturer according to the dielectric datasheet.

3.1 TEMPERATURE INFLUENCE ON CAPACITANCE

The temperature is supposed to be the more critical parameter for embedded capacitors based on a polymer-ceramic material. Capacitance measurements versus frequency were carried out using the impedance meter HP 4192A and the test fixture HP 16034E in the frequency range [1kHz, 1000kHz].

Figure 2 shows the capacitance variation versus frequency in the temperature range [25°C, 150°C] for an Oak-Mitsui embedded capacitor based on the BC16T-FaradFlex® material. The electrode area of the studied capacitor is 6.0 x 6.0 mm² and the dielectric thickness is 16µm.

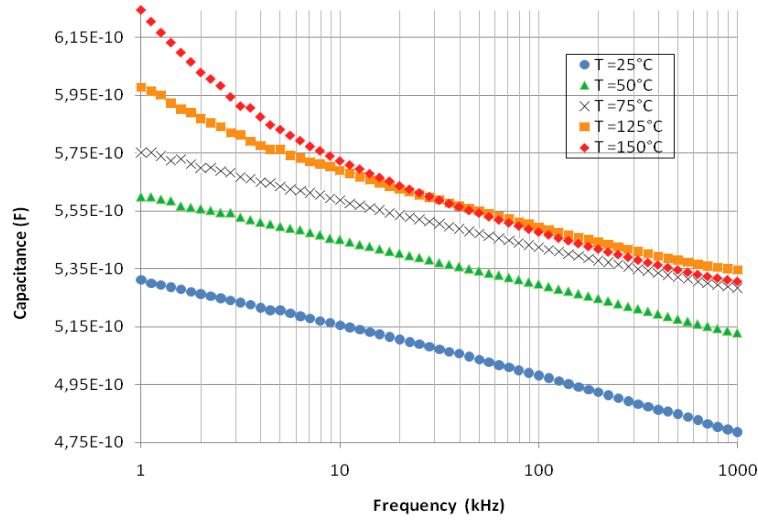


Figure 2: Capacitance variation versus frequency in the temperature range [25°C, 150°C] for an Oak-Mitsui embedded capacitor based on the BC16T-FaradFlex® material

Whatever the temperature, the capacitance decreases as increases the frequency due to the drop of the relative permittivity $\epsilon_r(\omega)$ with frequency. The thermal expansion coefficient (CTE) of such commercial dielectrics is about $20 \cdot 10^{-6} \text{ C}^{-1}$. So the dimensional expansion is about $0.04\mu\text{m}$ in the studied temperature range [25°C, 150°C] that is negligible compared to the material thickness ($16\mu\text{m}$).

The capacitance $C(\omega)$ is linked to $\epsilon_r(\omega)$ by the following equation:

$$C(\omega) = \frac{\epsilon_r(\omega) \epsilon_0 S}{d} \quad (1)$$

where ω is the excitation frequency, ϵ_0 the vacuum permittivity, S the electrode area and d the dielectric thickness.

Moreover the capacitance increases with temperature due to the increase of the relative permittivity $\epsilon_r(\omega)$ with temperature. A change of the behaviour of the capacitance is observed after 125°C due to glass transition of the dielectric.

3.2 THERMAL STORAGE

In order to perform a robustness study, similar embedded capacitors as previously (made of the same nature of dielectric) were placed in a climatic chamber for hours. The electrode area of the studied capacitors is 1.0 x 1.0 mm² and the dielectric thickness is 8µm.

Figure 3 shows the capacitance variation versus frequency after the first exposure hours to (a) 85°C and (b) 150°C for an Oak-Mitsui embedded capacitor based on the BC8T-FaradFlex[®] material.

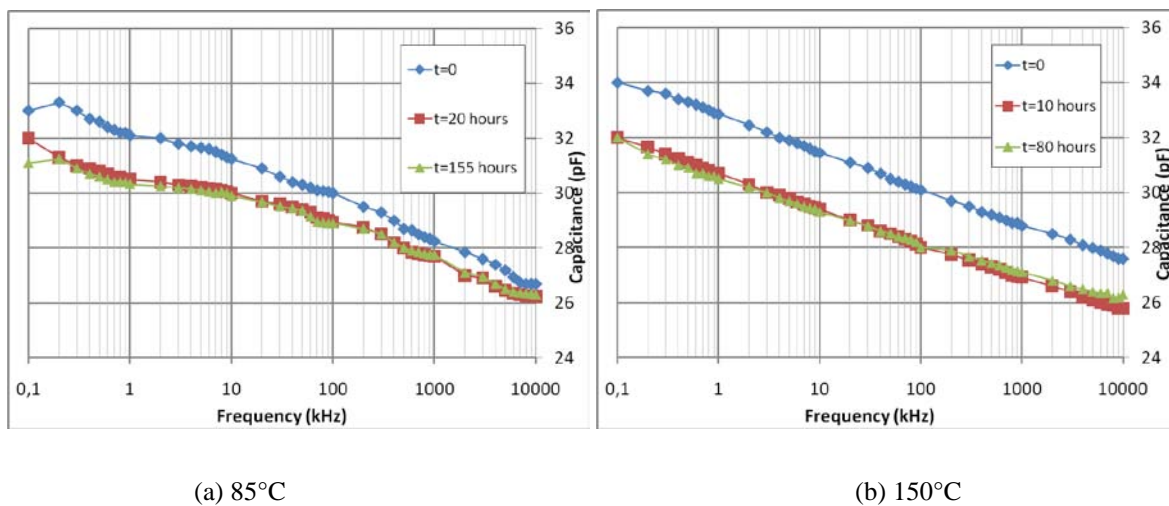


Figure 3: Capacitance variation versus frequency after various exposure times to (a) 85°C and (b) 150°C for an Oak-Mitsui embedded capacitor based on the BC8T-FaradFlex[®] material

Whatever the temperature, the capacitance decreases with exposure time. At ambient temperature, the dielectric material contains a little part of water. After exposure to 85°C / 150°C, water is evaporated. Water permittivity is higher than dielectric one whatever the frequency. So the material permittivity and then the capacitance decrease after exposure to 85°C / 150°C. The behaviour is similar at 85°C and 150°C. However the capacitance decrease is accelerated at 150°C: the same variation is observed at 150°C after 80h and at 85°C after 155h.

Figure 4 presents the capacitance variation versus exposure time at 150°C at different frequencies after drying for the Oak-Mitsui embedded capacitor based on the BC8T-FaradFlex[®] material.

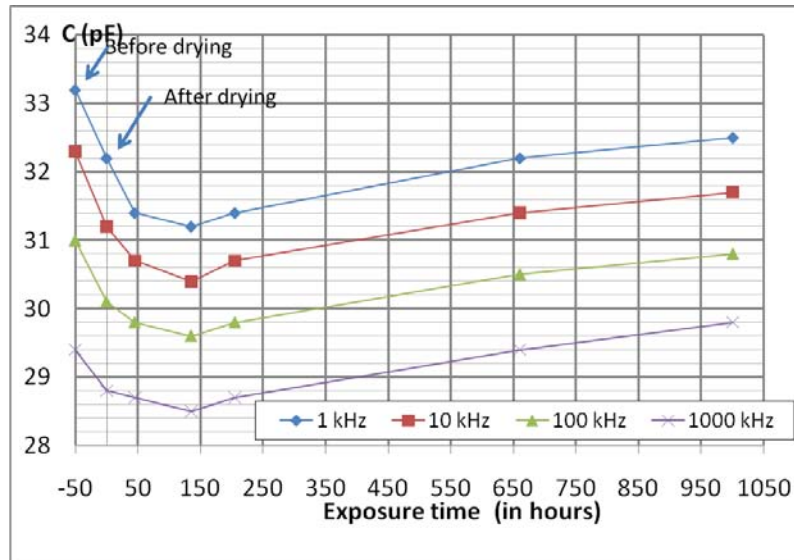


Figure 4: Capacitance variation versus exposure time at 150°C at different frequencies for an Oak-Mitsui embedded capacitor based on the BC8T-FaradFlex[®] material

After drying, the capacitance decreases due to water evaporation. A maximal capacitance variation of only 6% is observed after 135h.

A gradual slope change is observed with exposure time (especially at high frequency). After 135h the capacitance increases. This phenomenon may be correlated with a modification of the dielectric material during thermal ageing.

3.3. HUMIDITY STORAGE

Similar embedded capacitors (electrode area 1.0x1.0mm² and dielectric thickness 8µm) were stored in a climatic chamber with humidity control for hours. Capacitance measurements versus frequency were carried out after various exposure times to 85°C / 85% RH (Relative Humidity). Figure 5 presents the capacitance variation versus exposure time to 85°C / 85% RH at different frequencies for the Oak-Mitsui embedded capacitor based on the BC8T-FaradFlex[®] material.

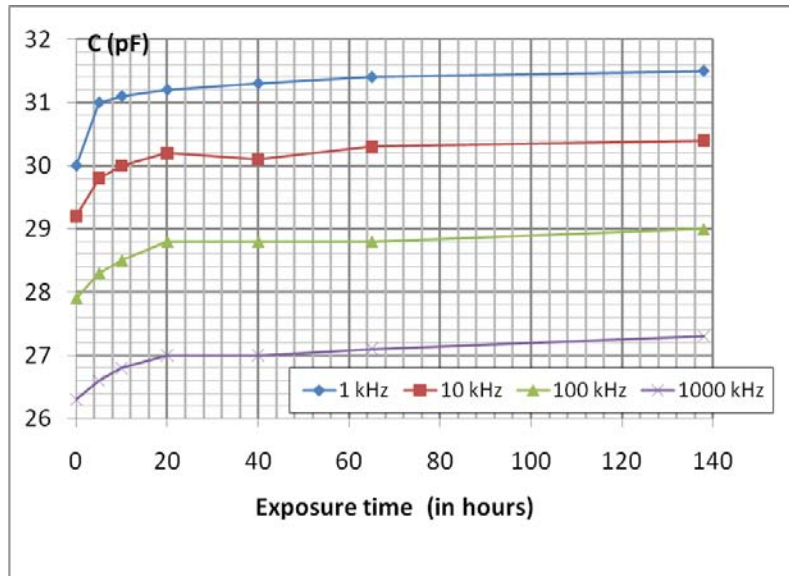


Figure 5: Capacitance variation versus exposure time to 85°C / 85% RH at different frequencies for an Oak-Mitsui embedded capacitor based on the BC8T-FaradFlex® material

The capacitance increases with exposure time because of water absorption by the material (water permittivity higher than polymer/ceramic permittivity). After 138 hours, no significant variation is observed. The maximal capacitance variation is about 5%.

4. COMPARISON WITH DISCRETE CAPACITORS

4.1 ENVIRONMENTAL CONDITIONS INFLUENCE

The maximum exposure time at high temperature, number of reflow solder cycles or re-work cycles are not limited by ceramic capacitors but by the laminate assembly and the plastic moulded components quality. Generally defects induced on ceramic capacitors are due to manufacturing process and reflow problems. This kind of defects can generate failures during components operation. For example, in a previous study, we tested two lots of 25X7R 0805 ceramic capacitors 47pF using impedance spectroscopy. After thermal ageing (150°C during 1000h), a significant drift of the capacitance was systematically observed (nearly 20%) and some components were found defective because of cracks [10].

In the case of embedded capacitors based on polymer/ceramic dielectric, a 6% maximal capacitance variation is observed after 135h. After 1000h, the capacitance variation is only 3%, clearly lower than ceramic capacitors one (15%).

Humidity is not a recognized factor of failure of ceramic capacitors. Humidity may be dangerous for the metal terminations that make up the electrodes but not for the dielectric material. When both precious metal electrode and base metal electrode (BME) capacitors were subjected to autoclave (121 °C/ 100% RH) testing after 500h, it was found that the precious metal capacitors aged (average capacitance degraded less than 3% from their starting values), but the BME capacitors degraded to below the -30% specification limit [11]. This problem is reduced in embedded capacitors because the electrodes are protected by the PCB. Our study showed a maximal capacitance variation with humidity of about 5%.

4.2 INTERNAL STRESS

Embedded passives are supposed to be more reliable than discrete passives as they do not require solder joints. The shear stress (due to CTE mismatch) is the main cause of delaminations and cracks in the case of discrete capacitors. For the reliability point of view, it is essential to know the stress at the different interfaces in embedded capacitors.

The shear stress values inside the PCB were estimated using the analytical program CRITIDEL from EURELNET [12] at the interfaces:

- PCB-Copper
- Copper-Dielectric (i.e. Figure 6)

The values of mechanical properties necessary to the analytical calculation of the stress are summarized in the Table 1.

Material	Young Modulus (GPa)	CTE (ppm. C ⁻¹)
PCB	15	14
Copper	100	17
Dielectric	1.3	32

Table 1: Mechanical properties of materials [12]

Figure 6 presents the calculated shear stress values versus the thickness of the dielectric layer.

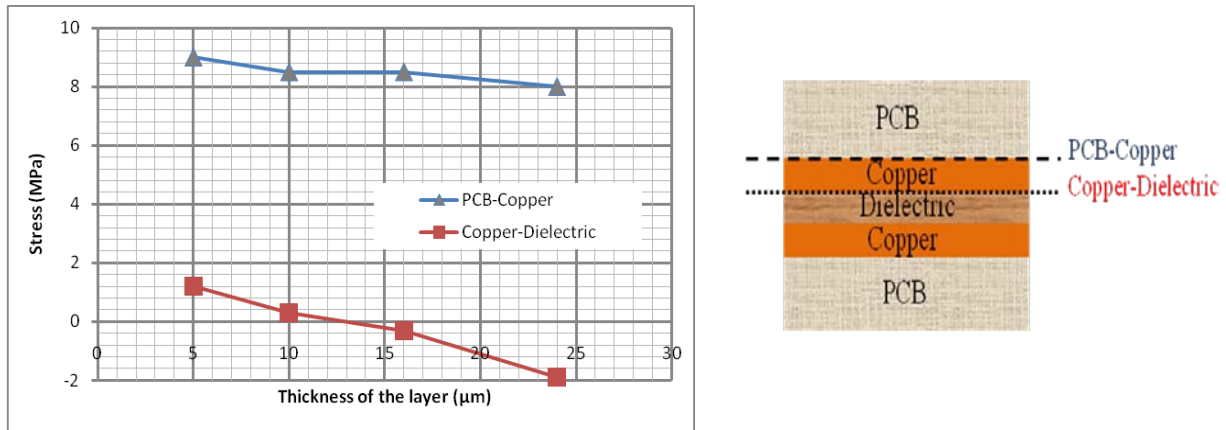


Figure 6: Shear stress values between PCB-Copper and Copper-Dielectric versus dielectric thickness calculated using CRITIDEL

The embedded capacitors (PCB-Copper interface) undergo a stress domain from 8 MPa to 10 MPa that is less critical than the one of capacitors reflowed on a PCB (200 MPa to 300 MPa [13]).

The behaviour and the robustness of isolated embedded capacitors are now checked. But what about the operational microwave performance of such components ?

5. OPERATIONAL MICROWAVE PERFORMANCE

5.1 EMBEDDED CAPACITORS FUNCTIONAL TEST VEHICLE

In the framework of HIFAP program (PIDEA+), we designed an embedded structure for « real » MMIC decoupling measurements: the Low Noise Amplifier (LNA) UMS (United Monolithic Semiconductors) CHA3023 could be easily implemented with such embedded capacitors and it could be then easy to clearly demonstrate the potential of this new technology. In this way, two equivalent versions of the LNA were manufactured:

- a version with an embedded capacitor as decoupling capacitor (using an organic substrate)
- a version with a discrete ceramic capacitor as decoupling capacitor.

5.2 EMBEDDED DESIGNS EVALUATION

S parameters measurements were carried out using the test set network analyzer Agilent 85110A (connected to HF probes) in the frequency range [2, 18GHz] for both previous LNA versions. The following parameters were checked:

- S11 input return loss
- S22 output return loss
- S21 output insertion loss (LNA's gain).

Figure 7 presents the LNA Gain (S21 output insertion loss) using (a) ceramic discrete capacitors and (b) embedded capacitors as decoupling capacitor.

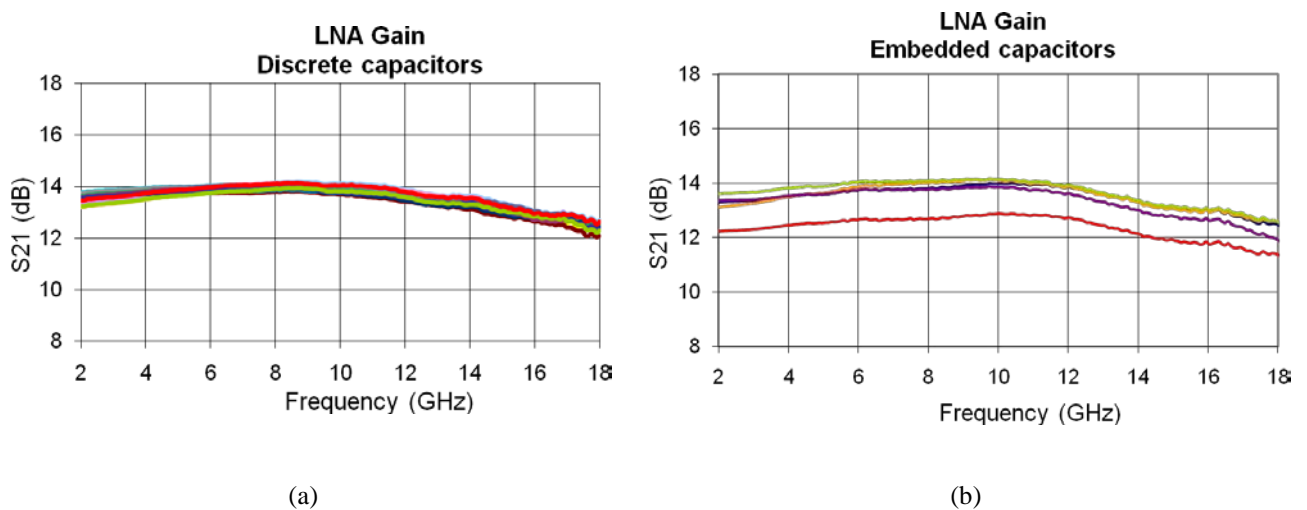


Figure 7: LNA Gain measurements using discrete decoupling capacitors and using embedded decoupling capacitors

The LNA Gain is very similar in discrete and embedded capacitors configuration (around 13dB).

Figure 8 presents the LNA return losses (S11 and S22) using (a) ceramic discrete capacitors and (b) embedded capacitors.

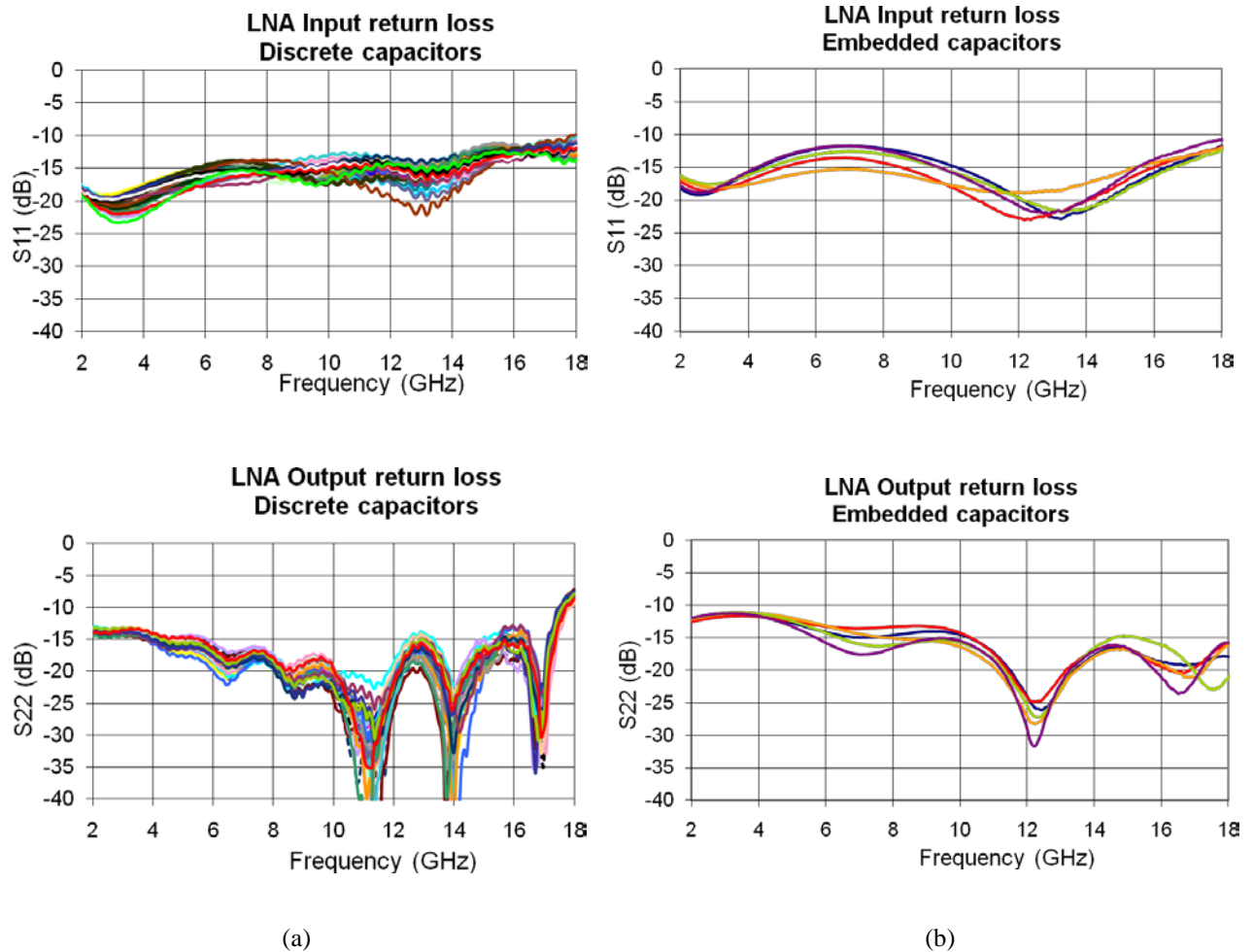


Figure 8: LNA Return losses measurements using (a) discrete decoupling capacitors and (b) embedded decoupling capacitors

The LNA return losses are also very similar in discrete and embedded capacitors configuration (around -15dB). The oscillations are different in both cases (embedded en discrete capacitors configurations) because of the difference in impedance due to the design. However there is no impact on the LNA Gain.

The previous results show that the concept is validated at room temperature.

5.3 Thermal stability

No difference in LNA behaviour was observed for both vehicles (discrete and embedded capacitors configuration) at room temperature.

The previous circuits were then considered for measurement under high/low temperature in order to check decoupling thermal stability.

Measurement was performed using the following method (detailed in Figure 9) in the temperature range $[-40, +80^{\circ}\text{C}]$.

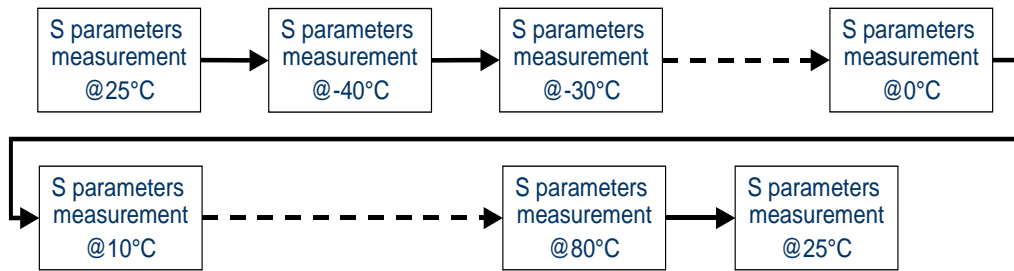


Figure 9: Thermal stability measurement method

This way we could assess potential S parameters values hysteresis that would indicate that the embedded capacitors are not thermally stable.

Figure 10 presents the LNA return losses using embedded capacitors in the temperature range $[-40, +80^{\circ}\text{C}]$.

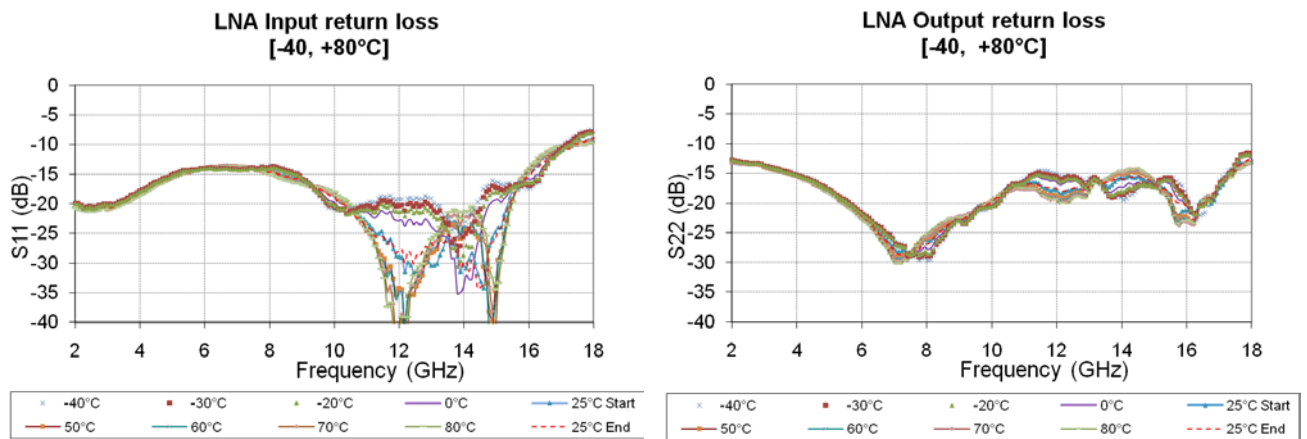


Figure 10: LNA (a) input Return loss and (b) output Return loss measurements using embedded decoupling capacitors in the temperature range $[-40, +80^{\circ}\text{C}]$

No significant modification of the LNA Return Losses is noticed in respect to temperature.

Figure 11 presents the LNA Gain using embedded capacitors in the temperature range $[-40, +80^{\circ}\text{C}]$.

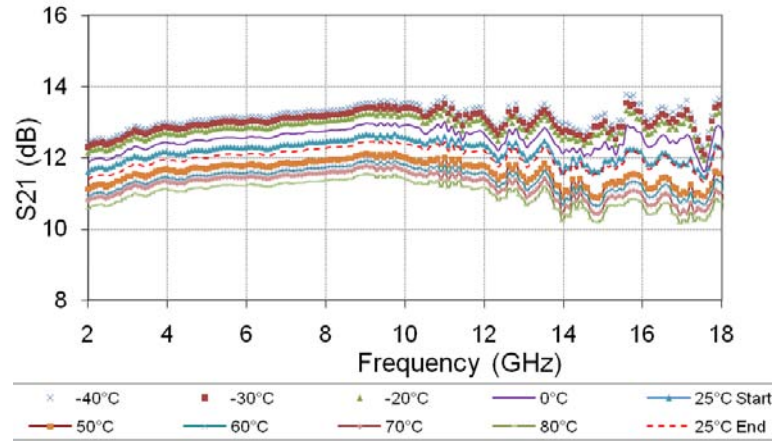


Figure 11: LNA Gain versus frequency using embedded decoupling capacitors in the temperature range $[-40, +80^{\circ}\text{C}]$

A change is noticed in the LNA Gain with temperature: the S21 parameter decreases with temperature.

Figure 12 presents the LNA Gain variation compared to the reference gain at 25°C with embedded capacitors in the temperature range $[-40, +80^{\circ}\text{C}]$.

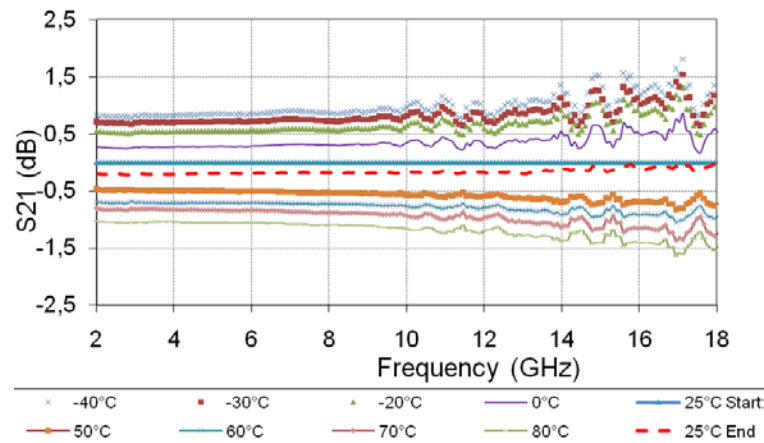


Figure 12: LNA Gain variation compared to the reference gain at 25°C with embedded decoupling capacitors in the temperature range $[-40, +80^{\circ}\text{C}]$

According to the LNA (UMS, CHA3023) datasheet, the S21 (LNA gain) temperature sensitivity is actually modified according the following rule: $-0.03 \text{ dB}/^{\circ}\text{C}$.

According to Figure 12, measurements are fully compliant with datasheet:

- $(S21)_{80^{\circ}\text{C}} = (S21)_{25^{\circ}\text{C}} - 1.65 \text{ dB}$
- $(S21)_{-40^{\circ}\text{C}} = (S21)_{25^{\circ}\text{C}} + 1.95 \text{ dB}$

So the S21 variation is related to the LNA intrinsic thermal sensitivity and not to the embedded capacitors.

Moreover the S21 is stable after thermal cycling which implies that temperature effect is reversible. The assessed LNA operates properly within the frequency band and with respect to temperature.

The stability of embedded capacitors is now achieved representing a major breakthrough for microwave packaging.

6. CONCLUSIONS

Capacitive laminates commercially available were studied. Theoretical performance of embedded capacitors using organic polymer dielectric was first assessed:

- the capacitance decreases after thermal exposure ($\leq 6\%$) because of water evaporation.
- the capacitance increases after humidity exposure ($\leq 5\%$) due to water absorption.

Operational performance of embedded capacitors using organic polymer dielectric was then checked and compared to discrete capacitors one. Finally it was demonstrated that the commercial laminates could be used efficiently to replace discrete decoupling capacitors for microwave packaging and with no reliability issues (with respect to moisture and temperature) to be foreseen.

With the added benefits of elimination of solder joints, inventory, assembly and rework with respect to their surface mounted counterparts, embedded ceramic capacitors provide a very convincing and compelling option to meet the ever increasing need for miniaturization and improve performance in modern electronic designs.

Although embedded capacitors are promising, the range of available capacitance values is not sufficient for the moment (close to 0.5 to 2 nF/cm²). In order to reduce the size of the basic capacitor and generate a larger range of capacitance values (20 to 200 nF/cm²), two ways are currently explored:

- the synthesis of materials with higher values of dielectric permittivity (high-k materials) over a broad frequency range with respect to PCB manufacturing process requirements,
- and the reduction of the dielectric layer ($< 8\mu\text{m}$) which induces a good processability of the material (printable formulation).

In this way, polymer-ceramic nanomaterials used to make embedded capacitors must be improved.

ACKNOWLEDGEMENTS

The authors gratefully acknowledge Jean-Yves Gondouin from Elvia-Lithos as PCB manufacturer and the different manufacturers (in particular Oak Mitsui) which provide us materials for the tests within research and development programs as HIFAP (PIDEA+), PROCURE FP5 and AMELIE FP6.

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