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Self-Starting DC:DC Boost Converter for Low-Power and Low-Voltage Microbial Electric Generators

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Abstract—This paper describes and evaluates an original boost converter able to harvest energy from low-power and low-voltage power sources. Design and sizing are made according to specifications issued from the stringent characteristics of microbial electric generators such as microbial fuel cells and microbial desalination cells. The harvested power is 10mW under input voltage $V_{in}=0.3V$ (33mA input current). The design of the converter is adapted from a classical boost topology. It includes a self-oscillating circuit for autonomous operation, and a simple analog maximum power point regulation. Measurements of the conceived discrete realization enable evaluation of the circuit. Best global efficiency of 74% is achieved under realistic harvesting conditions.

I. INTRODUCTION

Microbial electric generators (MEGs) are bio-electrochemical systems that use the metabolisms of bacteria to produce electrical energy from organic matter. This energy can be advantageously used to drive various processes including desalination.

Microbial fuel cells (MFCs) are the most common type of MEG. Researches on this subject started in 1911 when M.C. Potter observed electricity production by bacteria *E. coli* [1]. These results gained increased interest later when Cohen demonstrated a voltage greater than 35V from serially connected MFCs. Research amplified 10 years ago when two applications sprouted. The first is to use MFCs as biological sensors [2]. The second is to use MFCs as energy source to produce electricity while consuming organic matter and eventually decontaminating waste-water [3-4]

MFCs are composed of two electrodes (Fig. 1). The anode is in presence of electrogenic bacteria and organic matter. The cathode is in presence of oxygen. When bacteria consume organic matter, complex molecules are broken and electrons and protons are liberated. Electrons migrate to the anode in different ways (direct transfer, electron shuttle, or nanowire) [5]. Protons migrate to the cathode through the electrolyte. At the cathode, electrons, protons and oxygen react to form water.

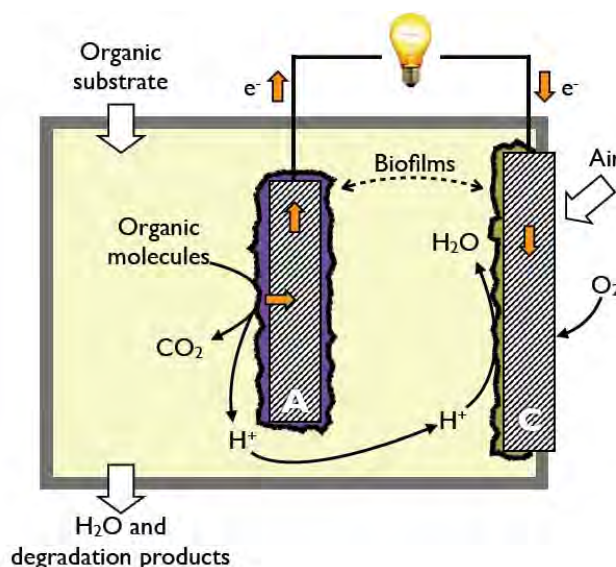


Figure 1. Schematic of a single-chamber MFC

Microbial desalination cells (MDCs) are a combination of MFCs and electro dialysis technologies [6]. The research on this area is at a very early stage. The addition of two membranes between the anode and the cathode of a MFC creates a middle chamber for water desalination between the membranes (Fig. 2). An anion exchange membrane (AEM) is placed on the anode side, and a cation exchange membrane (CEM) is positioned on the cathode side. When the oxidation on the anode creates protons, anions from the middle chamber are transferred into the anode chamber. Identically, cations from the middle chamber are transferred to the cathode chamber when the reduction consumes electrons. These transfers of ions out of the middle chamber enable desalination of the inside water. In addition to desalination, MDCs also enable waste-water treatment and electricity generation just like MFCs.

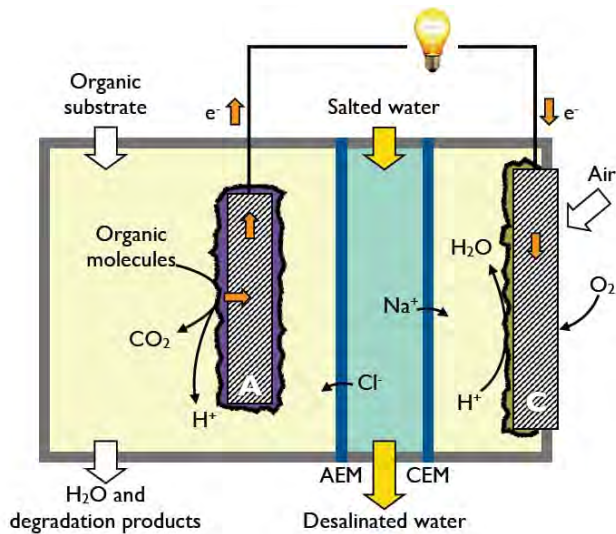


Figure 2. Schematic of a MDC

A similarity between different kinds of MEGs is that they provide a very small amount of power (presently about 1mW for a 1-liter single-chamber MFC) under low-voltage (0.3V). Internal presence and nature of substrate and bacteria is inconstant and uncontrollable and has a strong influence on cells electrical characteristics [7]. In the case of MDCs, the salt concentration of the middle membrane has a high influence on the cell internal resistance. These inconstancies cause inevitable dispersions of electrical characteristics (maximum power is obtained under different current conditions) and one consequence is that it is inefficient to associate many cells in series [8]. Therefore, we must rely on low-voltage power conversion structures to valorize the electrical energy produced from MEGs.

Low-power and low-voltage converters require specific design attention. Kimball, in [9], emphasizes start-up and efficiency issues. Most silicon components do not switch at gate voltages below 0.5V. Once in operation, the output voltage of the step-up converter can be sufficiently high to

supply the circuit control. If the output voltage is initially low, specific start-up topologies need to be added like in DC:DC converters compared Table I.

A first option is to realize the start-up circuit independently from the main DC:DC converter. The advantage is to allow the use of a classical high-efficiency main DC:DC converter. In [10], [11] and [12], the boost topology was respectively implemented with a charge-pump, a mechanical, and a transformer-based start-up.

A second option is to merge the start-up and the main circuit in order to decrease the number of components and thus the cost and the reliability. Respectively boost and flyback architectures were presented in [13] and [14]. These two structures are close to the converter presented in this paper.

A third option is to design new architectures especially built for low-voltage and low-power. The architecture presented in [15] is a transformer-based oscillating structure connected with a voltage-doubler circuit. It transfers energy both directly and indirectly. This topology has gained interest recently in energy harvesting applications [16]-[17].

The present paper analyzes a step-up converter topology customized for autonomous operation and sized for harvesting energy from MEGs. It is a modified boost converter which includes self-starting and self-oscillating command. These are based on the association of a transformer which secondary windings control a switch connected in series with the primary windings. This association ensures a positive feedback and sustained oscillations. In order to initiate operation, a normally-ON JFET is used as a switch.

The present paper intends to unveil this original harvesting converter. First, a description of MEGs is given. Specifications for the converters are deduced. Then, the circuit is extensively described and operations principles are detailed. Next, simulations and measurements results are given and discussed.

TABLE I. HARVESTING DC:DC CONVERTERS COMPARISON

Circuit	Description				
	Start-up voltage	Power range	Vout	MPPT	Peak efficiency
Linear technologies LTC310 (1:20) [16]	100mV	100μW to 100mW	2.35V to 5V	no	0.6
Enocean ECT310 [18]	20mV	100μW to 100mW	3V to 5V	no	0.3
Seiko S-882Z [19]	300mV	? to 150μW	1.8V to 2.4V	no	0.2
Markus [14]	70mV	200μ to 16mW	2V to 5V	no	0.7
Qiu [20]	500mV	5μW to 10mW	0V to 5V	yes	0.7
Ramadass [11]	35mV	10μW to ?	1.8V	yes	0.58

II. MICROBIAL ELECTRIC GENERATORS SPECIFICATIONS

A. Construction of a single-chamber microbial fuel cell

Because MEGs are derived from MFCs and because their electrical specifications are also very similar to the ones of MFCs, the following study is based on MFCs only. One batch reactor was designed to harvest energy from wastewater. In this purpose, a single-chamber MFC structure was preferred (Fig. 3). The main advantages compared with the dual counterpart are the higher power density, the decreased risks of fouling, and the autonomous aeration of the cathode.



Figure 3. Photograph of the constructed MFC

The main originality of this structure relies on the cathode (usually named air-cathode) which is directly in contact with air on the external side of the reactor. This cathode (Fig. 4) is composed of a frame in carbon cloth on which 4 PTFE layers are coated on the external surface. These layers ensure the water-proofing while enabling oxygen diffusion. On the internal side, a nafion membrane is coated in order to limit the diffusion of oxygen to the anode while permitting the ions to access the cathode. This membrane is supplemented with platinum ($0.1\text{g}\cdot\text{cm}^{-2}$) to serve as a catalyst. This structure was conceived manually, using directives from Penn State University [21].

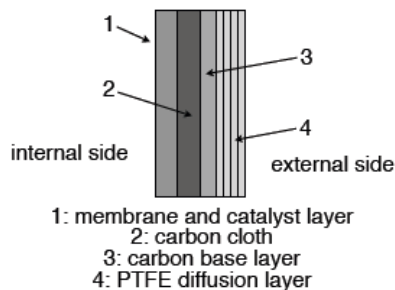


Figure 4. Air-cathode side view

The anode is a graphite fiber brush with titanium base wire to ensure a large specific area (Gordon Brush Inc.). The reactor was conceived standard evacuation pipes in PVC (Nicoll) in order to lower the cost and enable easy reproducibility of the design.

B. Electrical characteristics of a single microbial fuel cell

Polarization and power density curves of MEGs offer precious information to design the harvesting power electronic module. A polarization curve (Fig. 5) was acquired for the MFC described above. The MFC was previously fed with 0,1g of acetate and the curve was acquired under ambient temperature $\approx 25^\circ\text{C}$. The load resistance was varied from open-circuit to short circuit using a resistor box. Voltages were measured for each resistance value after a stabilization time of 5min.

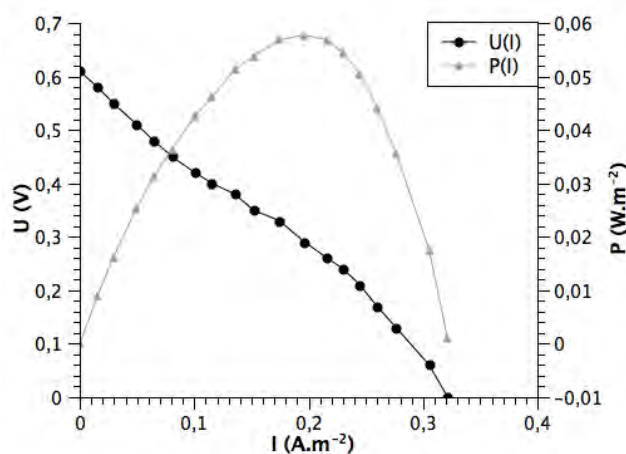


Figure 5. Polarization and power density curves of the constructed MFC

The normalized curves indicates that open-circuit voltage is $\approx 0.6\text{V}$, short-circuit current $\approx 0.32\text{A}\cdot\text{m}^{-2}$ and maximum power $\approx 58\text{mW}\cdot\text{m}^{-2}$ obtained for a maximum power point (MPP) voltage $\approx 0.29\text{V}$. For the MFC we consider, the maximum power was $540\mu\text{W}$ obtained for a current of 1.84mA , at a voltage of 0.29V and with a 160Ω external load resistor.

It is now common to find in literature power densities up to few $\text{W}\cdot\text{m}^{-2}$ [22]. These results are usually obtained for small scale reactors which use platinum in higher concentrations. Those MFCs are difficultly scalable and their price is inherently high.

C. Basic microbial fuel cell model

The output voltage of fuel cells is usually described as an open-circuit voltage to which we subtract 3 different kinds of current-dependant voltage losses: activation losses, resistive losses, and diffusion losses (also called concentration losses). These losses are dependant on temperature and on fuel concentration.

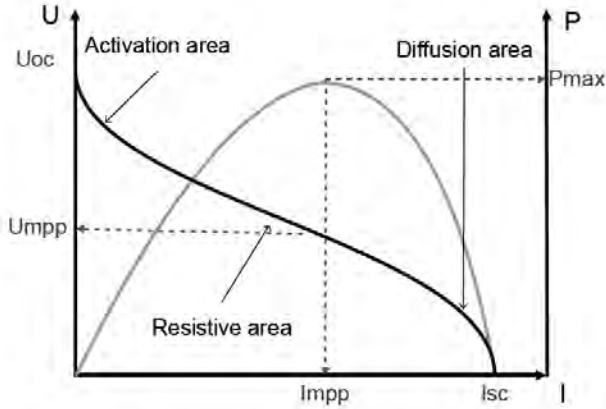


Figure 6. Theoretical polarization and power density curves for a typical fuel cell

A realistic model would consider these 3 voltage losses and compute them for determined experimental conditions of temperature and fuel concentrations. In this paper, the model was largely approximated and only considers equivalent resistive losses. MEGs are therefore assumed to all correspond to the electrical circuit (Fig. 7), with an open circuit voltage of 0.6V and an internal serial resistance $\approx 160\Omega$.

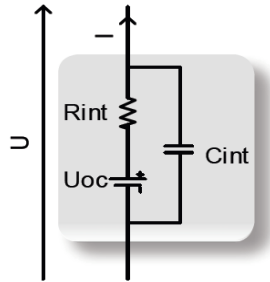


Figure 7. Simplified model of a MFC

D. Association of microbial fuel cells

MFCs were tested under different fuel conditions, and it was found that the internal equivalent resistance (R_{int}) increases and open-circuit voltage is almost constant when fuel concentration decreases. In this case, the maximum power point (MPP) voltage is identical for two reactors while the MPP current is different (Fig. 8). For this reason, parallel association is preferred. The issue with parallel association is that the voltages are low ($\approx 0.3V$). The current can be increased by multiplying the number of connected cells. Since a single laboratory MFC like described above outputs 1.84mA, we chose to design a converter for a current of 33mA, corresponding to 10mW (under 0.3V) and to 18 lab cells in parallel.

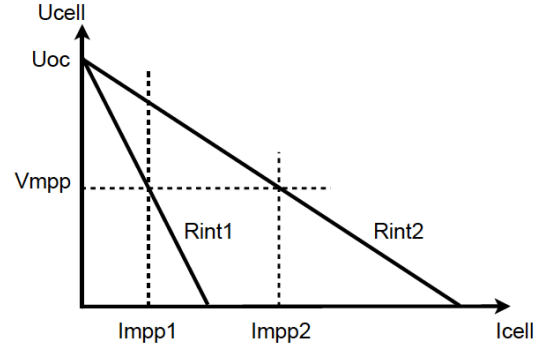


Figure 8. Polarization curves for parallel associations

E. DC:DC converter specifications

The converter ideally includes maximum power point tracking (MPPT) function to harvest the maximum available power from the source regardless of its internal resistance. A simple way to ensure this function is to regulate the input voltage to a value V_{mpp} like introduced above. The output voltage should not be regulated precisely, but must be above 1V in order to enable a second step-up stage realized in conventional CMOS technology.

TABLE II. SPECIFICATIONS OF THE DC:DC CONVERTER

	Specifications				
	Name	Unit	Min	Typical	Max
Input open-circuit voltage	V_{in0}	V	0.5	0.6	0.7
Input source serial resistance	R_{int}	Ω	3	9	45
Input regulated voltage	V_{in}	V	0.2	0.3	0.4
Global efficiency	η	%	70		
Output voltage	V_{out}	V	1V		

III. CIRCUIT PRESENTATION

A. Architecture

The converter presented Fig. 9 is based on a conventional boost converter like proposed in [14]. The main circuit inductor L_1 is coupled to L_2 to feed start-up, self oscillating, and regulation circuits.

B. Sizing

The circuit was designed using Orcad software with SPICE models for each part at the exception of the transformer which model is unavailable and which was replaced by an ideal linear transformer ($L_1=326.7\mu H$, $L_2=8167\mu H$, coupling $K=0.999$) with serial resistances $R_{L1}=34m\Omega$ and $R_{L2}=170m\Omega$. Different component values and references were simulated in order to identify an effective configuration which permits proper operation at high efficiency (Table III).

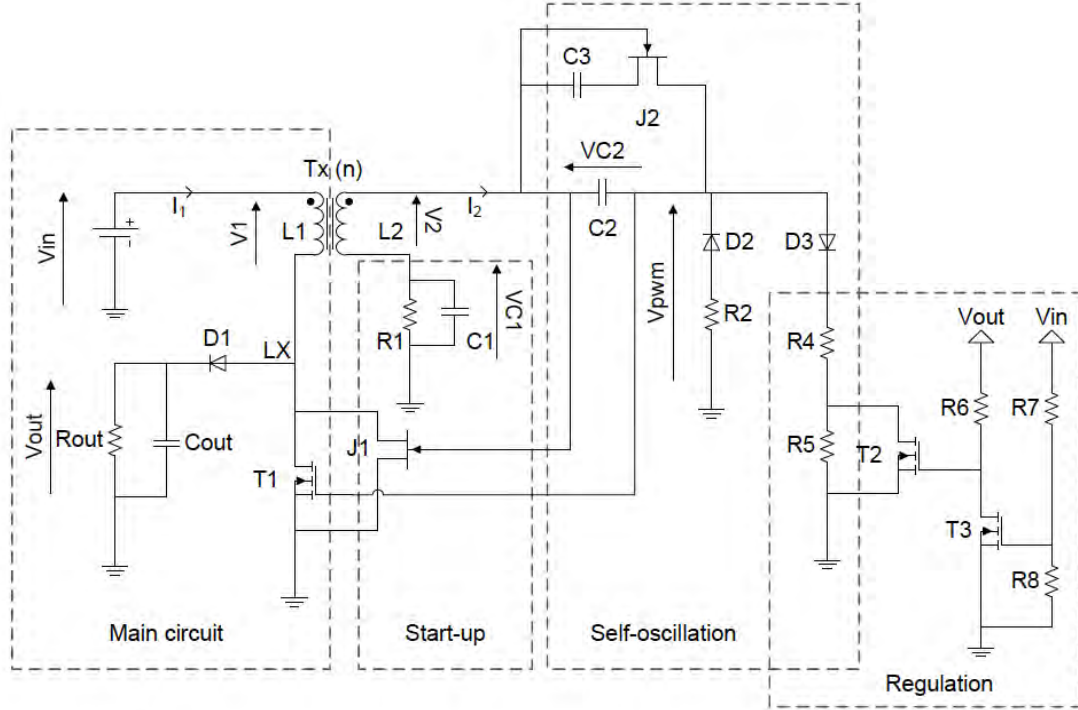


Figure 9. Schematic of the converter powered by an ideal voltage source and loaded by a resistor

TABLE III. COMPONENT VALUES AND REFERENCES

Part	Description		
	Type	Company	Reference
Vin	Voltage source		0.3V
Tx	Transformer (1:5)	Würth Elektronik	WE749197301
T1	Transistor NMOS	Diode Inc	DMG6968
T2, T3	Transistor	Advanced Linear Devices	ALD110802
J1, J2	Transistor JFET	NXP	JBF862
D1	Diode	Diode Inc	BAT54
D2, D3	Diode	Avago technologies	HSMS285X
R1, Rout	Resistor		1kΩ
R2	Resistor		50Ω
R4	Resistor		20kΩ
R5	Resistor		500kΩ
R6	Resistor		2MΩ
R7	Resistor		100kΩ
R8	Resistor		200kΩ
C1, Cout	Capacitor		8μF
C2	Capacitor		300pF
C3	Capacitor		3nF

IV. CIRCUIT OPERATION

A. Main boost

The main circuit is an asynchronous boost converter operating at the boundary of the continuous and discontinuous mode (critical conduction mode). Ideal step-up ratio is given by (1), D being the duty cycle.

$$\frac{V_{out}}{V_{in}} = \frac{1}{1-D}. \quad (1)$$

B. Self-oscillation

The boost main switch T_1 is controlled by a pulse-width modulated signal V_{PWM} . During steady-state, the primary winding of the transformer is either subject to voltage $V_1 = V_{IN}$ during the ON-state of T_1 (T_{up}), either to $V_1 = V_{IN} - V_{OUT}$ during the OFF-state of T_1 (T_{down}). If L_1 and L_2 are considered ideally coupled, $V_2 = n \cdot V_1$.

During T_{up} , $V_2 = n \cdot V_{in}$ and V_{pwm} is positive (we assume $VC_2 = 0$). Diode D_3 is ON and the capacitor C_2 charges (VC_2 increases) in the equivalent serial resistance (to D_3) with defined time constant. The voltage V_{pwm} decreases and when it reaches the threshold voltage V_{th} of T_1 , it turns OFF.

During T_{down} , diode D_2 is ON. The capacitor C_2 discharges (VC_2 decreases) through R_2 . T_{down} lasts until the current through the diode D_1 decreases to zero and cancels. I_1 gets negative (through the body-diode of T_1),

lowers the LX node voltage and inverts voltage V1 at the primary winding of the transformer. When D3 is ON again (Tup), the initial PWM voltage (Vpwmmax) will be higher than Vth because VC2 decreased during Tdown. The discontinuities of Vpwm are theoretically equal to the discontinuities of V2 ($= n \cdot V_{out}$).

The experimental measures (Fig. 11) were acquired such that $V_{in} = 0.3V$ and $V_{out} = 2.7V$. The amplitude of V2 is about 17.6V which is higher than $n \cdot V_{out} = 5 \cdot 2.7$. The reason can be partially found in the presence of a threshold voltage Vd1 for diode D1 which imposes an amplitude equal to $n \cdot (V_{out} + V_{d1})$. The measured amplitude of Vpwm is close to 3V which is a less than the 17.6V expected. The reason is that the equivalent parasitic capacitance Cpwm (composed of the parasitic capacitances in the interconnections and in T1, D2, D3 and J2) seen at the gate of T1 is high compared to C2. The currents required to charge Cpwm at each commutation cause apparent discontinuities of VC2 and attenuation of the amplitude of Vpwm.

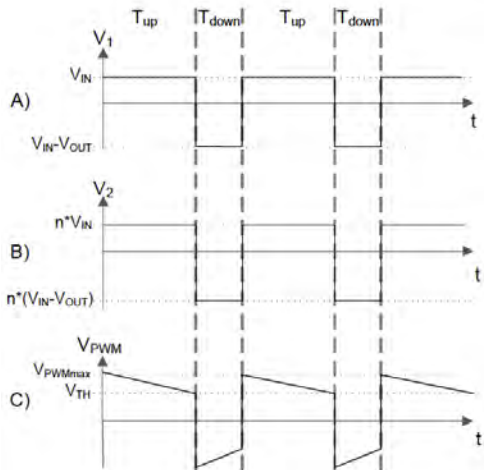


Figure 10. Theoretical shapes of the converter

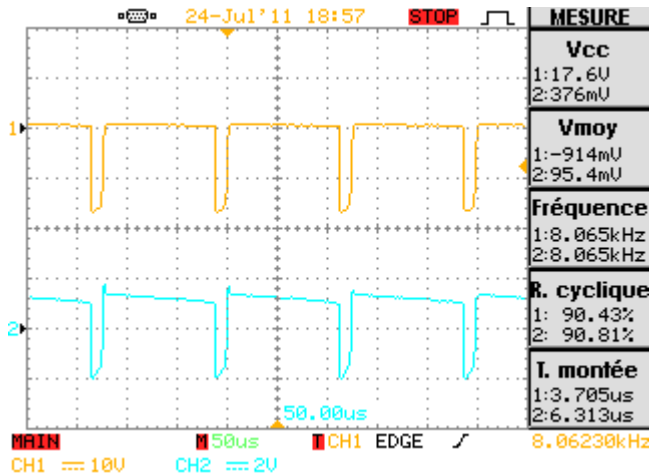


Figure 11. Experimental shapes of V2 (above) and of Vpwm (below)

C. Regulation

The regulation loop controls the input voltage V_{IN} . It modifies the equivalent resistance (serially connected to D3) that sets T_{up} and the switching frequency ($f \approx 8kHz$ for $R_{int} = 9\Omega$). Transistor T_3 is a specific ultra-low voltage NMOS transistor. T_3 drives transistor T_2 through a logic-inverter structure (R_6 and T_3) which is supplied by the output voltage of the converter. Resistors R_4 and R_5 are necessary to adapt minimum and maximum values of the equivalent serial resistance.

D. Start-up

JFET J_1 is connected in parallel with NMOS T_1 and enables autonomous start-up. When a positive voltage V_{IN} is applied at the input, the current I_1 through the primary winding and through the normally-ON JFET T_1 increases. The voltage $V_2 = n \cdot V_1$ ($n = n_2/n_1 = 5$ turn-ratio) at the secondary winding is positive. Since the gate of T_1 is biased positively (initially $V_{C1} = 0$), the internal diode of T_1 conducts and V_{C1} gets negative. When the current I_1 is high enough, V_1 decreases ($R \cdot L$ time constant), and V_2 decreases. The gate voltage of J_1 therefore decreases, and its ON-resistance increases. J_1 is driven slightly towards the off-state.

When J_1 is OFF, the current I_1 decreases through D_1 . V_1 and V_2 get negative. When I_1 gets negative, voltage V_1 increases, V_2 increases and T_1 switches ON again. Oscillations amplify and the output capacitor C_3 is charged, amplifying the current slopes. At a certain point, the amplitude of oscillations is high enough and T_1 switches ON and OFF synchronously with J_1 . When the voltage across C_1 is negative enough, J_1 stops switching.

V. EXPERIMENTATION RESULTS

The circuit was realized on a printed circuit board (PCB) using discrete components (Fig. 12). Resistors R2, R4, R5 and R7 were replaced by potentiometer to enable manual tuning of the regulation.

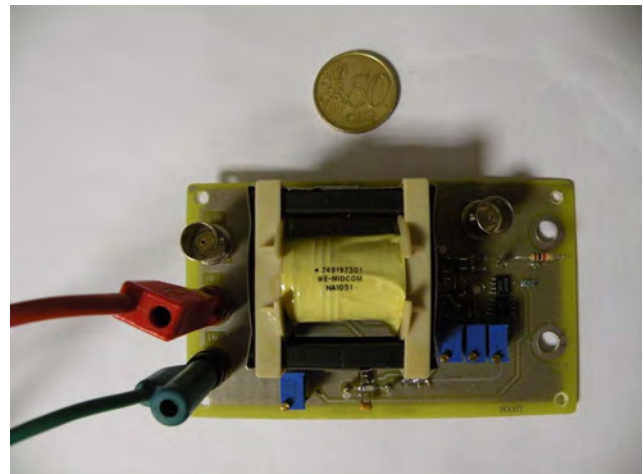


Figure 12. photograph of the printed circuit board of the circuit

The most critical part of the circuit is the transformer which consumes almost half of the PCB area, and which accounts for most of the circuit total cost. This transformer was chosen for its large primary inductance, low serial resistance, and high turn-ratio. More compact transformers can be used, but their higher serial resistances cause additional conduction losses and their lower primary inductance impose a higher switching frequency leading to increased switching losses.

The circuit was tested upon varied source characteristics, corresponding to MFCs connected in parallel fed under different substrate concentration. The experimental setup (Fig. 13) includes an ideal 0.6V voltage source serially connected to a variable resistor R_{int} (representing the equivalent internal resistance of a MFC). An input capacitor $C_{in} = 1\text{mF}$ is added in parallel in order to filter the input of the converter. At the output, the load is composed by a resistor parallelly connected to an output filter capacitor $C_{out} = 8\mu\text{F}$.



Figure 13. Description of the test methodology

The values of the potentiometer were set in a reproducible way. R_4 was set to its minimum value (0Ω), R_5 to its maximum value ($1\text{M}\Omega$). R_7 was first set to its minimum value (0Ω) and R_2 was set to have an input voltage $V_{in} = 0.25\text{V}$ (under nominal $R_{int} = 9\Omega$). R_7 was then readjusted to have $V_{in} = 0.3\text{V}$.

The converter shows simulated global efficiency of 80.6%. Loss analysis is identical to the boost standard circuits it is derived from, except for additional switching losses due to the self-oscillating circuits. Low switching frequency enables decrease of switching losses at the expense of a large transformer magnetization inductance. Most losses are therefore located in diode D_1 and can be decreased with the addition of a parallel MOS transistor (at the cost of circuit complexity). The transformer used is chosen for its very low serial resistance and ensures low conductive losses at the expense of size.

Fig. 14 shows how global efficiency is impacted by the input serial resistance R_{int} . The discrete realization of the circuit has a lower efficiency than what was expected from simulation, especially for low values of R_{int} . These differences can be attributed to: (1) improper tuning of the regulation through the choice of potentiometer values, (2) additional losses in the interconnections, (3) large switching loss in the self-oscillation circuit due to high voltage levels, and (4) additional parasitic elements not considered in the model of the transformer (e.g. losses in the magnetic materials and losses due to the parasitic capacitances).

The efficiency of the converter under nominal conditions was measured to be 74%.

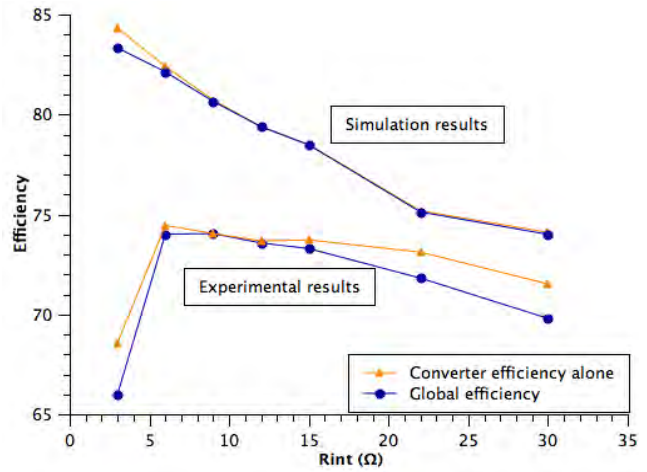


Figure 14. Simulation and experimental efficiency

VI. DISCUSSIONS

In this section, the proposed converter properties are discussed in term of step-up ability, control and cost.

A. Step-up

Boost efficiency theoretically decreases when the step-up ratio is high (duty cycle $D > 0.9$). It is not verified with the described converter, because of the low switching frequency. The proposed converter steps-up by a ratio over 10 in adequate loading conditions.

B. Control

The converter includes a regulation loop which is designed to regulate the input voltage (enabling therefore the implementation of MPPT function). The difference between converter efficiency and global efficiency (Fig. 14) corresponds to the losses caused by improper MPPT regulation. The non-effectiveness of the regulation is visible on the experimental results in case of large variations of R_{int} , but still accounts for less than 3% efficiency loss. Future works will have to focus on improving MPPT regulation.

C. Cost

The circuit is area consuming because of the large inductive elements it requires for operation. Integration of active parts would decrease switching losses, enable higher frequency operation, and decrease constraints on inductive components. Assuming a switching loss reduction of a factor 100, the primary inductance of the transformer could be sized to equal few micro-henries, which permits the use of compact transformer, but still forbids the thought of a fully integrated circuit.

VII. CONCLUSION

The paper presents a DC:DC boost converter specifically designed for MEGs. Its self-starting and self-oscillating circuits enable autonomous operation at input voltages of

0.3V. Its high step-up ratio enables outputs voltages above 2V depending on loading conditions. The MPPT regulation helps the converter harvest maximum energy from MEGs under variable fuel concentration. The maximum global efficiency achieved experimentally under nominal condition is 74%.

VIII. PERSPECTIVES

One of the next challenges is to modify the circuit so that it can be integrated. In order to foresee a full integration (active and passives), the structure probably has to be largely modified. We propose to separate the main boost circuit from the start-up circuit. The boost converter, if switching at high frequency (in the mega-hertz range) would permit the integration of its inductor. The start-up is necessary, but can be done separately, and differently.

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