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Modular Abstractions of Reactive Nodes using Disjunctive Invariants*

David Monniaux[†] Martin Bodin[‡]

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Abstract

We wish to abstract nodes in a reactive programming language, such as Lustre, into nodes with a simpler control structure, with a bound on the number of control states. In order to do so, we compute disjunctive invariants in predicate abstraction, with a bounded number of disjuncts, then we abstract the node, each disjunct representing an abstract state. The computation of the disjunctive invariant is performed by a form of quantifier elimination expressed using SMT-solving.

The same method can also be used to obtain disjunctive loop invariants.

1 Introduction

Our goal is to be able to compute sound abstractions of reactive nodes, with tunable precision. A reactive node in a language such as LUSTRE,¹ or SCADE,² SAO,³ or even SIMULINK,⁴ has input streams, output streams, and an (optional) internal state: at each clock cycle, the value on each output is a function of the values on the inputs and the state; and so is the next value of the state.

If the state consists in a finite vector of Booleans, or other finite values, then the node is a finite automaton, with transitions guarded according to the current values of the inputs, and for each state a relation between the current values of the inputs and the current values of the outputs. This is often referred to as the *control structure* of the reactive program. The problem with that representation, which exposes the full internal state, is that the number of states

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¹LUSTRE is a synchronous programming language, which gets compiled into C. [2].

²SCADE is a graphical synchronous programming language derived from LUSTRE. It is available from Esterel Technologies. It has been used, for instance, for implementing parts of the Airbus A380 fly-by-wire systems.

³SAO is an earlier industrial graphical synchronous programming language. It has been used, for instance, for implementing parts of the Airbus A340 fly-by-wire systems.

⁴SIMULINK is a graphical data-flow modeling tool sold as an extension to the MATLAB numerical computation package. It allows modeling a physical or electrical environment along the computerized control system. A code generator tool can then provide executable code for the control system for a variety of targets, including generic C. SIMULINK is available from The Mathworks.

grows exponentially with the number of state variables, making it unwieldy for analysis. The problem is even more severe if the control conditions are not directly exposed as Boolean state variables, but as predicates over, say, integer or real variables (see example in Sec. 4).

The main contribution of this article is a method for constructing a more abstract automaton, with a bounded number of states ($\leq n$), whose behaviors still over-approximate the behaviors of the node. In order to do so:

1. We compute an over-approximation of the set of reachable states of the node, in an unspecified context, as a union of at most n “abstract states”, each defined by a conjunction of constraints (these abstract states need not be disjoint).
2. We compute the most precise transition relation between these abstract states.

This automatic abstraction maps a reactive node into another, more abstract (and, in general, nondeterministic) reactive node. This enables modular and compositional analysis: if a node is composed of several nodes, then one can replace each of these nodes by its abstraction, and then analyze the compound node.

As a secondary contribution, the analysis method at step 1 can also be used to obtain disjunctive loop invariants for imperative programs (or, more generally, invariants for arbitrary control flow graphs), given a precondition and an optional postcondition. We describe this algorithm for obtaining invariants in disjunctive normal form, but it in fact also works for other templates.

Our algorithms use *satisfiability modulo theory* (SMT) solving as an essential subroutine; see e.g. [3] for an introduction.

2 Invariants by Predicate Abstraction

Predicate abstraction abstracts program states using the truth value of a given finite set of predicates $\{\pi_1, \dots, \pi_m\}$: each state σ is abstracted by a m -tuple of Booleans $(\pi_1(\sigma), \dots, \pi_m(\sigma))$. The most precise abstract transition relation between such vectors of Booleans is $(B_1, \dots, B_m) \rightarrow_\pi (B'_1, \dots, B'_m)$ if and only if there exist $\sigma \models \bigwedge (\pi_i = B_i)$, $\sigma' \models \bigwedge (\pi_i = B'_i)$, and $\sigma \rightarrow \sigma'$ where \rightarrow is the transition relation of the program. Then, given an abstract initial state, the set of reachable states of the abstract transition relation can be computed within finite time (in general, exponential in m) by Kleene iterations (equivalently, by computing the transitive closure of \rightarrow_π).

Such an approach is, however, unworkable in general because of the exponential number of states generated, and thus all current predicate abstraction schemes use some stronger form of abstraction [7]; for instance, they may simply compute a conjunction of the π_i that holds inductively at a given program point. Conjunctive invariants are however fairly restrictive; in this article, we consider the problem of obtaining invariants as *disjunctions of a fixed number of conjunctions* of the chosen predicates.

The set of reachable states of a reactive node, in an unspecified environment, is the strongest invariant of an infinite loop:

```

while (true) {
  i = inputs();
  o = outputs(state, i);
  state = next_state(state, i);
}

```

We shall therefore investigate the problem of automatically finding disjunctive inductive loop invariants (or, more generally, invariants for predicate abstraction following a fixed template), using predicate abstraction, given a precondition and an optional postcondition. These invariants shall be minimal with respect to the inclusion ordering; there shall be no stronger inductive invariant definable by the same template.

2.1 Solution of a Universally Quantified Formula

Let us assume a finite set $\Pi = \{\pi_1, \dots, \pi_m\}$ of predicates over the state space of the variables of the program. Let $n \geq 1$ be an integer. We are looking for invariants of the form $C_1 \vee \dots \vee C_n$ where the C_i are conjunctions of predicates from Π (most of our techniques are not specific to this template form, see Sec. 2.5 for extensions).

Any such invariant can be obtained by instantiating the Booleans $b_{i,j}$ in the following template:

$$\mathcal{T} \triangleq \bigvee_i \underbrace{\bigwedge b_{i,j}}_{C_i} \Rightarrow \pi_j \quad (1)$$

Setting $b_{i,j}$ to **true**(respectively, **false**) in that template means that predicate π_j appears (respectively, does not appear) in the i -th disjunct C_i . For instance, if $\Pi = \{x > 0, x < 1, y > 0\}$ and $n = 2$, then $b_{1,1} = \text{true}$, $b_{1,2} = \text{true}$, $b_{1,3} = \text{false}$, $b_{2,1} = \text{false}$, $b_{2,2} = \text{false}$, $b_{2,3} = \text{true}$ correspond to $(x > 0 \wedge x < 1) \vee y > 0$.

The problem of finding an invariant reduces to finding suitable values for these Booleans. There is therefore a search space for invariant candidates of *a priori* size 2^{mn} . We impose that the invariant I obtained be *minimal* within that search space with respect to the inclusion ordering; that is, there is no I' expressive using the template such that $I' \subsetneq I$.

Our algorithm can in fact apply to any control-flow graph. For the sake of simplicity, we shall describe it on a single loop.

In Hoare logic, the conditions for proving that a postcondition P holds after a while loop whose condition is C , whose transition relation is T and whose precondition is S using loop invariant I are:

- I must contain the precondition, otherwise said $\forall \sigma S \Rightarrow I$.
- I must be inductive, otherwise said $\forall \sigma, \sigma' I \wedge C \wedge T \Rightarrow I'$, with I' denoting I where all state variables have been primed.
- $I \wedge \neg C$ must imply the postcondition, otherwise said $\forall \sigma I \wedge \neg C \Rightarrow P$.

If we impose I to be an invariant of the required form, that is, an instantiation $\mathcal{T}[B/b]$ of \mathcal{T} obtained by setting the $b_{i,j}$ variables to certain values $B_{i,j}$, these conditions boil down to the values $B_{i,j}$ of the $b_{i,j}$ variables must satisfy

certain formulas universally quantified over the state σ or on the couple of states σ, σ' .

We now make an additional assumption: the states σ or σ' comprise a fixed, *finite* number of variables⁵ expressible in a theory \mathfrak{T} for which there exists a satisfiability testing algorithm, in which the predicates π_1, \dots, π_m can be expressed, and which allows propositional variables. Thus, the problem boils down to finding a solution to a conjunction of universally quantified formulas of that theory such that the only free variables are the $b_{i,j}$ Booleans.

In the following sections, lowercase σ and σ' stand for states (thus stand for a finite number of variables in the theory \mathfrak{T}), uppercase Σ and Σ' stand for values of these state variables. Similarly, lowercase b stands for the matrix of propositional variables $(b_{i,j})_{1 \leq i \leq m, 1 \leq j \leq n}$, and uppercase B stands for the matrix of Booleans $(B_{i,j})_{1 \leq i \leq m, 1 \leq j \leq n}$. $F[B/b]$ thus stands for the formula F where the propositional values b have been replaced by the corresponding Booleans in B , and $F[\Sigma/\sigma]$ stands for the formula F where the state variable σ has been replaced by the state value Σ .

2.2 Naive Algorithm for a Given Postcondition

In this section, we shall explain how to compute an invariant suitable for proving the Hoare triple of a loop, given a precondition, a postcondition (which may be true), a loop condition and a transition relation.

Let us first give an intuition of the algorithm. A universally quantified formula $\forall \sigma F$ with free Boolean variables b can be understood as specifying a potentially infinite number of constraints $F[\Sigma/\sigma]$ over b , where Σ ranges all possible values for σ (in this section, we will lump together σ and σ' as a single σ). The idea is to “discover” such constraints one at a time, when they are violated.

Let us now examine the algorithm in more detail; see Sec. 3 for a complete algorithm run. The H_k sequence of propositional formulas over the b variables will express successive refinements of the constraints during the search of a suitable assignment. Initially, we do not know anything about possible solutions, so we set $H_1 \triangleq \text{true}$.

We start by taking any initial assignment $B^{(1)}$ (since any will satisfy H_1) and check whether $\neg F[B^{(1)}/b]$ is satisfiable, that is, whether one can find suitable values for σ . If it is not, then $B^{(1)} \models \forall \sigma F$. If it is satisfiable, with example value Σ_1 , we add $F[\Sigma_1/\sigma]$ as a constraint — that is, we take $H_2 \triangleq H_1 \wedge F[\Sigma_1/\sigma]$; note that this constraint excludes $B^{(1)}$ and possibly other values for b . Now find an assignment $B^{(2)}$ satisfying H_2 , check whether $\neg F[B^{(2)}/b]$ is satisfiable. If it is not, then $B^{(2)} \models \forall \sigma F$. If it is satisfiable, with example value Σ_2 , we take $H_3 = H_2 \wedge F[\Sigma_2/\sigma]$; note that H_3 excludes $B^{(1)}$ and $B^{(2)}$. The process continues until a suitable assignment is found or the constraints exclude all assignments. Note that one Boolean assignment at least is excluded at each iteration, and that the number of Boolean assignments is finite (exponential in the number of propositional variables in b).

⁵These variables are not necessarily scalar variables. It is for instance possible to consider uninterpreted functions from the integers to the integers, which stand for a countably infinite number of integers.

More formally: recall that we have reduced our problem of finding an invariant to finding Boolean values $B_{i,j}$ such that $(B_{i,j})_{1 \leq i \leq m, 1 \leq j \leq n} \models \forall \sigma F$ for a certain quantifier-free formula F whose free variables are $(b_{i,j})_{1 \leq i \leq m, 1 \leq j \leq n}$. Let us now assume we have a SMT-solver for theory \mathfrak{T} , a function $SMT(G)$ which given a formula G answers $sat(M)$ when G is satisfiable, where M is a model, that is, a suitable instantiation of the free variables in G , or $unsat$ otherwise. We shall also assume a SAT-solver SAT with similar notations, for purely propositional formulas. We run the following algorithm, expressed in pseudo-ML:

```

H := true
loop
  match SAT(H) with
  | unsat → return “no solution”
  | sat((Bi,j)1 ≤ i ≤ m, 1 ≤ j ≤ n) →
    match SMT(¬F[B/b]) with
    | unsat → return “solution B”
    | sat(Σ) → H := H ∧ F[Σ/σ].

```

This algorithm always terminates, since the main loop iterates over a finite set of size $2^{|b|}$ where $|b| = mn$ is the size of the matrix b of propositional variables: the number of models of the propositional formula H decreases by at least one at each iteration, since model B is excluded by the $F[\Sigma/\sigma]$ condition. The loop invariant is $\forall \sigma F \implies H$. This invariant is maintained: whatever we choose for Σ , if $\forall \sigma F \implies H$, $\forall \sigma F \implies H \wedge F[\Sigma/\sigma]$. If the algorithm answers “no solution” for H , because of the invariant, there is no solution for $\forall \sigma F$. If the solution answers “solution B ”, the “unsat” answer for $SMT(\neg F[B/b])$ guarantees that $B \models \forall \sigma F$.

Note the use of two solvers: one SAT for the propositional variables b , and one SMT for the state variables σ (or σ, σ'). The SAT solver is used incrementally: one only adds new constraints. The SMT solver is always used with the same set of predicates, enabling it to cache theory lemmas.

2.3 Performance Improvements

The algorithm in the preceding subsection is sound, complete and terminating. Yet, experiments have shown that it tends to generate useless iterations. One reason is that the system may iterate across instances B that yield the same formula $T[B/b]$ up to a permutation of the C_i disjuncts. Another is that the system may generate empty disjuncts C_i , or more generally disjuncts that are subsumed by the other disjuncts (and are thus useless). We shall explain how to deal with those issues.

2.3.1 Removal of Permutations

We impose that the disjunction $C_1 \vee \dots \vee C_n$ follows a unique canonical ordering. For this, we impose that the vectors of m Booleans $(B_{1,j})_{1 \leq j \leq m}, \dots, (B_{n,j})_{1 \leq j \leq m}$ are in strict increasing order with respect to the lexicographic ordering \prec_L induced by $\text{false} < \text{true}$. This corresponds to $n - 1$ constraints $(b_{i,j})_{1 \leq j \leq m} \prec_L (b_{i+1,j})_{1 \leq j \leq m}$, each of which can be encoded over the propositional variables $(b_{i,j})$ as formula $L_{i,1}$ defined as follows:

- L_{i,j_0} is a formula whose meaning is that $(b_{i,j})_{j_0 \leq j \leq m} \prec_L (b_{i+1,j})_{j_0 \leq j \leq m}$
- $L_{i,m+1}$ is false
- L_{i,j_0} for $1 \leq j_0 \leq m$ is defined using L_{i,j_0+1} as follows: $(\neg b_{i,j_0} \wedge b_{i+1,j_0}) \vee ((b_{i,j_0} \Rightarrow b_{i+1,j_0}) \wedge L_{i,j_0+1})$.

All such constraints can be conjoined to the initial value of H .

2.3.2 Removal of Subsumed Disjuncts

We can replace the SAT-solver used to find solutions for $(b_{i,j})$ by a SMT-solver for theory \mathfrak{T} , in charge of finding solutions for $(b_{i,j})$ and for some auxiliary variables $\sigma_1, \dots, \sigma_n$ (we actually shall not care about the actual values of $\sigma_1, \dots, \sigma_n$). The following constraint expresses that the disjunct C_{i_0} is not subsumed by the disjuncts $(C_i)_{1 \leq i \leq n, i \neq i_0}$:

$$\exists \sigma_{i_0} C_{i_0}[\sigma_{i_0}/\sigma] \wedge \bigwedge_{1 \leq i \leq n, i \neq i_0} \neg C_i[\sigma_i/\sigma] \quad (2)$$

It therefore suffices to conjoin to the initial value of H the following constraints, for $1 \leq i_0 \leq n$: $C_{i_0}[\sigma_{i_0}/\sigma] \wedge \bigwedge_{1 \leq i \leq n, i \neq i_0} \neg C_i[\sigma_i/\sigma]$.

A variant consists in simply imposing that each of the C_i is satisfiable, thus eliminating useless false disjuncts. For this, one imposes $1 \leq i_0 \leq n$, the constraint $C_{i_0}[\sigma_{i_0}/\sigma]$. Equivalently, one can pre-compute the “blocking clauses” over the $b_{i_0,j}$ propositional variable that constrain these variables so that C_{i_0} is satisfiable, and add them as purely propositional constraint. This is the method that we used for the example in Sec. 3 (we wanted to keep to propositional constraints for the sake of simplicity of exposition).

2.4 Iterative Refinement of Invariants

We have so far explained how to compute *any* invariant, with or without imposing a postcondition. If we do not impose a postcondition, the formula `true`, for instance, can denote a wholly uninteresting invariant; clearly we would like a smaller one. In this section, we shall explain how to obtain *minimal* invariants within the search space.

2.4.1 For a Fixed Disjunction Size

Let us now assume we have the postcondition P (if we do not have it, then set P to `true`). A natural question is whether one can get a *minimal* inductive invariant of the prescribed form for the inclusion ordering; that is, an invariant $T[B_0/b]$ such that there exists no B such that $T[B/b] \subseteq T[B_0/b]$, by which we denote $\forall \sigma T[B/b] \Rightarrow T[B_0/b]$. We shall now describe an iterative algorithm that first obtains any inductive invariant of the prescribed form, and then performs a downwards iteration sequence for the inclusion ordering, until a minimal element is found.

Let us first note that it is in general hopeless to find a global minimum B_0 , that is, one such that $\forall B T[B_0/b] \subseteq T[B/b]$, for there may exist incomparable minimal elements. For instance, consider the program:

```

float i = 0;
while(random()) {
    i = i+1;
    if (i > 2) i = 0;
}

```

The least inductive invariant of this loop, for variable i , is the set of floating-point numbers $\{0, 1, 2\}$. Now assume our set of predicates is $\{i \leq 0, i \geq 0, i \geq 1, i \leq 1, i \leq 2, i \geq 2\}$, and take $n = 2$; we thus look for disjunctions of two intervals. Two minimal incomparable invariants are $(i \geq 0 \wedge i \leq 1) \vee (i \geq 2 \wedge i \leq 2)$, that is, $[0, 1] \cup \{2\}$, and $(i \geq 1 \wedge i \leq 2) \vee (i \leq 0 \wedge i \geq 0)$, that is, $[1, 2] \cup \{0\}$.

Let us now assume we have already obtained an invariant $T[B'/b]$ and we wish to obtain a better invariant $T[B/b] \subsetneq T[B'/b]$. This last constraint can be written as the conjunction of:

1. $T[B/b] \subseteq T[B'/b]$, otherwise said $\forall \sigma T[B/b] \Rightarrow T[B'/b]$; such a universally quantified constraint can be handled as explained in Sec. 2.2.
2. $\exists \sigma T[B'/b] \wedge \neg T[B/b]$. Again, as explained in Sec. 2.3.2, one can treat such an existentially quantified constraint by using a SMT-solver instead of a SAT-solver and adding to H an extra variable σ and the constraint $T[B'/b] \wedge \neg T[B/b]$. When an invariant $T[B/b]$ is found, the value Σ of σ is a witness that this invariant is *strictly included* in $T[B'/b]$.

It is possible to compute a downward iteration sequence until a minimal element is reached: compute any initial invariant $B^{(0)}$, then $B^{(1)} \subsetneq B^{(0)}$ etc. until the system fails to provide a new invariant satisfying the constraints; one then takes the last element of the sequence. The termination condition is necessarily reached, for the $(B_{i,j}^{(k)})_{1 \leq i \leq m, 1 \leq j \leq n}$ Boolean matrices can never be twice the same within the sequence (because of the strict descending property). Furthermore, one can stop at any point $B^{(k)}$ within the sequence and get a (possibly non minimal) inductive invariant.

One can replace point 2 above by a weaker strategy, but with the advantage of operating only on propositional formulas. Note that $B^{(k+1)}$ has at least one component higher than $B^{(k)}$ for the standard ordering $\text{false} < \text{true}$ on the Booleans, for if all components are lower or equal, then $B^{(k+1)} \supseteq B^{(k)}$, which is the opposite direction of what we wish. The strategy is to enforce this condition using $\bigvee_{i,j} (b_{i,j} \wedge \neg b'_{i,j})$. This is what we used in Sec. 3.

2.4.2 For Varying Disjunction Sizes

The algorithms described above work for a given disjunction size n . The method for preventing subsumed disjuncts of part Sec. 2.3.2 imposes that all n disjuncts are truly needed: it is thus possible that no solution should be found for $n = n_0$ while solutions exist for $n = n_0 - 1$.

We therefore suggest that, once a minimal invariant I_{n_0} is obtained for $n = n_0$ fixed, one looks for an invariant strictly included in I_{n_0} for $n = n_0 + 1$. One can choose to stop such iterations when no solutions are found for a given n , or when a limit on n or a timeout is reached.

2.5 Extensions

Prohibition of Overlapping Modes Our algorithms produce disjunctions that cover all reachable states, but that do not define partitions: distinct abstract states may be overlapping. This may be somewhat surprising and counterintuitive.

It is possible to impose that disjuncts should be pairwise disjoint. For any i and j , one can impose that C_i and C_j are disjoint by the universally quantified formula $\forall\sigma \neg C_i \vee \neg C_j$. We have explained in the preceding sections how to deal with such universally quantified formulas.

Other Template Forms We have described our algorithm for templates of the form $C_1 \vee \dots \vee C_m$ where the C_i are conjunctions constructed from the chosen predicates, but the algorithm is not specific to this template shape. Instead of disjunctive normal form, one could choose conjunctive normal form, for instance, or actually any form [23], though reductions of the search space such as those from Sec. 2.3.1 or 2.3.2 may be more difficult to define.

Predicate Choice Our method is based on predicate abstraction; so far we have not discussed methods for obtaining the predicates, beyond the obvious syntactic detection. In many systems based on predicate abstraction, one uses *counterexample-based abstraction refinement* (CEGAR): from an abstract trace violating the specification, but not corresponding to a concrete trace violating the specification, one derives additional predicates for refining the system. Because we did not implement such refinement, we shall only give a rough description of our CEGAR method.

If there is no inductive invariant built from the requested template that can prove the desired postcondition, the algorithm from Sec. 2.2 will end up with an unsatisfiable constraint system. This system is unsatisfiable because of the postcondition constraints (otherwise, in the worst case, one would obtain a solution yielding the **true** formula); relevant postcondition constraints can be obtained from an unsatisfiable core of the constraint system. One can then try removing such constraints one by one until the constraint system becomes satisfiable again. Any solution of this relaxed constraint system defines an inductive invariant, but one that does not satisfy the postcondition. As with the usual CEGAR approach, one could try generating test traces leading from the initial states to the complement of the postcondition and staying within the invariant; if the postcondition holds, such searches are unsuccessful and yield interpolants from which predicates may be mined.

3 Step-by-step Example of Invariant Inference

For the sake of simplicity of exposition, in this section we have restricted ourselves to pure propositional constraints on the $b_{i,j}$, and satisfiability modulo the theory of linear integer arithmetic for the combination of the $b_{i,j}$ and the state variables. We consider the following simple program.

```
int b, i=0, a; /* precondition a > 0 */  
while (i < a) {  
    b = random();
```

```

if (b)
  i = i + 1;
}

```

The predicates are $\{\pi_1, \dots, \pi_8\} \triangleq \{i = 0, i < 0, i > 0, i = a, i < a, i > a, b, \neg b\}$. The state variable σ stands for (i, a, b) . For the sake of simplicity, we model i and a as integers in \mathbb{Z} , and b as a Boolean. We assume the loop precondition $S \triangleq i = 0 \wedge a \geq 1$. The loop condition is $C \triangleq i < a$, and the transition relation is $T \triangleq (b' \wedge i' = i + 1) \vee (\neg b' \wedge i' = i)$. We choose $n = 2$.

We shall now run the algorithm described in Sec. 2.2 with the iterative refinement of Sec. 2.4.1. For the sake of simplicity, we shall use none of the improvements described in the preceding sections that need the H_i to contain non propositional variables: no removal of subsumed disjuncts as described in Sec. 2.3.2 and no strict inclusion enforcement as described in Sec. 2.4.1.

We initialize H as follows: H_1 contains Boolean constraints on $(b_{i,j})_{1 \leq i \leq 2, 1 \leq j \leq 8}$

- That prevent C_1 and C_2 from being unsatisfiable, using blocking clauses as explained in Sec. 2.3.2: one cannot have both $i = 0$ and $i > 0$, and so on.
- That force $(b_{1,j})_{1 \leq j \leq 8} \prec_L (b_{2,j})_{1 \leq j \leq 8}$ for the lexicographic ordering \prec_L on Boolean vectors (this avoids getting the same disjunction twice with the disjuncts swapped).

Let us now see the constraint solving and minimization steps.

1. We perform SAT-solving on H_1 and obtain a satisfying assignment $B_{1,1}^{(1)} = \text{true}$, $B_{1,2}^{(1)} = \text{false}$, $B_{1,3}^{(1)} = \text{false}$, $B_{1,4}^{(1)} = \text{true}$, $B_{1,5}^{(1)} = \text{false}$, $B_{1,6}^{(1)} = \text{false}$, $B_{1,7}^{(1)} = \text{true}$, $B_{1,8}^{(1)} = \text{false}$, $B_{2,1}^{(1)} = \text{true}$, $B_{2,2}^{(1)} = \text{false}$, $B_{2,3}^{(1)} = \text{false}$, $B_{2,4}^{(1)} = \text{true}$, $B_{2,5}^{(1)} = \text{false}$, $B_{2,6}^{(1)} = \text{false}$, $B_{2,7}^{(1)} = \text{false}$, $B_{2,8}^{(1)} = \text{true}$. This corresponds to the invariant-candidate $T[B^{(1)}/b]$, that is, $(i = 0 \wedge i = a \wedge b) \vee (i = 0 \wedge i = a \wedge \neg b)$. Now is this invariant-candidate truly an inductive invariant? It is not, because it does not contain the whole of the set of initial states. SMT-solving on $S \wedge \neg T[B^{(1)}/b]$ gives a solution $\Sigma_1 \triangleq (i = 0, a = 1, b = \text{false})$. We therefore take $H_2 \triangleq H_1 \wedge F[\Sigma_1/\sigma]$.
2. A satisfying assignment $B^{(2)}$ of H_2 yields the invariant candidate $(i = 0 \wedge i = a \wedge b) \vee (i = 0 \wedge i < a \wedge b)$. Again, SMT-solving shows this is not an invariant because it does not contain the initial state $\Sigma_2 \triangleq (i = 0, a = -1, b = \text{false})$. We therefore take $H_3 \triangleq H_2 \wedge F[\Sigma_2/\sigma]$.
3. A satisfying assignment $B^{(3)}$ of H_3 yields the invariant candidate $(i = 0 \wedge i = a \wedge b) \vee (i = 0 \wedge i < a)$. SMT-solving shows this is not inductive, since it is not stable by the transition $\Sigma_3 \triangleq (i = 0, a = 1, b = \text{false}, i' = 1, b' = \text{true})$. We therefore take $H_4 \triangleq H_3 \wedge F[\Sigma_3/\sigma]$.
4. A satisfying assignment $B^{(4)}$ of H_4 yields the invariant candidate $(i = 0 \wedge i < a \wedge \neg b) \vee b$. SMT-solving shows this is not inductive, since it is not

stable by the transition $\Sigma_4 \triangleq (i = 1, a = 3, b = \text{true}, i' = 1, b' = \text{false})$. We therefore take $H_5 \triangleq H_4 \wedge F[\Sigma_4/\sigma]$.

5. A satisfying assignment $B^{(5)}$ of H_5 yields the invariant candidate $(i = 0 \wedge i < a) \vee (i > 0 \wedge i = a \wedge b)$. SMT-solving shows this is not inductive, since it is not stable by the transition $\Sigma_5 \triangleq (i = 0, a = 2, b = \text{false}, i' = 1, b' = \text{false})$. We therefore take $H_6 \triangleq H_5 \wedge F[\Sigma_5/\sigma]$.
6. A satisfying assignment $B^{(6)}$ of H_6 yields the invariant candidate $I_1 \triangleq (i = 0 \wedge i < a) \vee i > 0$. SMT-solving shows this is an inductive invariant, which we retain. We however would like a *minimal* inductive invariant within our search space. As described at the end in Sec. 2.4.1, we take H_7 the conjunction of H_6 and a propositional formula forcing at least one of the $b_{i,j}$ to be **true** while $B_{i,j}^{(6)}$ is **false**. Furthermore, as described in point 1 of Sec. 2.4.1, we now consider $F_2 \triangleq F \wedge (T \Rightarrow I_1)$, which ensures that we shall from now on only consider invariants included in I_1 .
7. A satisfying assignment $B^{(7)}$ of H_7 yields the invariant candidate $(i > 0 \wedge i = a \wedge b) \vee i < a$. SMT-solving shows this is not included in I_1 , using $\Sigma_7 \triangleq (i = -47, a = 181, b = \text{true})$. We therefore take $H_8 \triangleq H_7 \wedge F_2[\Sigma_7/\sigma]$.
8. H_8 has no solution. I_1 is thus minimal and the algorithm terminates.

A postcondition for this loop is thus $I_1 \wedge \neg(i < a)$, thus $i > 0 \wedge i = a$. Note that the method did not have to know this postcondition in advance in order to prove it.

4 Construction of the Abstract Automaton

We can now assume that the set of reachable states is defined by a formula $I = I_1 \vee \dots \vee I_n$, with each formula I_i meant to define a state q_i of the abstract automaton.

To each couple of states (q_i, q_j) we wish to attach an input-output relation expressed as a formula $\tau_{i,j}$ with variables \mathcal{I} , ranging over the set of possible current values of the inputs and \mathcal{O} over the set of possible current values of the outputs.

Recall that T is a formula expressing the transition relation of the reactive node, over variables \mathcal{I} (inputs), σ (preceding state), σ' (next state) and \mathcal{O} (outputs). Then the most precise transition relation is:

$$\tau_{i,j} \triangleq \exists \sigma, \sigma' I_i \wedge I_j[\sigma'/\sigma] \wedge T \quad (3)$$

Any over-approximation of this relation is a sound transition relation for the abstract automaton. If we have a quantifier elimination procedure for the theory in which T and the I_i are expressed, then we can compute the most precise $\tau_{i,j}$ as a quantifier-free formula; but we can also, if needed, use an approximate quantifier elimination that yields an over-approximation.

Let us consider, as an example, the following Lustre node. It has a single integer input `dir` and a single integer output `out`. If `dir` is nonzero, then it is copied to `out`; else `out` decays to zero by one unit per clock cycle:

```

node clicker(dir : int) returns (out : int);
let
    out = if dir ≥ 1
        then dir
        else if dir ≤ -1
            then dir
            else 0 → if pre out ≤ -1
                then (pre out) + 1
                else if pre out ≥ 1
                    then (pre out) - 1
                else 0;
tel.

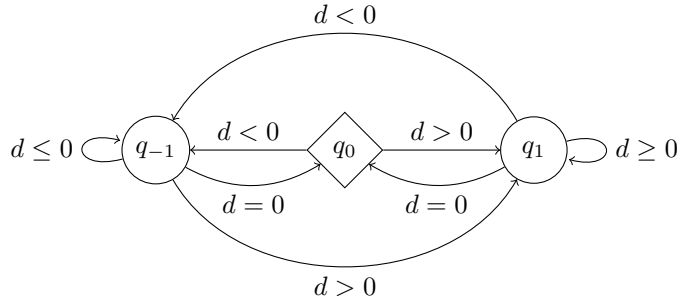
```

In mathematical notation, let us denote **dir** by d , **pre out** by o and **out** by o' . The state consists in a single variable o , thus σ is the same as o . The transition relation then becomes

$$T \triangleq \left\{ \begin{array}{l} (d \neq 0 \wedge o' = d) \vee (d = 0 \wedge o \geq 1 \wedge o' = o - 1) \\ \vee (d = 0 \wedge o \leq -1 \wedge o' = o + 1) \vee (d = 0 \wedge o' = o = 0) \end{array} \right. \quad (4)$$

Suitable predicates are $\{o \leq -1, o = 0, o \geq 1\}$, thus defining the set of reachable states as a partition $I_{-1} \vee I_0 \vee I_1$ where $I_{-1} \triangleq o \leq -1$, $I_0 \triangleq o = 0$, $I_1 \triangleq o \geq 1$.

Let us compute $\tau_{0,1} \triangleq \exists o, o' I_0 \wedge I_1[o'/o] \wedge T$, that is, $\exists o, o' o = 0 \wedge o' \geq 1 \wedge T$: we obtain $d > 0$. More generally, by computing $\tau_{i,j}$ for all $i, j \in \{-1, 0, 1\}$, we obtain the automaton below; the initializers (left hand side of the Lustre operator \rightarrow) define the initial state q_0 .



Note that the resulting automaton is nondeterministic: in state q_1 (respectively, q_{-1}), representing $o > 0$ (resp. $o < 0$), if $d = 0$, then one can either remain in the same state or return to the initial state q_0 .

5 Related Work

There have been many approaches proposed for finding invariants and proving properties on transition systems. [21] surveys earlier ones.

The problem of finding the control structure of reactive nodes written in e.g. Lustre has been studied previously, most notably by B. Jeannet [12, 13, 14], but with respect to a property to prove: the control structure is gradually refined until the property becomes provable. This supposes that we know the desired

property in advance, which is not always the case in a modular setting: the property may pertain to another module, and may not be easy to propagate back to the current module. The NBAC tool performs such an analysis using convex polyhedra as an abstract domain. More recent methods for refining the control structure of reactive nodes include [1]. We have already proposed some modular abstractions for reactive nodes, but these targeted specific filters with no control structure [15] or needed some precomputation of the control structure [17].

The problem of finding disjunctive invariants has been much studied especially in the context of convex numerical domains, such as polyhedra: if the property to prove is not convex, or relies on a non-convex weakest precondition, then *any* analysis inferring convex invariants will fail. A number of methods have been proposed to infer invariants consisting in finite disjunctions of elements of an abstract domain: some distinguish states according to the history of the computation, as in *trace partitioning* [19], some recombine elements according to some affinity heuristics [20, 18], or decompose the transition relation according to some “convexity witness” [8]. Other methods select predicates with which to split the control state [22]. Some recent methods leverage the power of modern SMT-solvers to impose convex invariants only at a limited subset of program points, and distinguish all execution paths between them, therefore acting as applying a complete trace partitioning between the points in the distinguished subset [17, 5]; the method in the present article also considers a limited subset of program points (e.g. loop heads), but can infer disjunctive invariants at these points too.

Both polyhedral abstraction and predicate abstraction search for an inductive invariant I ; then, in order to prove that a certain property P always holds, one shows that I is included in P . In all static analyzers by abstract interpretation known to the authors, some form of forward analysis is used: the set of initial states influences the invariant I obtained by the system. In contrast, with k -induction, as in the KIND tool [11] the initial states play a very limited role (essentially, they invalidate P if there exists a trace of k states starting in an initial state such that one of them does not satisfy P). A known weakness of pure k -induction is that it may fail to prove a property because it bothers about bad, but unreachable, states. If one has obtained an invariant I by other methods, one can use it to constrain the system and get rid of these bad, unreachable states. Thus, abstraction-based methods and k -induction based methods nicely combine.

The algorithms presented in this article can be seen as a form of minimization constrained by a universally quantified formula $\forall\sigma F$, achieved by maintaining a formula H such that $\forall\sigma F \Rightarrow H$, H being a conjunction of an increasingly large number of constraints generated from F “on demand”: a constraint is added only if it is violated by the current candidate solution. This resembles quantifier elimination algorithms we have proposed for linear real arithmetic [16]; one difference is that the termination argument is simpler: with a finite number n of Booleans as free variables, a new added constraint suppresses at least one of the 2^n models, thus there can be at most 2^n iterations; in comparison the termination arguments for arithmetic involve counting projections of polyhedra.

Reductions from invariant inferences to quantifier elimination, or to minimization constrained by a universally quantified formula, have already been proposed for numerical constraints, where the unknowns are numerical quanti-

ties, in contrast to the present work where they are Booleans [17].

Reductions from loop invariant inference in predicate abstraction to Boolean constraint solving were introduced in [10], but that work assumed a postcondition to prove, as opposed to minimizing the result. The problem we solve is the same as the one from the later work [23, Sec. 5], but instead of concretely enumerating the (potentially exponential) set of paths inside the program (corresponding to all disjuncts in a disjunctive normal form of the transition relation), each path corresponding to one constraint, we lazily enumerate witnesses for such paths. Unfortunately, we do not have an implementation of the algorithm from [23] at our disposal for performance comparisons.

More generally, a number of approaches for invariant inference based on constraint solving have been proposed in the last years, especially for reducing numerical invariant inference to numerical constraint solving [9, 4] or mathematical programming [6]. One difference between these constraint approaches and ours, except that our variables are Boolean and theirs are real, is that we use a *lazy* constraint generation scheme: we generate constraints only when a candidate solution violates them, a method long known in mathematical programming when applying *cuts*. We applied a similar technique for quantifier elimination for linear real arithmetic, using lazy conversions to conjunctive normal form [16]. A recent *max-policy iteration* considers each path through the loop as a constraint, and lazily selects a combination of paths, using SMT-solving to point the next relevant path [5].

6 Conclusion

We have given algorithms for finding loop invariants, or, equivalently, invariants for reactive nodes, given as templates with Boolean parameters. Using disjunctive invariants for reactive nodes, one obtains an abstraction of the reactive node as a finite automaton with transitions labeled with guards over node inputs.

If a system consists of a number of nodes, then some of these nodes may be replaced by their abstract automaton, resulting in a more abstract system whose behaviors include all behaviors of the original system. This new system can in turn be analyzed by the same method. Thus, our method supports modular and compositional analysis.

We provide the CANDLE tool, built using the YICES SMT-solver and the MJOLLNIR quantifier elimination procedure, which computes abstractions of LUSTRE nodes.

References

- [1] Gogul Balakrishnan, Sriram Sankaranarayanan, Franjo Ivančić, and Aarti Gupta. Refining the control structure of loops using static analysis. In *EMSOFT*, pages 49–58, New York, NY, USA, 2009. ACM. ISBN 978-1-60558-627-4. doi: 10.1145/1629335.1629343.
- [2] Paul Caspi, Daniel Pilaud, Nicolas Halbwachs, and John A. Plaice. LUSTRE: a declarative language for real-time programming. In *POPL (Symposium on Principles of programming languages)*, pages 178–188. ACM, 1987. ISBN 0-89791-215-2. doi: 10.1145/41625.41641.

- [3] Alessandro Cimatti. Beyond Boolean SAT: Satisfiability modulo theories. In *Discrete Event Systems (WODES)*, pages 68–73, May 2008. doi: 10.1109/WODES.2008.4605924.
- [4] Michael Colón, Sriram Sankaranarayanan, and Henny Sipma. Linear invariant generation using non-linear constraint solving. In Warren A. Hunt Jr. and Fabio Somenzi, editors, *CAV*, volume 2725 of *LNCS*, pages 420–432. Springer, 2003. ISBN 3-540-40524-0.
- [5] Thomas Gawlitza and David Monniaux. Improving strategies via SMT solving. In Gilles Barthe, editor, *ESOP*, volume 6602 of *LNCS*, pages 236–255. Springer, 2011. ISBN 978-3-642-19717-8. doi: 10.1007/978-3-642-19718-5_13.
- [6] Eric Goubault, Stéphane Le Roux, Jeremy Leconte, Leo Liberti, and Fabrizio Marinelli. Static analysis by abstract interpretation: A mathematical programming approach. *Electr. Notes Theor. Comput. Sci.*, 267(1):73–87, 2010.
- [7] Susanne Graf and Hassan Saïdi. Construction of abstract state graphs with PVS. In Orna Grumberg, editor, *Computer-Aided Verification (CAV)*, number 1254 in *LNCS*, pages 72–83. Springer, 1997. ISBN 3-540-63166-6. doi: 10.1007/3-540-63166-6_10.
- [8] Sumit Gulwani and Florian Zuleger. The reachability-bound problem. In Benjamin G. Zorn and Alexander Aiken, editors, *PLDI*, pages 292–304. ACM, 2010. ISBN 978-1-4503-0019-3. doi: 10.1145/1806596.1806630.
- [9] Sumit Gulwani, Saurabh Srivastava, and Ramarathnam Venkatesan. Program analysis as constraint solving. In *Proceedings of the 2008 ACM SIGPLAN conference on Programming language design and implementation, PLDI '08*, pages 281–292, New York, NY, USA, 2008. ACM. ISBN 978-1-59593-860-2. doi: 10.1145/1375581.1375616.
- [10] Sumit Gulwani, Saurabh Srivastava, and Ramarathnam Venkatesan. Constraint-based invariant inference over predicate abstraction. In *Verification, Model Checking, and Abstract Interpretation (VMCAI)*, volume 5403 of *LNCS*, pages 120–135. Springer, 2009. ISBN 978-3-540-93899-6. doi: 10.1007/978-3-540-93900-9_13.
- [11] George Hagen and Cesare Tinelli. Scaling up the formal verification of Lustre programs with SMT-based techniques. In Alessandro Cimatti and Robert B. Jones, editors, *Formal Methods in Computer-Aided Design (FMCAD)*, pages 109–117. IEEE, 2008.
- [12] Bertrand Jeannet. *Partitionnement dynamique dans l'analyse de relations linéaires et application à la vérification de programmes synchrones*. PhD thesis, Institut National Polytechnique de Grenoble, September 2000.
- [13] Bertrand Jeannet. Dynamic partitioning in linear relation analysis: Application to the verification of reactive systems. *Formal Methods in System Design*, 23:5–37, July 2003. ISSN 0925-9856. doi: 10.1023/A:1024480913162.

- [14] Bertrand Jeannet, Nicolas Halbwachs, and Pascal Raymond. Dynamic partitioning in analyses of numerical properties. In Agostino Cortesi and Gilberto Filé, editors, *Static analysis (SAS)*, volume 1694 of *LNCS*, pages 39–50. Springer, 1999. ISBN 3-540-66459-9. doi: 10.1007/3-540-48294-6_3.
- [15] David Monniaux. Compositional analysis of floating-point linear numerical filters. In Kousha Etessami and Sriram K. Rajamani, editors, *Computer Aided Verification (CAV)*, number 4590 in *LNCS*, pages 199–212. Springer, 2005. ISBN 3-540-27231-3. doi: 10.1007/b138445.
- [16] David Monniaux. Quantifier elimination by lazy model enumeration. In *Computer-aided verification (CAV)*, volume 6174 of *LNCS*, pages 585–599. Springer, 2010. ISBN 3642142958. doi: 10.1007/978-3-642-14295-6_51.
- [17] David Monniaux. Automatic modular abstractions for template numerical constraints. *Logical Methods in Computer Science*, June 2010. ISSN 1860-5974. doi: 10.2168/LMCS-6(3:4)2010.
- [18] Corneliu Popeea and Wei-Ngan Chin. Inferring disjunctive postconditions. In *ASIAN’06*, pages 331–345. Springer, 2007. ISBN 3-540-77504-8.
- [19] Xavier Rival and Laurent Mauborgne. The trace partitioning abstract domain. *ACM TOPLAS*, 29, August 2007. ISSN 0164-0925. doi: 10.1145/1275497.1275501.
- [20] Sriram Sankaranarayanan, Franjo Ivančić, Ilya Shlyahkter, and Aarti Gupta. Static analysis in disjunctive numerical domains. In *Static analysis (SAS)*, volume 4134 of *LNCS*, pages 3–17. Springer, 2006. ISBN 3-540-37756-5.
- [21] Natarajan Shankar. Symbolic analysis of transition systems. In Yuri Gurevich, Philipp W. Kutter, Martin Odersky, and Lothar Thiele, editors, *Abstract State Machines (ASM)*, volume 1912 of *LNCS*, pages 287–302. Springer, 2000. ISBN 3-540-67959-6.
- [22] Rahul Sharma, Işıl Dillig, Thomas Dillig, and Alex Aiken. Simplifying loop invariant generation using splitter predicates. In Ganesh Gopalakrishnan and Shaz Qadeer, editors, *CAV*, volume 6806 of *LNCS*, pages 703–719. Springer, 2011. ISBN 978-3-642-22109-5. doi: 10.1007/978-3-642-22110-1_57.
- [23] Saurabh Srivastava and Sumit Gulwani. Program verification using templates over predicate abstraction. *SIGPLAN Not.*, 44:223–234, June 2009. ISSN 0362-1340. doi: 10.1145/1543135.1542501.