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# Arithmetic over small extension fields via floating point routines

Jean-Guillaume Dumas \*

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## Abstract

We present here an algorithm to perform the arithmetic over small extension field via numerical arithmetic. The idea is to convert the  $X$ -adic representation of modular polynomials, with  $X$  an indeterminate, to a  $q$ -adic representation where  $q$  is a prime power coprime with the field characteristic. With some control on the different involved sizes it is then possible to perform some of the  $q$ -adic arithmetic directly with floating point operators. Depending also on the number of performed numerical operations one must then convert back to the  $q$ -adic or  $X$ -adic representation as to mod out high residues. In this note we present a new version of both conversions. More tabulation and a way to reduce the number of divisions involved in the process are presented and compared to the original version.

## 1 Introduction

The FFLAS/FFPACK project has demonstrated the need of a wrapping of the numerical routines for efficient small finite field linear algebra [1, 2].

A conversion between a modular representation of prime fields and floating points used exactly is quite natural. Now for extension fields (isomorphic to polynomials over a prime field) such a conversion is not direct. In [1] we propose to transform the polynomials into a  $q$ -adic representation where  $q$  is a prime power coprime to the field characteristic. With some care, in particular on the size of  $q$ , it is possible to map the operations in the extension field into the floating point arithmetic realisation of this  $q$ -adic representation.

In this note we still use this scheme but propose some implantation improvements: we propose to make further use of tabulated zech logarithm and give a trick to reduce the number of machine divisions involved. Therefore we recall in section 2 the previous conversion algorithm. We then present our new ideas in section 3 and end the presentation by a comparison.

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## 2 A first algorithm

We want now to use our ATLAS based implementation with non-prime fields. The requirements are to be able to produce a coherent representation of  $\text{GF}(p^k)$  with double precision numbers.

### 2.1 A $q$ -adic representation

We follow here the presentation of [1], we go back to the polynomial arithmetic but in a  $q$ -adic way, with  $q$  a sufficiently big prime or power of a single prime.

Suppose that  $a = \sum_{i=0}^{k-1} \alpha_i X^i$  and  $b = \sum_{i=0}^{k-1} \beta_i X^i$  are two elements of  $\text{GF}(p^k)$  represented by  $\mathbb{Z}/p\mathbb{Z}[X]/Q$ . One can perform the polynomial multiplication  $ab$  via  $q$ -adic numbers. Indeed, by setting  $\tilde{a} = \sum_{i=0}^{k-1} \alpha_i q^i$  and  $\tilde{b} = \sum_{i=0}^{k-1} \beta_i q^i$ , the product is computed in the following maner (we suppose that  $\alpha_i = \beta_i = 0$  for  $i > k - 1$ ):

$$\tilde{ab} = \sum_{j=0}^{2k-2} \left( \sum_{i=0}^j \alpha_i \beta_{j-i} \right) q^j \quad (1)$$

Now if  $q$  is big enough, the coefficient of  $q^i$  will not exceed  $q$ . In this case, it is possible to evaluate  $a$  and  $b$  as floating point numbers, compute the product of these evaluations, and convert back to finite field element, via a  $q$ -adic reconstruction, a division by  $p$  and a division by  $Q$ :

ALGORITHM 2.1. *Dot product over Galois fields via  $q$ -adic conversions to floating point numbers*

**Input** : – a field  $\text{GF}(p^k)$  represented as polynomials mod  $p$  and mod  $Q$ , for  
 $Q$  a degree  $k$  irreducible polynomial over  $\mathbb{Z}/p\mathbb{Z}$ .  
– Two elements  $v_1$  and  $v_2$  of  $\text{GF}(p^k)$  each, as polynomials.  
– a prime power  $q$ .

**Output** : –  $R \in \text{GF}(p^k)$ , with  $R = v_1.v_2$ .

#### Polynomial to $q$ -adic conversion

1 : Set  $\tilde{v}_1$  and  $\tilde{v}_2$  to the floating point vectors of the evaluations at  $q$  of the elements of  $v_1$  and  $v_2$ .  
{Using Horner's formula, for instance}

#### One computation

2 : Compute  $\tilde{r} = \tilde{v}_1 \tilde{v}_2$

#### Building the solution

3 :  $\tilde{r} = \sum_{i=0}^{2k-2} \tilde{\mu}_i q^i$ .

{Using radix conversion, see [3, Algorithm 9.14] for instance}

4 : For each  $i$ , set  $\mu_i = \tilde{\mu}_i \bmod p$

5 : set  $R = \sum_{i=0}^{2k-2} \mu_i X^i \bmod Q$

**THEOREM 2.2.** [1] *Let  $m$  be the number of available mantissa bits within the machine floating point numbers and  $n$  be the number of floating points products  $v_1.v_2$  accumulated, e.g. in a dot product, before the re-conversion. If*

$$q > nk(p-1)^2 \text{ and } (2k-1) \log_2(q) < m,$$

*then Algorithm 2.1 is correct.*

It is shown on [1, Figures 5 & 6] that this wrapping is already a pretty good way to obtain high speed linear algebra over some small extension fields. Even more so with the following optimizations.

## 2.2 Implementation optimizations

For the performance, a first naïve implementation would only give limited speed-up as the conversion cost is then very expensive. However, some simple optimizations were proposed in [1]:

1. The prime power  $q$  can be chosen to be a power of 2. Then the Horner like evaluation of the polynomials at  $q$  (line 1 of algorithm 2.1) is just a left shift. One can then compute this shift with exponent manipulations in floating point arithmetic and use then native C++ `<<` operator as soon as values are within the 32 bits range, or use the native C++ `<<` on 64 bits when available.
2. Some *sparse* primitive polynomials modulo  $p$  can be chosen to build  $\mathbf{GF}(p^k)$ . Then the division (line 5 of algorithm 2.1) can be simplified. The idea is to consider primitive tri or penta-nomials. In this case, proved that less than  $5k$  field operations were required to perform the final reconstruction.

With those optimization we were able to reach high peak performance, quite close to those obtained with prime fields, namely 420 Mop/s on a PIII, 735 MHz, and more than 500 Mop/s on a 64-bit DEC alpha 500 MHz. This is roughly 20 percent below the pure floating point performance and 15 percent below the prime field implementation.

## 3 Improvements

In this paper we propose to improve all the conversion steps of the previous algorithm in order to approach the performance of the prime field wrapping also for several extension fields:

1. Replace the Horner evaluation of the polynomials, to form the  $q$ -adic expansion, by a single table lookup recovering directly the floating point representation.
2. Replace the radix conversion by a single floating point division and some “la Montgomery” reductions.
3. Replace the polynomial division by a single field operation.

More precisely, we propose the following algorithm 3.1:

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ALGORITHM 3.1. *Fast Dot product over Galois fields via  $q$ -adic conversions to floating point numbers*

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**Input** : – a field  $\text{GF}(p^k)$  with elements represented as exponents of a generator of the field.  
– Two elements  $v_1$  and  $v_2$  of  $\text{GF}(p^k)$ .  
– a prime power  $q$  coprime with  $p$ .

**Output** : –  $R \in \text{GF}(p^k)$ , with  $R = v_1.v_2$ .

Tabulated  $q$ -adic conversion

{Use conversion tables from exponent to floating point evaluation}

1 : Set  $\tilde{v}_1$  and  $\tilde{v}_2$  to the floating point vectors of the evaluations at  $q$  of the elements of  $v_1$  and  $v_2$ .

The floating point computation

2 : Compute  $\tilde{r} = \tilde{v}_1 \tilde{v}_2$ ;

Computing a radix decomposition

3 :  $r = \lfloor \tilde{r} \rfloor$ ;

4 :  $rop = \lfloor \frac{\tilde{r}}{p} \rfloor$ ;

5 : **For**  $i = 0$  to  $2k - 2$  **Do**

6 :  $u_i = \lfloor \frac{r}{q^i} \rfloor - p \lfloor \frac{rop}{q^i} \rfloor$ ;

7 : **End For**

Tabulated radix conversion to exponents of the generator

{ $\mu_i$  is such that  $\mu_i = \tilde{\mu}_i \pmod p$  for  $\tilde{r} = \sum_{i=0}^{2k-2} \tilde{\mu}_i q^i$ }

8 : Set  $L = \text{representation}(\sum_{i=0}^{k-1} \mu_i X^i)$ .

9 : Set  $H = \text{representation}(X^k \times \sum_{i=k}^{2k-2} \mu_i X^{i-k})$ .

Reduction in the field

10 : **For**  $j = 1$  to  $n$  **Do**

11 : Compute  $R_j = H_j + L_j \in \text{GF}(p^k)$ ;

12 : **End For**

13 : Return  $R$ ;

---

We prove next that in algorithm 3.1,  $u_i$  satisfies  $u_i = \sum_{j=i}^{2k-2} \mu_j q^{j-i} \pmod p$ . Therefore the representations of  $\sum \mu_i X^i$  in the field can be precomputed and stored in a table where the indexing will be made by  $(u_0, \dots, u_{k-1})$  and  $(u_k, \dots, u_{2k-2})$  and not by the  $\mu_i$ 's\*. Note also that the representation of  $X^k$  can be just  $k$  if the irreducible polynomial used to build  $\text{GF}(p^k)$  is primitive and  $X$  has been chosen as the generator. Finally note that when  $q$  is a power of 2 division by  $q^i$  and flooring is just a right shift.

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\*In practice, indexing by a  $t$ -uple of integers mod  $p$  is made by evaluating at  $p$  e.g. as  $\sum u_i p^i$

THEOREM 3.2. *Algorithm 3.1 is correct.*

We first need the following lemma:

LEMMA 3.3. *For  $r, a, b \in \mathbb{N}$ ,*

$$\left\lfloor \frac{\left\lfloor \frac{r}{b} \right\rfloor}{a} \right\rfloor = \left\lfloor \frac{\left\lfloor \frac{r}{a} \right\rfloor}{b} \right\rfloor$$

PROOF. We proceed by splitting the possible values of  $r$  into intervals  $kab \leq r < (k+1)ab$ . Then  $kb \leq \frac{r}{a} < (k+1)b$  and since  $kb$  is an integer we also have that  $kb \leq \left\lfloor \frac{r}{a} \right\rfloor < (k+1)b$ . Thus  $k \leq \frac{\left\lfloor \frac{r}{a} \right\rfloor}{b} < k+1$  and  $\left\lfloor \frac{\left\lfloor \frac{r}{a} \right\rfloor}{b} \right\rfloor = k$ . Obviously the same is true for the left hand side which proves the lemma.  $\square$

*of theorem 3.2.* First we need to prove that  $0 \leq u_i < p$ . By definition of the truncation, we have  $\frac{r}{q^i} - 1 < \left\lfloor \frac{r}{q^i} \right\rfloor \leq \frac{r}{q^i}$  and  $\frac{r}{pq^i} - 1 - \frac{1}{q^i} < \left\lfloor \frac{rop}{q^i} \right\rfloor \leq \frac{r}{pq^i}$ . Thus  $-1 < u_i < p + \frac{p}{q^i}$ , which is  $0 \leq u_i \leq p$  since  $u_i$  is an integer. We now consider the possible case  $u_i = p$  and show that it does not happen.  $u_i = p$  means that  $\left\lfloor \frac{r}{q^i} \right\rfloor = p(1 + \left\lfloor \frac{rop}{q^i} \right\rfloor) = pg$ . This means that  $r < pgq^i + q^i$ . So that in turns  $rop \leq \frac{r}{p} < gq^i + \frac{q^i}{p}$ . Thus  $\frac{rop}{q^i} < g + \frac{1}{p}$  so that  $\left\lfloor \frac{rop}{q^i} \right\rfloor = g$ . but then from the definition of  $g$  we have that  $g = g - 1$  which is absurd. Therefore  $0 \leq u_i \leq p - 1$ .

Second we show that  $u_i = \sum_{j=i}^{2k-2} \mu_j q^{j-i} \pmod p$ . Well  $u_i = \left\lfloor \frac{r}{q^i} \right\rfloor - p \left\lfloor \frac{\left\lfloor \frac{r}{q^i} \right\rfloor}{p} \right\rfloor$  and thus lemma 3.3 gives that  $u_i = \left\lfloor \frac{r}{q^i} \right\rfloor - p \left\lfloor \frac{\left\lfloor \frac{r}{q^i} \right\rfloor}{p} \right\rfloor$ . The latter is  $u_i = \left\lfloor \frac{\tilde{r}}{q^i} \right\rfloor \pmod p$ . Now, since  $\tilde{r} = \sum_{j=0}^{2k-2} \tilde{\mu}_j q^j$ , we have that  $\left\lfloor \frac{\tilde{r}}{q^i} \right\rfloor = \sum_{j=i}^{2k-2} \tilde{\mu}_j q^{j-i}$ . Therefore, as  $\mu_j = \tilde{\mu}_j \pmod p$ , the equality is proven.

Finally  $R = HX^k + L$  needs to be reduced modulo the irreducible polynomial used to build the field. But, if we are given the representations of  $H$ ,  $X^k$  and  $L$  in the field,  $R$  is still equal to their multiply-add inside the field, directly using the internal representations.  $\square$

## 4 Conclusion

Table 1 gives the respective complexities of the two presented algorithms

	Shift & Add	Accesses	Divisions	Reduction	Memory
Algorithm 2.1	$4k - 2$	0	$2k - 1$	$5k$	$3p^k$
Algorithm 3.1	$4k - 2$	4	1	3	$6p^k$

Table 1: Complexity of the back and forth conversion between extension field and floating point numbers

Now, figure 1 shows only the speed of the conversion after the floating point operations. The log scales prove that for  $q$  ranging from  $2^1$  to  $2^{26}$  (on a 32 bit Pentium IV) our new implantation is two to three times faster than the previous one.

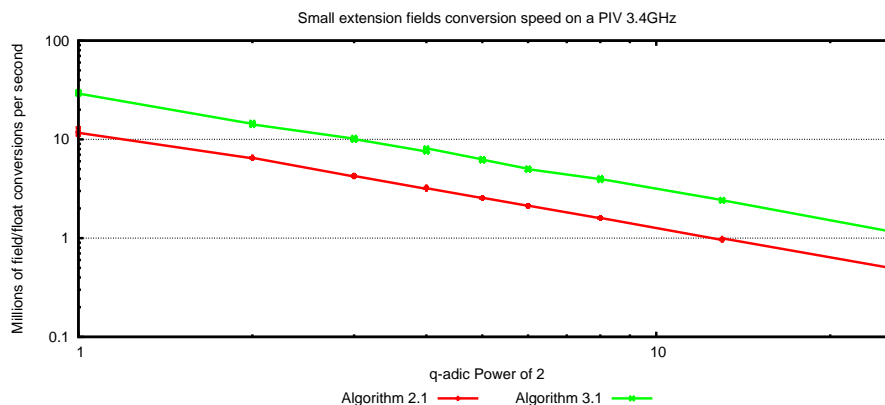


Figure 1: Small extension field conversion speed on a PIV 3.4GHz

This will almost surely improve the running time of linear algebra over some extension fields.

## References

- [1] Jean-Guillaume Dumas, Thierry Gautier, and Clément Pernet. Finite field linear algebra subroutines. In Teo Mora, editor, *Proceedings of the 2002 International Symposium on Symbolic and Algebraic Computation, Lille, France*, pages 63–74. ACM Press, New York, July 2002.
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