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# Parametrization of MIMO multi-controller schemes for bumpless switching

Joseph Yamé, Michel Kinnaert\* and Thomas Delwiche

Abstract—In this paper the realization and implementation of a multi-controller scheme made of a finite set of linear multiple-input-multiple-output controllers, possibly having different state dimensions, is considered. The problem of bumpless switching between the active controller acting in closed-loop and another controller in the set of controllers waiting to take over the control loop is addressed. A non minimal parametrization of the set of controllers such that all controllers share the same state variables is presented, and it is shown that such a parametrization guarantees bumpless switching between any two controllers within the controller set.

#### I. INTRODUCTION

In order to deal with changing operating environments, advanced control systems often consist of hybrid dynamical systems which mix continuous-time behavior with a discrete event based decision mechanism. In such control systems, it is common that the output issued by the controller is temporarily different from the input of the controlled process. This difference is caused by a substitution due to the instantaneous switching between two different control laws. Typical instances of input substitution occur in reconfigurable control or multi-controller schemes such as found in flight control for widening aircraft performance envelope, fault-tolerant systems, multi-mode systems, mode switching from manual to automatic industrial controllers, etc... The discrepancy between the process input and the controller output might lead to performance degradation and even instability of the closed-loop system. In order to alleviate this problem, socalled bumpless transfer techniques have been developed.

Bumpless transfer has often been regarded as similar to the anti-windup problem which has attracted a tremendous research activity for many years (see [1], [3], [6], [8] and references therein). In a simple mode switching, where the aim is to switch from manual control to an ultimate controller in the loop, bumpless transfer is usually unidirectional and it is obtained by adding an anti-windup mechanism to the controller to be switched on. In reference [4], the authors pointed out the need to consider bidirectional bumpless transfer between two controllers in modern industrial control

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systems where it is often the case that a newly designed controller should be temporarily installed and tested during normal process operation. They derived a bumpless transition scheme by recasting the problem into a tracking problem. In that framework, the output of the standby controller achieves tracking of the online control signal by means of a twodegrees-of-freedom controller. Bidirectionality of the bumpless transfer scheme is obtained by adding a symmetrical tracking loop for the other controller. A direct approach to bumpless transfer in a two-controller configuration is also considered in [9] where an optimal linear quadratic control problem aimed at minimizing the discrepancy between the two controller inputs and outputs is formulated. It results in a feedback gain which acts as a "subcontroller" for the standby controller. Bidirectionality is not considered in that reference, but it should be achievable by symmetry. For multi-controller schemes such as found in hybrid or switched mode systems, multidirectional bumpless transfer should be mandatory, and, to achieve this, the common approach consist in appending an anti-windup mechanism to each controller (see for instance [5] and [11]). In [5], this mechanism consists of high gain feedback loops around all idle controllers, which forces the controller outputs to track the process input, whilst, in [11], each controller is augmented with a dynamics identical to that of the plant in order to allow the controller state to evolve in an appropriate way when the controller is not connected to the plant input. Such bumpless transfer schemes become cumbersome and hard to implement when the number of controllers is large. Besides, in the high gain approach, the unstable zeros of the controllers limit the increase of the gain. Moreover, the technique presented in [11] might fail if a sufficiently accurate model of the plant is not available.

Quite surprisingly in all the above mentioned references the concept of bumpless transfer itself has never been precisely formalized. This gap is addressed in [10] and [12] where the concepts of ideal or target state, input and output responses are introduced. However, the considered bumpless transfer problems are different. In [12], the minimization of the gain from the plant state mismatch (namely the deviation from the ideal state) at the switching instant to the  $\mathcal{L}_2$ -norm of the plant output mismatch is achieved. This requires a bumpless transfer filter involving a model of the plant. In [10] the objective is to find a realization of multi-controller scheme so that the plant state mismatch at the switching instant vanishes in specific conditions. No model of the plant is involved in the latter case. The contributions of the present paper are to further formalize the work of [10] and

to generalize it to multiple-input-multiple-output controllers.

Notice that the problem of switching between linear time-invariant (LTI) controllers within a multi-controller scheme, in such a way that the stability of the closed-loop system is assured for any switching sequence, has also been solved by resorting to specific realizations for the controller family in [7]. However, the problem addressed in this paper is different and the resulting realization of the controller family has no link with that of [7]. Indeed, the realization of the latter paper involves the process model, while the realization proposed in this paper exclusively relies on the controller transfer functions. Besides, the equilibrium state of the controller depends of the active controller in the loop in [7], a situation which is avoided here, in order to achieve bumpless switching between the controllers.

The paper is organized as follows. In section II, a discussion and a formal definition of bumpless switching within a multi-controller scheme are given. In section III, a state-space parametrization of the multi-controller is presented and it is proved that it achieves bumpless switching under specific circumstances. In section IV, the proposed controller parametrization is implemented in a simulation in order to illustrate its effectiveness.

# II. PROBLEM STATEMENT

The linear time invariant process described by the following state space model is considered

$$\begin{cases} \dot{x}_P = A_P x_P + B_P u & x_P(t_0) = x_{P0} \\ y = C_P x_P & \end{cases}$$
 (1)

where  $x_P \in \mathbb{R}^{n_x}$  is the plant state,  $u \in \mathbb{R}^m$  is the control input and  $y \in \mathbb{R}^p$  is the measured plant output.

It is controlled by the multi-controller scheme depicted in Figure 1, in which each controller is described as

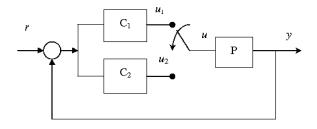


Fig. 1. Switching from controller 1 to controller 2

$$\begin{cases} \dot{x}_i = F_i x_i + G_i(r - y) ; \ x_i(t_0) = x_{i0} \\ u_i = H_i x_i + J_i(r - y) \end{cases}$$
 (2)

where  $x_i$  denotes the state of the  $i^{th}$  controller (i = 1, 2) and  $r \in \mathbb{R}^p$  is the reference signal. It is assumed that both controllers stabilize the process (1), namely the closed-loop systems of Figure 2 (a) and (b) are asymptotically stable.

The control signal u of this switched-mode system is equal either to  $u_1$  or to  $u_2$ , depending on the position of the switch. In this section, a switching from controller  $C_1$  to controller

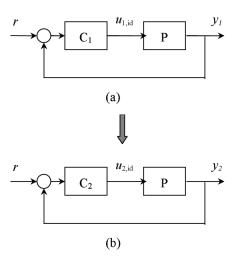


Fig. 2. Closed-loop systems with (a) controller 1 and (b) controller 2

 $C_2$  is considered, without loss of generality. It is supposed to occur at an arbitrary time instant  $t_s > t_0$ .

The design methodologies for the controllers of such a switched-mode system are usually based on the synthesis of the closed-loop systems composed of the individual controllers as depicted in figure 2, with the desire that the plant output y of the switched-mode system in figure 1 be given by the ideal signal  $y_{id}$  obtained by concatenating  $y_1$  on  $[t_0,t_s)$  with signal  $y_2$  on  $[t_s,\infty)$ . To define such an ideal or target response uniquely, it is further assumed that, at time  $t_s$ , system (a) and system (b) in figure 2 have reached their steady state response, namely that any effect of initial state has vanished and that the equilibrium input associated to the reference trajectory is applied since  $t \to -\infty$ . The corresponding outputs for each loop will be denotes  $y_{1,id}$  and  $y_{2,id}$ . Thus  $y_{id}$  is defined as:

$$y_{id}(t) \equiv \begin{cases} y_{1,id}(t) & \text{for } t \in [t_0, t_s) \\ y_{2,id}(t) & \text{for } t \ge t_s \end{cases}$$
 (3)

Such a concept of ideal or target response was introduced in [10], [12]. It requires to consider choices of reference signals  $r(t), t \in (-\infty, \infty)$  such that the solution of the closed-loop system (1),(2) is unique, both for i = 1 and i = 2.

If P denotes the transfer operator of the plant, then the output signal (3) is achieved in the switched-mode system of figure 1 through the following ideal input signal to the plant

$$u(t) = u_{id}(t) \equiv \begin{cases} u_{1,id}(t) & \text{for } t \in [t_0, t_s) \\ u_{2,id}(t) & \text{for } t \ge t_s \end{cases}$$
 (4)

where  $y_{j,id} = Pu_{j,id}, j = 1, 2$ . A key feature of the ideal signal  $u_{id}$  is that its segment on  $[t_s, \infty)$ , i.e. signal  $u_{2,id}$  from  $t_s$  to  $\infty$  is issued from the controller  $C_2$  which has been constantly connected to the plant in closed-loop. However, due to the very structure of the switched-mode system of figure 1, the output of controller  $C_2$  can only be connected to the plant from time  $t_s$ , and consequently its

state trajectory on  $[t_0,t_s)$  has no effect on the evolution of the state of the plant in that time interval. The resulting lack of dynamical consistency between the two state trajectories up to the switching instant turns out to be the main cause of the switching transients which impair the performance of the closed-loop system when controller  $C_2$  becomes active. Bumpless switching should aim at guaranteeing such dynamical consistency.

The actual input signal to the plant of the switched-mode system of figure 1 can be written as a perturbation to the ideal input signal, that is,

$$u(t) = u_{id}(t) + u_{tr}(t) \quad \text{for} \quad t \in [t_0, \infty)$$
 (5)

where  $u_{tr}$  is the transient signal induced by the switching at time  $t_s$ . It is obviously given by  $u_{tr}(t) = 0$  for  $t < t_s$  and  $u_{tr}(t) = u(t) - u_{id}(t)$  otherwise. To see how this switching transient arises, let us consider a state space formalism.

The ideal or target response for  $t \ge t_s$  is the response of the closed-loop system with controller  $C_2$  constantly in the loop, namely,

$$\begin{pmatrix} \dot{x}_2 \\ \dot{x}_P \end{pmatrix} = \begin{pmatrix} F_2 & -G_2C_P \\ B_PH_2 & A_P - B_PJ_2C_P \end{pmatrix} \begin{pmatrix} x_2 \\ x_P \end{pmatrix} + \begin{pmatrix} G_2 \\ B_PJ_2 \end{pmatrix} r \tag{6}$$

$$y = \begin{pmatrix} 0 & C_P \end{pmatrix} \begin{pmatrix} x_2 \\ x_P \end{pmatrix} \tag{7}$$

which is obtained by combining (1) and (2) and substituting  $u_2$  given by the following expression for u in (1)

$$u = u_2 = \begin{pmatrix} H_2 & -J_2 C_P \end{pmatrix} \begin{pmatrix} x_2 \\ x_P \end{pmatrix} + J_2 r \qquad (8)$$

Let  $\xi = \begin{pmatrix} x_2^T & x_P^T \end{pmatrix}^T$  denote the state vector of system (6),(7). The equilibrium state trajectory of the closed-loop system with controller  $C_2$  constantly in the loop, is given by

$$\xi_{id}(t) = \int_{-\infty}^{t} \Phi(t, \tau) r(\tau) d\tau \equiv \Theta(t, -\infty) r(t) \qquad (9)$$

where  $\Phi(t,\tau)$  is the state transition matrix of system (6), and  $\Theta(t,-\infty)$  denotes the integral operator with kernel  $\Phi(t,\tau)$ . It coı̈ncides with the ideal state trajectory for  $t \geq t_s$ . The ideal control and output signals for  $t \geq t_s$  are obtained by substituting (9) for the state vector in (8) and (7).

Let us now turn to the scenario where controller  $C_1$  is in the loop for  $t \in [t_0, t_s)$  and  $C_2$  for  $t \ge t_s$ . When controller  $C_1$  is in the loop, the evolution of the state vectors  $x_2$  and  $x_P$  is obtained from (1) and (2) by concatenating the state of the process and the multi-controller and imposing that  $u = u_1$ , which yields:

$$\begin{pmatrix} \dot{x}_2 \\ \dot{x}_P \\ \dot{x}_1 \end{pmatrix} = \begin{pmatrix} F_2 & -G_2C_P & 0 \\ 0 & A_P - B_P J_1 C_P & B_P H_1 \\ 0 & -G_1 C_P & F_1 \end{pmatrix} \begin{pmatrix} x_2 \\ x_P \\ x_1 \end{pmatrix}$$

$$+ \begin{pmatrix} G_2 \\ B_P J_1 \\ G_1 \end{pmatrix} r \tag{10}$$

Indeed, setting  $\eta = \begin{pmatrix} \xi^T & x_1^T \end{pmatrix}^T$  and solving (10) for the initial condition  $\eta(t_0) = \begin{pmatrix} \xi_0^T, x_{10}^T \end{pmatrix}^T$  yields the state trajectory

$$\eta(t) = \eta(t; t_0, \eta_0, r) \qquad t \in [t_0, t_s) \tag{11}$$

from which the trajectory of  $\xi$  for  $t \in [t_0, t_s)$  when controller  $C_1$  is in the loop can be extracted. This yields  $\xi(t_s^-)$  as the left limit of  $\xi(t)$  at  $t=t_s$ .

Now, for  $t \geq t_s$ , controller  $C_2$  stands in the loop, and the evolution of  $\xi$  is obtained from (6) with initial condition  $\xi(t_s^-)$  at time  $t_s$ , namely

$$\xi(t) = \Phi(t, t_s)\xi(t_s^-) + \Theta(t, t_s)r(t) \qquad t \ge t_s \tag{12}$$

This is to be compared with the evolution of  $\xi$  when controller  $C_2$  is constantly connected to the plant

$$\xi_{id}(t) = \Phi(t, t_s)\xi_{id}(t_s) + \Theta(t, t_s)r(t) \qquad t \ge t_s \tag{13}$$

The switching transient is thus the free motion given by the difference  $\xi_{tr}(t)$  between trajectories (12) and (13), that is

$$\xi_{tr}(t) = \Phi(t, t_s)(\Delta \xi)_{t_s} \qquad t \ge t_s \tag{14}$$

where  $(\Delta \xi)_{t_s} = \xi_{id}(t_s) - \xi(t_s^-)$ , and the associated transient in the input signal to the plant is directly deduced from (8), namely

$$u_{tr}(t) = \begin{pmatrix} H_2 & -J_2 C_P \end{pmatrix} \xi_{tr}(t) \tag{15}$$

It is now clear that no switching transient will occur if  $(\Delta \xi)_{t_s}$ , the mismatch between the ideal and the actual state vector  $\xi$  at time  $t_s$  is zero. This holds true, even if the input experiences a jump  $(\Delta u)_{t_s}=u_2(t_s^+)-u_1(t_s^-) \neq 0$ at the switching instant (the arguments  $t_s^+$  and  $t_s^-$  stand respectively for the right and left limit at  $t_s$  of the corresponding signal). The switching transient results from an inconsistency between the state of the plant  $x_P$  and the state of the controller  $x_2$ . Consistency is assured when the controller  $C_2$  is constantly connected to the plant and it is characterized by vector  $\xi_{id}(t)$ , in which case  $x_P$  depends on  $x_2$  (see (6)). When  $C_1$  is in the loop however, the plant state evolves independently from the state of the idle controller, as can be seen from the zeroed (2,1) entry of the state matrix in equation (10). Since in a switched-mode system, only one controller can be effectively connected to the plant at each time instant, a solution to the bumpless switching problem inspired from the above analysis is to realize a virtual constant connection of all idle controllers to the plant so as to assure the consistency between the state of the plant and the states of all controllers. This approach is the object of the next section.

Based on the above discussion, a formal problem statement for bumpless switching in a switched-mode control system can be stated as follows.

Problem statement: Bumpless switching

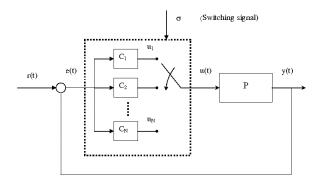


Fig. 3. Switched-mode control system

Consider plant (1) and the set of controllers (2), with  $i=1,\cdots,N$ , corresponding to the switched-mode control system of figure 3. Assume that each of the controllers stabilizes the plant and that there is a unique equilibrium state trajectory associated to the reference trajectory  $r(t), t \in (-\infty, \infty)$  for each of the N controllers. This scheme achieves bumpless switching if the following statement holds for any pair of controllers say  $(j,k), j \neq k, j, k \in \{1,\cdots,N\}$ :

Consider an arbitrary time instant  $t_s$  at which the closed-loop system with controller j as the active controller has reached the equilibrium state trajectory. Upon switching to controller k at time instant  $t_s$ , the state trajectory of the closed-loop system coı̈ncides, for  $t \geq t_s$ , with the equilibrium state trajectory for the closed-loop system with the active controller k.

In the next section, a shared-state parametrization of a switched-mode controller is presented and the resulting switched-mode control system is shown to achieve bumpless switching.

### III. PARAMETRIZATION

Let us rewrite the N control laws in terms of transfer matrices:

$$u_i(s) = C_i(s)e(s) = \frac{\tilde{B}_i(s)}{\tilde{a}_i(s)}e(s) \qquad i = 1, \dots, N \quad (16)$$

where e(s) stands for the control error r(s)-y(s),  $C_i(s)$  is a  $m \times p$  transfer matrix,  $\tilde{B}_i(s)$  is a  $m \times p$  polynomial matrix and  $\tilde{a}_i(s)$  is a monic polynomial constructed as the smallest common multiple of all the denominators of the elements of  $C_i(s)$ . Set  $n = \max_{i=1,N} deg(\tilde{a}_i(s))$  where  $deg(\tilde{a}_i(s))$  denotes the degree of polynomial  $\tilde{a}_i(s)$ . Then all transfer matrices  $C_i(s)$  can always be written in the form

$$C_i(s) = \frac{B_i(s)}{a_i(s)}$$

where the denominator polynomials  $a_i(s), i = 1, \dots, N$  are monic polynomials of degree n, namely

$$a_i(s) = s^n + a_{i,1}s^{n-1} + \dots + a_{i,n-1}s + a_{i,n}$$

and the numerator polynomial matrix can be written

$$B_i(s) = B_{i,0}s^n + B_{i,1}s^{n-1} + \dots + B_{i,n}.$$

All additional roots of  $a_i(s)$  with respect to the roots of  $\tilde{a}_i$  should be placed in the open-left half plane. These axtra roots are needed to obtain a shared-state parametrization for all controllers, as will be seen below. They do not play any role on the input-output dynamics of the closed-loop system.

Each controller transfer matrix can be separated in a direct feedthrough part  $B_{i,0}$  and a strictly proper part  $C'_i(s)$ :

$$C_i(s) = C_i'(s) + B_{i,0}$$

The  $i^{th}$  controller output can be written as

$$u_i(s) = u'_i(s) + B_{i,0}e(s)$$
 (17)

where  $u_i'(s)$  is defined by

$$u_{i}'(s) = \frac{B_{i}(s) - B_{i,0}a_{i}(s)}{a_{i}(s)} e(s) = \frac{B_{i}'(s)}{a_{i}(s)} e(s)$$

$$= \frac{B_{i,1}'s^{n-1} + \dots + B_{i,n}'}{s^{n} + a_{i,1}s^{n-1} + \dots + a_{i,n-1}s + a_{i,n}} e(s)$$
(18)

Let  $\lambda(s)$  be a Hurwitz monic polynomial of order n such that  $\lambda(s) \neq a_i(s), i = 1, \dots, N$ . Then (18) can be rewritten in a feedback form (see Figure 4) as follows

$$u_i'(s) = \frac{B_i'(s)}{\lambda(s)}e(s) + \frac{\lambda(s) - a_i(s)}{\lambda(s)}u_i'(s) \tag{19}$$

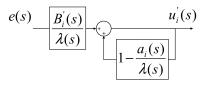


Fig. 4. Feedback loop described by (19)

For notation convenience, define

$$d_{i}(s) = \lambda(s) - a_{i}(s)$$

$$= (\lambda_{1} - a_{i,1})s^{n-1} + (\lambda_{2} - a_{i,2})s^{n-2}$$

$$+ \dots + (\lambda_{n} - a_{i,n})$$

$$= d_{i,1}s^{n-1} + \dots + d_{i,n}$$

and introduce the constant matrices

$$\mathcal{E}_i = (B'_{i,n} \quad B'_{i,n-1} \cdots B'_{i,1})$$

and

$$\mathcal{D}_i = (d_{i,n}I_m \quad d_{i,n-1}I_m \cdots d_{i,1}I_m).$$

Equation (19) can be expressed as:

$$u_{i}'(s) = (B_{i,n}' \quad B_{i,n-1}' \cdots B_{i,1}') \begin{pmatrix} I_{p} \\ s \quad I_{p} \\ \vdots \\ s^{n-1}I_{p} \end{pmatrix} \frac{1}{\lambda(s)} e(s)$$

$$+ (d_{i,n}I_{m} \quad d_{i,n-1}I_{m} \cdots d_{i,1}I_{m}) \begin{pmatrix} I_{m} \\ s \quad I_{m} \\ \vdots \\ s^{n-1}I_{m} \end{pmatrix} \frac{1}{\lambda(s)} u_{i}'(s)$$

$$= \mathcal{E}_{i}\mathcal{F}_{p}(s)e(s) + \mathcal{D}_{i}\mathcal{F}_{m}(s)u_{i}'(s) \tag{20}$$

where  $I_m$  is the  $m \times m$  identity matrix,

$$\mathcal{F}_m(s) = \begin{pmatrix} I_m \\ sI_m \\ \vdots \\ s^{n-1}I_m \end{pmatrix} \frac{1}{\lambda(s)}$$
 (21)

and a similar definition holds for  $\mathcal{F}_n(s)$ .

It is straightforward to prove (see e.g. [2]) that the transfer matrix  $\mathcal{F}_m(s)$  can be expressed in state space form as

$$\mathcal{F}_m(s) = (sI - \mathcal{A}_m)^{-1} \mathcal{B}_m \tag{22}$$

where  $A_m$  is in the following block companion form

$$\mathcal{A}_{m} = \begin{pmatrix} 0 & I_{m} & 0 & \cdots & \cdots & 0 \\ 0 & 0 & I_{m} & 0 & \cdots & 0 \\ \vdots & \vdots & & \ddots & \ddots & \vdots \\ 0 & 0 & \cdots & \cdots & 0 & I_{m} \\ -\lambda_{n}I_{m} & -\lambda_{n-1}I_{m} & \cdots & \cdots & -\lambda_{1}I_{m} \end{pmatrix}$$

and

$$\mathcal{B}_m = \left(\begin{array}{c} 0\\ \vdots\\ 0\\ I_m \end{array}\right)$$

Introducing (20) into (17) yields the following expression for the  $i^{th}$  control law:

$$u_i(s) = \mathcal{E}_i \mathcal{F}_p(s) e(s) + \mathcal{D}_i \mathcal{F}_m(s) u_i'(s) + B_{i,0} e(s)$$
 (23)

Now, let us define the state variables  $\zeta_1(s) = \mathcal{F}_p(s)e(s)$ and  $\zeta_2(s) = \mathcal{F}_m(s)u_i'(s)$ . By resorting to (22) a state space representation of the control law (23) is obtained:

$$\begin{pmatrix} \dot{\zeta}_{1}(t) \\ \dot{\zeta}_{2}(t) \end{pmatrix} = \begin{pmatrix} \mathcal{A}_{p} & 0 \\ 0 & \mathcal{A}_{m} \end{pmatrix} \begin{pmatrix} \zeta_{1}(t) \\ \zeta_{2}(t) \end{pmatrix}$$
(24)
$$+ \begin{pmatrix} \mathcal{B}_{p} & 0 \\ 0 & \mathcal{B}_{m} \end{pmatrix} \begin{pmatrix} e(t) \\ u'_{i}(t) \end{pmatrix}$$
$$u_{i}(t) = \mathcal{E}_{i}\zeta_{1}(t) + \mathcal{D}_{i}\zeta_{2}(t) + B_{i,0}e(t)$$
(25)

The desired parametrization is obtained by substituting  $u_i(t) - B_{i,0}e(t)$  for  $u_i'(t)$  in the state equation, and then substituting u(t) for  $u_i(t)$ . The latter substitution does not change the control law if the  $i^{th}$  controller is the active controller ( $\sigma(t) = i$ ). However, if this is not the case, it modifies the controller state in such a way that it only depends on the active controller at each time instant. This is actually one of the keys to achieve bumpless switching. The final parametrization for the  $i^{th}$  controller is described by

$$\begin{pmatrix} \dot{\zeta}_{1}(t) \\ \dot{\zeta}_{2}(t) \end{pmatrix} = \begin{pmatrix} \mathcal{A}_{p} & 0 \\ 0 & \mathcal{A}_{m} \end{pmatrix} \begin{pmatrix} \zeta_{1}(t) \\ \zeta_{2}(t) \end{pmatrix}$$

$$+ \begin{pmatrix} \mathcal{B}_{p} & 0 \\ -\mathcal{B}_{m}B_{0,\sigma(t)} & \mathcal{B}_{m} \end{pmatrix} \begin{pmatrix} e(t) \\ u(t) \end{pmatrix}$$

$$u_{i}(t) = \mathcal{E}_{i}\zeta_{1}(t) + \mathcal{D}_{i}\zeta_{2}(t) + B_{i,0}e(t)$$

$$(26)$$

where  $\sigma(t)$  indicates the controller which is presently active in the loop.

Notice that the multi-controller itself can now be viewed as a system with input e and output u described by

(21) 
$$\begin{cases} \dot{\zeta}(t) = \mathcal{A}\zeta(t) + \mathcal{B}_{u}u(t) + \mathcal{B}_{e}e(t) \\ u_{i}(t) = \mathcal{C}_{i}\zeta(t) + B_{i,0}e(t) & i = 1, 2, \dots, N \\ u(t) = u_{\sigma(t)}(t) \end{cases}$$
(28)

where  $\zeta(t) = \begin{pmatrix} \zeta_1^T(t) & \zeta_2^T(t) \end{pmatrix}^T$  and

$$\mathcal{A} = \begin{pmatrix} \mathcal{A}_p & 0 \\ 0 & \mathcal{A}_m \end{pmatrix} \qquad \mathcal{B}_e = \begin{pmatrix} \mathcal{B}_p \\ -\mathcal{B}_m B_{0,\sigma(t)} \end{pmatrix}$$
$$\mathcal{B}_u = \begin{pmatrix} 0 \\ \mathcal{B}_m \end{pmatrix} \qquad \mathcal{C}_i = \begin{pmatrix} \mathcal{E}_i & \mathcal{D}_i \end{pmatrix}$$

The main property of this parametrization is summarized in the following theorem.

Theorem 1: Consider the switched-mode system of figure

- 1) each controller is a stabilizing controller for plant (1),
- 2) perfect tracking of the reference is achieved asymptotically for each controller,
- 3) for the given plant, there is a unique control trajectory associated to the reference trajectory at equilibrium,

then the parametrization (28) achieves bumpless switching.

Proof 1: Let us consider an arbitrary controller pair  $(j,k), j \neq k, j, k \in \{1,2,\cdots N\}$ . Our aim is to show that, starting from an equilibrium situation in which controller j is in the loop, upon switching to controller k the closed-loop system instantaneously follows the ideal trajectory associated to that controller. Given hypothesis 2, the ideal (equilibrium) trajectory of the plant output coïncides with the reference signal r which yields a zero tracking error e. The equilibrium state and control signal with controller k in the loop thus fulfill

$$\dot{\zeta}_{id} = \mathcal{A}\zeta_{id} + \mathcal{B}_u u_{id} \tag{29}$$

$$u_{id} = \mathcal{C}_k \zeta_{id} \tag{30}$$

$$u_{id} = C_k \zeta_{id}$$

$$\dot{x}_{P,id} = A_P x_{P,id} + B_P u_{id}$$
(30)
(31)

$$r = C_P x_{P,id} (32)$$

Due to hypothesis 3,  $u_{id}$  is uniquely defined by the last two equations. This input trajectory is associated to a unique trajectory for the state of the controller at equilibrium obtained by solving (29):

$$\zeta_{id}(t) = \int_{-\infty}^{t} e^{\mathcal{A}(t-\tau)} \mathcal{B}_{u} u_{id}(\tau) d\tau$$
 (33)

This state trajectory is independent of the particular controller which lies in the loop, since A and  $B_u$  are common to all controllers. Therefore  $\zeta_{id}(t)$  is also the state trajectory reached at equilibrium when any controller, and in particular the  $j^{th}$ 's one, lies in the closed-loop. Since all controllers reach the same ideal state at equilibrium, bumpless switching is achieved.

Remark 1: Hypothesis 2 can be fulfilled by resorting to the internal model principle in the design of each controller

Remark 2: A common particular case is the tracking of a piece-wise constant reference signal for a square (m = p) process with a non-singular steady state gain. In this situation the ideal process input is directly computed from

$$u_{id} = P(0)^{-1}r,$$

and the ideal state trajectory for the controller results from (29) by setting the derivative to zero

$$\zeta_{id} = -\mathcal{A}^{-1}\mathcal{B}_u u_{id}$$

Remark 3: The parametrization of the set of controllers obtained in continuous time extends *mutatis mutandis* to the discrete time setting, and the statement of the bumpless switching problem remains valid in that setting.

#### IV. EXAMPLE

As a simple example illustrating the bumpless switching property of the proposed parametrization for a multicontroller scheme, consider a fluid mixing plant depicted in figure 5. This plant is made of three tanks. A first one contains colored water (coloring concentration denoted by  $C_c(t)$ ) and a second one contains clear water. These two tanks are connected to a third one (the mixer) where the mixing of the clear and colored water takes place. The aim is to achieve a specific level h(t) and concentration  $C_o(t)$ in the mixer by acting on the flows of colored and clear water (denoted by  $Q_1(t)$  and  $Q_2(t)$  respectively). Mixing is supposed to be perfect and to take place instantaneously. Besides, the output flow  $Q_o(t)$  and input concentration  $C_c(t)$ are assumed to be fixed at known nominal values  $Q_o^*$  and  $C_c^*$ . Here, the asterisk indicates values corresponding to the nominal operating point, for which the numerical values are given in table I. A state-space model of the mixer is easily deduced from material conservation laws. Linearizing the resulting equations around the indicated operating point yields expressions (34) and (35) where S denotes the section of the cylindrical mixer (equal to  $0.01 m^2$ ). By an abuse of notation, the same symbols are used to denote the original variables and their deviation with respect to the operating point.

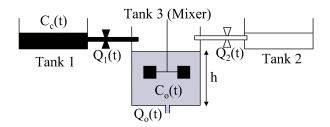


Fig. 5. Fluid mixing plant.

$$\begin{pmatrix} \dot{h}(t) \\ \dot{C}_{o}(t) \end{pmatrix} = \begin{pmatrix} 0 & 0 \\ 0 & -\frac{Q_{o}^{*}}{Sh^{*}} \end{pmatrix} \begin{pmatrix} h(t) \\ C_{o}(t) \end{pmatrix}$$
(34)
$$+ \begin{pmatrix} \frac{1}{S} & \frac{1}{S} \\ \frac{C_{c}^{*} - C_{o}^{*}}{Sh^{*}} & \frac{-C_{o}^{*}}{Sh^{*}} \end{pmatrix} \begin{pmatrix} Q_{1}(t) \\ Q_{2}(t) \end{pmatrix}$$

$Q_o^*$	$0.015 \ l/s$
$h^*$	$0.15 \ m$
$Q_1^*$	$0.0075 \ l/s$
$Q_2^*$	$0.0075 \ l/s$
$C_c^*$	0.3
$C_o^*$	0.15

TABLE I

NUMERICAL VALUES ASSOCIATED TO THE OPERATING POINT.

$$\begin{pmatrix} h(t) \\ C_o(t) \end{pmatrix} = \begin{pmatrix} 1 & 0 \\ 0 & 1 \end{pmatrix} \begin{pmatrix} h(t) \\ C_o(t) \end{pmatrix}$$
 (35)

Two decoupling controllers were designed for this system. They are characterized by the following discrete transfer matrices (sampling period  $0.02\ s$ )

$$C_1(z) = \begin{pmatrix} 0.005 & \frac{0.0025z - 0.0024995}{20.005} \\ 0.005 & \frac{-0.0025z + 0.0024995}{z - 1} \end{pmatrix}$$
 (36)

$$C_2(z) = \begin{pmatrix} \frac{0.019595z - 0.019205}{z - 1} & \frac{0.00275z - 0.00225}{z - 1} \\ \frac{0.019595z - 0.019205}{z - 1} & \frac{-0.00275z + 0.00225}{z - 1} \end{pmatrix}$$
(37)

After parametrization, the state equations of these two controllers are given in discrete time by expressions (38) if controller  $C_1$  is connected in the loop and (39) if controller  $C_2$  is connected in the loop. The output of controllers  $C_1$  and  $C_2$  are given respectively by expressions (40) and (41).

$$\begin{pmatrix} \zeta_{1}(k+1) \\ \zeta_{2}(k+1) \end{pmatrix} = \begin{pmatrix} 0.5 & 0 & 0 & 0 \\ 0 & 0.5 & 0 & 0 \\ 0 & 0 & 0.5 & 0 \\ 0 & 0 & 0 & 0.5 \end{pmatrix} \begin{pmatrix} \zeta_{1}(k) \\ \zeta_{2}(k) \end{pmatrix}$$

$$+ \begin{pmatrix} 1 & 0 & 0 & 0 \\ 0 & 1 & 0 & 0 \\ -0.005 & -0.0025 & 1 & 0 \\ -0.005 & 0.0025 & 0 & 1 \end{pmatrix} \begin{pmatrix} e(k) \\ u(k) \end{pmatrix}$$

$$\begin{pmatrix} \zeta_{1}(k+1) \\ \zeta_{2}(k+1) \end{pmatrix} = \begin{pmatrix} 0.5 & 0 & 0 & 0 \\ 0 & 0.5 & 0 & 0 \\ 0 & 0 & 0.5 & 0 \\ 0 & 0 & 0 & 0.5 \end{pmatrix} \begin{pmatrix} \zeta_{1}(k) \\ \zeta_{2}(k) \end{pmatrix}$$

$$+ \begin{pmatrix} 1 & 0 & 0 & 0 \\ 0 & 1 & 0 & 0 \\ -0.019595 & -0.00275 & 1 & 0 \\ -0.019595 & 0.00275 & 0 & 1 \end{pmatrix} \begin{pmatrix} e(k) \\ u(k) \end{pmatrix}$$

$$u_{1}(k) = \begin{pmatrix} 0 & 0.0000005 \\ 0 & -0.0000005 \end{pmatrix} \zeta_{1}(k) + \begin{pmatrix} 0.5 & 0 \\ 0 & 0.5 \end{pmatrix} \zeta_{2}(k)$$

$$+ \begin{pmatrix} 0.005 & 0.0025 \\ 0.005 & -0.0025 \end{pmatrix} e(k)$$

$$(40)$$

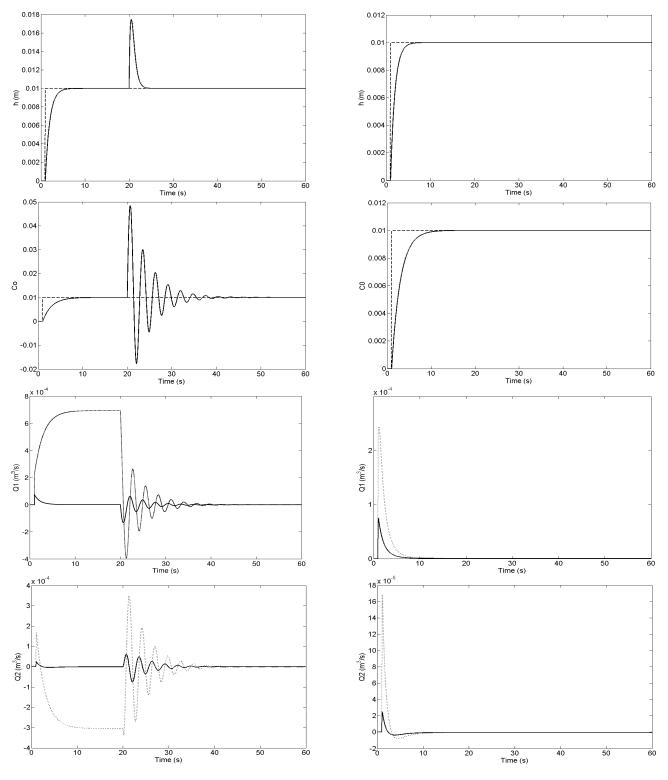


Fig. 6. Inputs and outputs of the plant without parametrization. In the first two figures of this column, the interrupted line represents the reference step. In the remaining figures, the full line represents the output of  $C_1$  and the dotted line the output of  $C_2$ .

Fig. 7. Inputs and outputs of the plant with parametrization. In the first two figures of this column, the interrupted line represents the reference step. In the remaining figures, the full line represents the output of  $C_1$  and the dotted line the output of  $C_2$ .

$$u_{2}(k) = \begin{pmatrix} 0.00039 & 0.0005 \\ 0.00039 & -0.0005 \end{pmatrix} \zeta_{1}(k) + \begin{pmatrix} 0.5 & 0 \\ 0 & 0.5 \end{pmatrix} \zeta_{2}(k) + \begin{pmatrix} 0.019595 & 0.00275 \\ 0.019595 & -0.00275 \end{pmatrix} e(k)$$

Figures 6 and 7 show the process inputs and outputs during a simulation. The reference is initially equal to zero. At t=1s, a reference step of magnitude 0.01 is imposed to both outputs. First, controller  $C_1$  is in the loop, then at t=20s, it is replaced by controller  $C_2$  ( $u(t)=u_1(t)$  for t<20s and  $u(t)=u_2(t)$  for  $t\geq20s$ ). Note that without parametrization (figure 6), the control system experiences an undesirable transient which significantly deteriorates the tracking performance after switching. With the proposed controller parametrization (figure 7), the multi-controller shows successful bumpless switching without any transients after switching and it is worth noticing how the pending controller output tracks the actual process input.

#### V. CONCLUSIONS

In this paper, bumpless switching in the case of multicontrollers schemes was achieved by using a specific statespace parametrization of the set of controllers.

First, bumpless switching was defined formally in terms of a given response of the system. This response corresponds to the ideal response that would be obtained if all controllers were physically and constantly connected to the plant in closed-loop, providing dynamical consistency between the state trajectory of the controllers and the state trajectory of the plant. Based on this analysis, a solution to the bumpless switching appeared to be a constant virtual connection between all idle controllers and the physical plant.

Next, a state-space parametrization of the set of controller assuring this virtual connection was obtained. It was demonstrated that this parametrization achieves bumpless switching under the following hypothesis:

- 1) each controller is a stabilizing controller for the plant
- perfect tracking of reference is achieved asymptotically for each controller
- 3) for the given plant, there is a unique control trajectory associated to the reference trajectory at equilibrium

Finally, an example was presented in order to illustrate the effectiveness of the proposed method for bumpless switching.

## REFERENCES

- K.J. Aström and B. Wittenmark. Computer -Controlled Systems: Theory and Design, Prentice-Hall, Engelwood Cliffs, New Jersey, 1984
- [2] C-T. Chen. *Linear System Theory and Design*, Sauders College Publishing, New York, 1984.
- [3] A.H. Glattfelder et al. (Editors). Anti-Windup Control, European Journal of Control, Special Issue, vol. 6, no. 5, 2000.
- [4] S.F. Graebe and A.L.B. Ahlén. Dynamic Transfer Among Alternative Controllers and Its relation to Anti-windup Controller design, IEEE Transactions on Control Systems Technology, vol.4, no.1, pp.92-99, 1996.
- [5] M. Green and D.J.N. Limebeer. *Linear Robust Control Prentice-Hall Inc.*, Engelwood Cliffs, New Jersey, 1995.

- [6] R. Hanus, M. Kinnaert and J.L. Henrotte. Conditionning Technique, a General Anti-Windup and Bumpless Transfer Method, Automatica, vol. 23, no. 6, pp. 729-739.
- [7] J.P. Hespanha and A.S. Morse. Switching between stabilizing controllers, Automatica, 38, pp 1905-1917, 2002.
- [8] Y. Peng, D. Vrancic and R. Hanus. Anti-Windup, Bumpless and Conditioned Transfer Techniques for PID Controllers, IEEE Control Systems Magazine, vol. 10, no. 4, pp. 48-57, 1996.
- [9] M.C. Turner and D.J. Walker. *Linear Quadratic Bumpless Transfer*, Automatica, vol. 36, pp. 1089-1101, 2000.
- [10] J. Yamé and M. Kinnaert. Parametrization of Linear Controllers for Bumpless Switching in Multi-Controller Schemes, AIAA Guidance, Navigation, and Control Conference and Exhibit, Providence, Rhode Island, 2004.
- [11] L. Zaccarian and A.R. Teel. A Common Framework for Anti-Windup, Bumpless Transfer and Reliable Designs, Automatica, vol. 38, pp. 1734-1744, 2002.
- [12] L. Zaccarian and A.R. Teel. The L<sub>2</sub> (l<sub>2</sub>) bumpless transfer problem for linear plants: Its definition and solution, Automatica, vol. 41, pp. 1273-1280, 2005.