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## To cite this version:

Virgile Mogbil. Non-deterministic Boolean Proof Nets. First International Workshop Foundational and Practical Aspects of Resource Analysis (FOPARA 2009), Nov 2009, Eindhoven, Netherlands. pp. $131-145,10.1007 / 978-3-642-15331-0$. hal-00122981v3

## HAL Id: hal-00122981 <br> https://hal.science/hal-00122981v3

Submitted on 5 Jan 2010

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# Non-deterministic Boolean Proof Nets 

(rapport interne LIPN - Novembre 2009)

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#### Abstract

We introduce Non-deterministic Boolean proof nets to study the correspondence with Boolean circuits, a parallel model of computation. We extend the cut elimination of Non-deterministic Multiplicative Linear logic to a parallel procedure in proof nets. With the restriction of proof nets to Boolean types, we prove that the cut-elimination procedure corresponds to Non-deterministic Boolean circuit evaluation and reciprocally. We obtain implicit characterization of the complexity classes NP and NC (the efficiently parallelizable functions).


## 1 Introduction

The proof nets [Gir87,DR89] of the Linear logic (LL) are a parallel syntax for logical proofs without all the bureaucracy of sequent calculus. Their study is also motivated by the well known Curry-Howard isomorphism: there is a correspondence between proofs and programs which associates cut-elimination in proofs and execution in functional programs. Proof nets were used in subsystems of LL to give Curry-Howard characterizations of complexity classes. Usually this is done in LL by reducing the deductive power of the exponentials connectives, also known as modalities, controlling duplication in cut-elimination process. The most known restrictions characterize $P$, the class of decision problems which can be solved in polynomial time. E.g. it is the case of the Intuitionistic Light Affine Logic (ILAL). By expressing non-determinism by an explicit rule to choose between proofs of the same formula, the Non-deterministic extension of ILAL characterizes quite naturally $N P$ [Mau03]. This sum rule is a logical counterpart to non-deterministic choice in process calculi. With proof nets, other characterizations were given by correspondence with models of parallel computation like Boolean circuits as we shall see later. In this paper we make use of the sum rule to extend those proof nets to a non-deterministic framework.

Boolean circuits are a standard models of parallel computation [Coo85,BS90] [Vol99]. Several important complexity classes are defined in terms of Boolean circuits. E.g. $N C$ can be thought of as the problems that can be efficiently solved on a parallel computer just as the class $P$ can be thought of as the tractable problems. Because a Boolean circuit has a fixed input size, an infinite family of Boolean circuits is needed to do computations on arbitrary inputs. With a

[^0]uniformity property, a family can be however regarded as an implementation of an algorithm. The Boolean circuit depth is the time on a parallel computer where the size is the number of processors. For instance $N C$ is the set of Boolean functions computable by uniform Boolean circuits of polynomial size and polylogarithmic depth.

By restricting proved formulas and with a logical depth notion, there is a proofs-as-programs correspondence between proof nets and $N C$ such that both size and depth are preserved [Ter04]. In this setting, the proof nets allow among others to simulate gates by higher order. Here we consider a nondeterministic extension of proof nets, to give a proof-as-programs correspondence with $N N C(k(n))$, the class defined from $N C$ Boolean circuits with $O(k(n))$ nondeterministic variables. In particular $N C=N N C(\log n)$ and $N P=N N C($ poly $)$. So the Curry-Howard isomorphism for parallel model of computation gives us new tools to study theoretical implicit complexity.

In section 2 we present $M L L_{u}$, the multiplicative LL with arbitrary arity This is the smallest fragment of LL that encodes the unbounded fan-in Boolean circuits [Ter04]. We recall the reduction steps of the cut-elimination. We give its non-deterministic extension $n M L L_{u}$ and proof nets for it as in [Mau03]. We recall several size and depth notions used in the proofs, all are natural graph theoretic notions. In section 3 we mostly analyze and define a new cut elimination from a parallel point of view. We prove the central theorems which allow us to establish the results on the complexity classes. In section 4 we recall Boolean circuit definitions. They include uniformity of both proof net families and circuit families as well as hierarchy of $N C$ and $N N C()$. We introduce the Boolean proof nets of $n M L L_{u}$, i.e with sum-boxes, which generalize the Boolean proof nets of $M L L_{u}$. In section 5 we apply the previous theorems to our Boolean proof nets with sum-boxes and we establish a proofs-as-programs correspondence with $N N C()$ Boolean circuits. They are translation and simulation theorems that preserve both size and depth of the Boolean proof nets and Boolean circuits. Finally in section 6 we summarize the obtained results via $U B P N()$, a hierarchy of proof net complexity classes defined analogously to the $N N C()$ hierarchy. The classes $U B P N($ poly $), U B P N($ polylog $)$ and $U B P N(1)$ of uniform Boolean proof net families with respectively $n^{O(1)}, \log { }^{O(1)} n$ and $O(1)$ sum-boxes are respectively equal to $N P, N N C($ polylog $)$ and $N C$.

## 2 Non-deterministic Linear logic

### 2.1 Formulas, Sequent calculus and cut-elimination

We write $\vec{A}$ (respectively $\overleftarrow{A}$ ) for an ordered sequence of formulas $A_{1}, \ldots, A_{n}$ (resp. $A_{n}, \ldots, A_{1}$ ).

The formulas of $M L L_{u}$ and $n M L L_{u}$ are built on literals by conjunction $\otimes^{n}(\vec{A})$ and disjunction $>^{n}(\overleftarrow{A})$ of Multiplicative Linear logic but with unbounded arities $n \geqslant 1$. The only difference with binary connectives is that this gives us depth-efficient proofs [DR89]. The negation of a non-literal formula is defined by

De Morgan's duality: $\left(\otimes^{n}(\vec{A})\right)^{\perp}=\gamma^{n}\left(\overleftarrow{A}^{\perp}\right)$ and $\left(\gamma^{n}(\vec{A})\right)^{\perp}=\otimes^{n}\left(\overleftarrow{A}^{\perp}\right)$ where the negation applies to sequences of formulas as expected.

The sequents of $n M L L_{u}$ are of the form $\vdash \Gamma$, where $\Gamma$ is a multiset of formulas. The rules of the $n M L L_{u}$ sequent calculus are described in Fig.1. As in Linear logic sequent calculus, $M L L_{u}$ and $n M L L_{u}$ admit a cut-elimination theorem (Hauptsatz) and implicit exchange. The cut-elimination steps of $n M L L_{u}$

$$
\left.\begin{array}{rl|l} 
& \quad \frac{\vdash \Gamma, A \vdash \Delta, A^{\perp}}{\vdash \Gamma, \Delta} \text { cut } & \stackrel{\vdash A, A^{\perp}}{ } \text { (ax } \\
& \frac{\vdash \Gamma_{1}, A_{1} \cdots \cdot \vdash \Gamma_{n}, A_{n}}{\vdash \Gamma_{1}, \ldots, \Gamma_{n}, \otimes^{n}(\vec{A})} \otimes^{n} \frac{\vdash \Gamma, A_{n}, \ldots, A_{1}}{\vdash \Gamma, 8^{n}(\overleftarrow{A})}>^{n}
\end{array} \right\rvert\, \quad \text { (b) } \frac{\vdash \Gamma \ldots \vdash \Gamma}{\vdash \Gamma} \text { sum }
$$

Fig. 1. Sequent calculus rules: (a) $M L L_{u}$ and (a+b) $n M L L_{u}$
sequent calculus are $n$-ary versions of the Linear logic proofs rewriting: an axiom cut (i.e. a cut rule with an axiom premise) rewrites without the axiom rule, and the multiplicative cut (i.e. a cut rule between multiplicative formulas) rewrites in sequence of cuts between premises. In Fig. 2 we give the sum-rule cut-elimination step as in [Mau03].

Fig. 2. $n M L L_{u}$ sum-rule cut-elimination step

### 2.2 Proof nets and reduction rules

We suppose the reader familiar with proof nets and more specifically with the reduction rules corresponding to the cut-elimination steps [Gir96].

A proof net [Gir87,DR89] is a graph that just keeps the structure of sequent calculus proofs without some computational irrelevant part. It is a set of connected links that we build by inference from the sequent calculus rules. There


Fig. 3. $a x$-link, $\otimes$-link, $\ngtr$-link and $\square$-link ; small proof nets
are several sorts of links: $a x$-link, $\otimes$-link and $\ngtr$-link corresponding respectively
to $M L L_{u}$ rules. Every link has several ports numbered by convention as in Fig.3. This numbering is omitted when there is no ambiguity. The conclusion port, also called conclusion of the link, is the port numbered 0 . We use a wire between two conclusion ports rather than a cut-link that corresponds to sequent calculus cutrule. Now we focus us on the $n M L L_{u}$ proof nets which extend those of $M L L_{u}$. As we only consider proof nets inferred from sequent calculus, we can inductively built them in a equivalent way from the $a x$-links with the constructors of Fig. 4. The sequent calculus sum-rule corresponds to a box (Fig.4), called sum-box, enclosing proof nets, called sub-nets, such that we associate one $\square$-link for each shared conclusion.


Fig. 4. Proof net constructors: $\otimes$-link, $>$-link, cut and sum-box

Definition 1. A proof net is (i) a finite set $L$ of links, (ii) a function $\sigma$ : $L \rightarrow\{\bullet, \square\} \cup\left\{\otimes^{n}, \otimes^{n}\right\}_{n \geqslant 1}$ giving the sort of each link, (iii) a symmetric binary relation over $L \times \mathbb{N}$ for each wire between two ports of distinct links, and (iv) a function $\tau: L \rightarrow \mathbb{N}$ which associates to each link a unique number corresponding to the sum-box that encloses it.

A summand of a proof net is a proof net obtained by erasing all but one sub-net in every sum-box.

In section 4 we give a Logspace description which extends this one. We call the conclusions of a proof net the set of links whose conclusion ports are unrelated with other link ports. We write proof nets either with graphs or just with the link names. For instance the last proof net in Fig. 4 is $\operatorname{sum}\left(S_{1}, \ldots, S_{n}\right)$.

Remark that in every summand of a proof net the $\square$-links correspond to superfluous sum-rules with only one premise. So every summand of a proof net of $n M L L_{u}$ easily induces a proof net of $M L L_{u}$.

Definition 2. The reduction rules ${ }^{1}$ for $M L L_{u}$ proof nets, called respectively axreduction and $m$-reduction, are defined by:

$$
\operatorname{cut}\left(\operatorname{ax}\left(A, A^{\perp}\right), A\right) \rightarrow_{a x} A \text { and } \operatorname{cut}\left(\otimes^{n}(\vec{A}), \gamma^{n}\left(\overleftarrow{A}^{\perp}\right)\right) \rightarrow_{m}\left\{\operatorname{cut}\left(A_{i}, A_{i}^{\perp}\right)\right\}_{1 \leqslant i \leqslant n}
$$

For $n M L L_{u}$ proof nets there are moreover the reduction rules [Mau03], called respectively merge-reduction and down-reduction, defined for an arbitrary context $C$ by:

[^1]\[

$$
\begin{gathered}
\operatorname{sum}(\operatorname{sum}(\vec{A}), \vec{B}) \rightarrow_{\text {merge }} \operatorname{sum}(\vec{A}, \vec{B}) \\
\operatorname{sum}(C[\operatorname{sum}(\vec{A})], \vec{B}) \rightarrow_{\text {down }} \operatorname{sum}(\operatorname{sum}(C[\vec{A}]), \vec{B})
\end{gathered}
$$
\]

We define various notions of depth in a natural way: the box-depth associated to sum-boxes, the depth of formulas and the logical depth associated to cuts.

Definition 3. The box-depth $b(l)$ of a link $l$ is the number of sum-boxes that enclose it. The box-depth of a sum-box is the box-depth of its $\square$-link links. The box-depth $b(P)$ of a proof net $P$ is the maximal box-depth of its links.

The depth $d(A)$ of a formula $A$ in a sequent calculus derivation $\pi$ is the length of the longest sequence of rules in $\pi$ since axioms until the introduction of the formula $A$. The depth $d(\pi)$ of a $n M L L_{u}$ sequent calculus derivation $\pi$ is the maximal depth of its cut formulas.

The logical depth $\underline{c}(P)$ of a proof net $P$ is $\underline{c}(P)=\min \{d(\pi) \mid P$ is inferred by the sequent calculus derivation $\pi\}$. We write $c(P)$ the logical depth without counting the sum-rules.

Remark that by definition all $\square$-links of the same sum-box are at same boxdepth. When a proof net is induced by a sequent calculus proof, he keeps traces of sequentialization by the sum-boxes. This corresponds to a stratification by the box-depth that we use in our reduction strategy.

The size $|P|$ of a proof net $P$ is the number of links in $P$ such that we count boxes but not the $\square$-links.

## 3 Parallel reductions

Like the cut-elimination of sequent calculus the reduction of proof net is terminating. Even if the reduction of proof nets has the Church-Rosser property, reductions cannot be done in parallel as there is critical pairs. A critical pair arises when redexes of reduction rules overlap. In order to have efficient proof net reductions we consider all possible critical pairs. E.g. a cut between two distinct axioms has two overlapping redexes depending on if we consider one or the other axioms. This is solved for $M L L_{u}$ in [Ter04] by introducing a tightening reduction: one reduces each maximal alternating chain of cuts and axioms in only one global step leading to what is expected by sequential reduction steps. Here is an example where the maximal chain starts with a cut and finishes with an axiom:


Remark that by maximality, no critical pairs can be tightening redexes. So such reductions can be done in parallel. We denote $t$-reduction a tightening reduction and we write it $\rightarrow_{t}$. We write $\Rightarrow_{t}$ for the simultaneous application of all the $\rightarrow_{t}$ reductions that can be fired in a given proof net. As $\rightarrow_{m}$ redexes are never
critical pairs we also write $\Rightarrow_{m}$ for the simultaneous $\rightarrow_{m}$ reductions. In the rest of the paper we write $\Rightarrow^{k}$ for $k$ parallel reduction steps $\left(\Rightarrow_{t}\right.$ or $\left.\Rightarrow_{m}\right)$ in $M L L_{u}$.

In this section we introduce new reduction rules to answer the parallelization question for $n M L L_{u}$ such that reduction is confluent. We will give a bound on the number of reductions to normalize a proof net (theorem 1).

### 3.1 Parallel reductions of merged sum-boxes

The merge rules applied to distinct sum-boxes immediately within a same sumbox are not really a conflict case even if it is a critical pair. We could merge them in parallel if the sum-box itself would not be merged at the same time. Because the merge-rule is confluent one can consider a global rewriting rule by merging sum-boxes as expected. This corresponds to a sort of innermost sequential strategy. We define a new reduction $\rightarrow_{M}$ whose redex is a maximal tree of successively nested sum-boxes. So the root of the tree of sum-boxes is an outermost sum-box that cannot be merged further. The $\rightarrow_{M}$ reduction merges this tree in the root sum-box as it is mimicked in Fig.5. We do not draw neither wires between the $\square$-links nor the sub-nets which are not sum-boxes. By maximality this reduction has no critical pairs, so we can do simultaneously $\rightarrow_{M}$ reductions.


Fig. 5. $\rightarrow_{M}$ is the global reduction by merging

### 3.2 Parallel down-reductions

According to the context $C$, the pattern $\operatorname{sum}(C[\operatorname{sum}(\vec{A})], \vec{B})$ can reveal a critical pair for the down rule. E.g. within a sum-box $\operatorname{cut}\left(\operatorname{sum}\left(A_{1}, A_{2}\right) \operatorname{sum}\left(B_{1}, B_{2}\right)\right)$ reduces to $\left.\operatorname{sum}\left(\operatorname{sum}\left(\left\{\operatorname{cut}\left(A_{i}, B_{j}\right)\right\}_{1 \leqslant i, j \leqslant 2}\right)\right)\right)$ in four $\rightarrow_{\text {doun }}$ steps. The same holds for a more general context $C$ but remains convergent. So we define a new reduction rule based on the mathematical notion of section of indexed family of elements.

Definition 4. Let $B$ be a sum-box and let $S$ be a sub-net of $B$, i.e. $B=$ $\operatorname{sum}(\vec{X}, S)$. Let $\left\{B_{i}\right\}_{i \in I}$ be the family of nested sum-boxes of $B$ in $S$, i.e. $B_{i} \subset S$ and $b\left(B_{i}\right)=b(B)+1$ for all $i \in I$. Let $C$ be the context of the $B_{i}$ with relation to $S$, i.e. $S=C\left[\left\{B_{i}\right\}_{i \in I}\right]$. We write $\overline{B_{i}}$ the family of sub-nets of $B_{i}$, i.e. $B_{i}=\operatorname{sum}\left(\overline{B_{i}}\right)$. The section $A \triangleleft F$ of a family $F=\left\{\overline{B_{i}}\right\}_{i \in I}$ is a family $A=\left\{a_{k}\right\}_{k \in I}$ such that for all $k \in I$ we have $a_{k} \in \overline{B_{i}}$. We define $\rightarrow_{B}$ the reduction of a sub-net of a sum-box $B$ by:

$$
\operatorname{sum}\left(\vec{X}, C\left[\left\{\operatorname{sum}\left(\overline{B_{i}}\right)\right\}_{i \in I}\right]\right) \quad \rightarrow_{B} \quad \operatorname{sum}\left(\vec{X},\left\{C[A] \mid A \triangleleft\left\{\overline{B_{i}}\right\}_{i \in I}\right\}\right)
$$

We abusively use a set to denote the reduct but the corresponding family is easily obtained from the implicit order associated to sections.

The intuition is to reduce in one (parallelizable) reduction step the sum-boxes $B_{i}$ of one sub-net $S$, merging only affected sum-boxes: the result is a set of subnets which are the combination of all the contents of the $B_{i}$ and the context outside the $B_{i}$. So the sum-boxes $B_{i}$ go down whereas contexts go up. Remark that if the family of $B_{i}$ is such that each $B_{i}$ contains exactly one sub-net then by definition the context $C$ applies on this set of sub-nets.

We give an example: let $B_{1}=\operatorname{sum}\left(a_{1}, a_{2}\right)$ and $B_{2}=\operatorname{sum}\left(b_{1}, b_{2}\right)$, let $C$ be a context without other sum-boxes than $B_{1}$ and $B_{2}$. We have:
$\operatorname{sum}\left(\vec{X}, C\left[B_{1}, B_{2}\right]\right) \rightarrow_{B} \operatorname{sum}\left(\vec{X}, C\left[a_{1}, b_{1}\right], C\left[a_{1}, b_{2}\right], C\left[a_{2}, b_{1}\right], C\left[a_{2}, b_{2}\right]\right)$.
Remark that the affected sum-boxes were merged by our reduction, but just to have a more readable definition. This reduction rule is a generalization of the case of context composed only with cut sum-boxes: in such case we need to reduce at fixed box-depth after all other reductions at this box-depth. But without this generalization we are not able to decrease at the same time the logical depth.

We redefine now $\rightarrow_{B}$ as the previous reduction extended to all sub-nets of the same sum-box $B$. This reduction is well defined as sub-nets are disjoints. We write $\Rightarrow_{D x}$ such reductions applied in parallel to every sum-box at the same box-depth $x-1$. By this way the sum-boxes at box-depth $x$ go down. We do this in order to have no conflicts. For a given proof net $P$, we write $\Rightarrow_{D}{ }^{b(P)}$ the reduction sequence $\Rightarrow_{D b(P)} \cdots \Rightarrow_{D_{1}}$. So with our new reduction rule and our strategy we have the following remarkable properties:

Lemma 1. Let $P$ be a proof net such that $b(P)>0$.
If $\operatorname{sum}(P) \Rightarrow_{D b(P)} \operatorname{sum}\left(P^{\prime}\right)$ then $b\left(P^{\prime}\right)=b(P)-1$ and $c\left(P^{\prime}\right)=c(P)$.
Proof. Let $S$ be a sub-net of a sum-box $B$ of box-depth $b(P)-1$ in $\operatorname{sum}(P)$. We have $b(S)=1$. By definition each link in the reduct of $S$ by $\rightarrow_{B}$ has the box-depth $b(P)-1$. It is the same for all sub-nets of $B$ by $\rightarrow_{B}$ reduction. By definition the $\Rightarrow_{D b(P)}$ reduction applies to all sum-boxes of box-depth $b(P)-1$ in $\operatorname{sum}(P)$. So the conclusion follows.
The above Lemma implies the following:
Lemma 2. Let $P$ be a proof net such that $b(P)>0$.
If $\operatorname{sum}(P) \Rightarrow_{D b(P)} \cdots \Rightarrow_{D 1} \Rightarrow_{M} \operatorname{sum}\left(P^{\prime}\right)$ then we have:
i) $c\left(P^{\prime}\right)=c(P)$ and $b\left(P^{\prime}\right)=0$ i.e. $P^{\prime}$ is sum-box free.
ii) For all choice of summand $s_{i}$ for $i \in I, s_{i}\left(\operatorname{sum}\left(P^{\prime}\right)\right) \Rightarrow^{c(P)} P_{i}$ cut-free.

Theorem 1. There is a sequence of $O(c(P)+b(P))$ parallel reductions which reduces a $n M L L_{u}$ proof net $P$ in a cut-free one.
Proof. Let $P^{\prime}$ defined from $P$ as in lemma 2. Because all the summands $s_{i}$ for $i \in I$ are pairwise disjoint we have $\cup_{i \in I} s_{i}\left(\operatorname{sum}\left(P^{\prime}\right)\right) \Rightarrow{ }^{c(P)} \cup_{i \in I} P_{i}$ cut-free. So $\operatorname{sum}(P) \Rightarrow{ }_{D}^{b(P)} \Rightarrow{ }_{M}^{1} \operatorname{sum}\left(P^{\prime}\right) \Rightarrow{ }^{c(P)} \operatorname{sum}\left(\left\{P_{i}\right\}_{i \in I}\right)$ gives a cut-free proof net.

## 4 Boolean circuits and Boolean proof nets

In this section we recall the definitions and some properties of Boolean circuits and Boolean proof nets. We give also certain relationships between complexity classes associated to Boolean circuits [Coo85,All89,Wol94,Par89]. The novelties concern only the Boolean proof nets of $n M L L_{u}$ that extend those of $M L L_{u}$.

### 4.1 Boolean circuits

A basis is a finite set of sequences of Boolean functions. The standard basis are $\mathcal{B}_{0}=\{\neg, \wedge, \vee\}$ and $\mathcal{B}_{1}=\left\{\neg,\left(\wedge^{n}\right)_{n \in \mathbb{N}},\left(\vee^{n}\right)_{n \in \mathbb{N}}\right\}$. The circuits over basis with an infinite sequence of Boolean functions (resp. without) are called unbounded fan-in (resp. bounded fan-in) circuits. We extend the basis with a $s t C O N N_{2}$ gate to test the strong connectivity of an edge set given in input. As in [Ter04] we will use this gate to simulate the tightening reduction of proof nets.

Definition 5. A deterministic Boolean circuit with $n$ inputs over a basis $\mathcal{B}$ is a directed acyclic graph with $n+1$ sources or inputs (vertices with no incoming edges) and one sink or output (a vertex with no out-going edges). Sources are labeled by literals from $\left\{x_{1}, \ldots, x_{n}\right\} \cup\{1\}$ and nodes of in-degree $k$ are labeled by one of the $k$-ary Boolean functions of $\mathcal{B}$. Non-inputs nodes are called gates, and in-degree and out-degree are called fan-in and fan-out respectively. Let $F_{n}$ be the set of all Boolean functions $f:\{0,1\}^{n} \rightarrow\{0,1\}$ for some $n \in \mathbb{N}$. A deterministic circuit computes a function in $F_{n}$ (or accepts a set $X \subseteq\{0,1\}^{n}$ ) in a natural way.
$A$ non-deterministic Boolean circuit $C$ with $n$ inputs over a basis $\mathcal{B}$ with $k$ nondeterministic variables [Wol94,Par89] is a circuit with $n+k+1$ sources labeled by $\left\{x_{1}, \ldots, x_{n}\right\} \cup\left\{y_{1}, \ldots, y_{k}\right\} \cup\{1\}$ s.t. it computes a function $f \in F_{n}$ as follows: for $x \in\{0,1\}^{n}, f(x)=1$ iff $\exists y \in\{0,1\}^{k}$ a witness s.t. $C(x, y)$ evaluates to 1 . $A$ family of circuits $C=\left(C_{n}\right)_{n \in \mathbb{N}}$ computes a function $f:\{0,1\}^{*} \rightarrow\{0,1\}$ (or accepts a set $X \in\{0,1\}^{*}$ ) if for every $n \in \mathbb{N}$ the circuit $C_{n}$ computes the restriction of $f$ to $F_{n}$.
The size of a circuit is the number of gates and the depth is the length of a longest directed path.

### 4.2 Boolean proof nets

Boolean values are represented with the type $\mathbf{B}=\gamma^{3}\left(\alpha^{\perp}, \alpha^{\perp}, \otimes^{2}(\alpha, \alpha)\right)$. The non-deterministic Boolean values are represented with the same type! Let us consider the following proof nets of type $\mathbf{B}$ : the non-deterministic Boolean represented by $b_{2} \equiv \operatorname{sum}\left(b_{0}, b_{1}\right)$ where $b_{0}$ and $b_{1}$ are the two cut-free proof nets of $M L L_{u}$ respectively called false and true given in Fig.3.
Definition 6 ([Ter04]). A Boolean proof net with $n$ inputs $\vec{p}=p_{1}, \ldots, p_{n}$, one output and $k(n)$ sum-boxes is a proof net $P(\vec{p})$ of $n M L L_{u}$ of type:

$$
\vdash p_{1}: \mathbf{B}^{\perp}\left[A_{1}\right], \ldots, p_{n}: \mathbf{B}^{\perp}\left[A_{n}\right], q: \otimes^{m+1}(\mathbf{B}, \vec{C})
$$

with $k(n)$ a function of $n$, and some $\vec{A} \equiv A_{1}, \ldots, A_{n}$ and $\vec{C} \equiv C_{1}, \ldots, C_{m}$ where we denote $\mathbf{B}[A]$ the formula $\mathbf{B}$ where all occurrences of $\alpha$ are substituted by A. Given $\vec{x} \equiv b_{i_{1}}, \ldots, b_{i_{n}}$ we write $P(\vec{x})$ the proof net where we cut every $b_{i}$ with $p_{i}$.

Without loss of generality we can always set $\operatorname{sum}(P)$ for $P$ : if a Boolean proof net $P$ is without sum-boxes then $\operatorname{sum}(P)$ is a sum-box with only one summand in $M L L_{u}$. This generalizes uniform $M L L_{u}$ Boolean proof nets. We often omit this initial sum-box for the sake of simplicity.

Following the definition, for a given $\vec{x} \equiv b_{i_{1}}, \ldots, b_{i_{n}}$, a Boolean proof net $\operatorname{sum}(P(\vec{x}))$ is of type $\otimes^{m+1}(\mathbf{B}, \vec{C})$ and reduces in a unique cut-free proof net of the same type (e.g. by the reduction sequence of the previous section). We say that $\operatorname{sum}(P(\vec{x}))$ evaluates to 1 iff one of its summands is $b_{1}$ with some garbage $\vec{C}$. There is an asymmetry between 1 and 0 as for non-deterministic Turing machines.

Definition 7 ([Ter04]). A Boolean proof net $P(\vec{p})$ with $n$ inputs computes $a$ function $f:\{0,1\}^{n} \rightarrow\{0,1\}$ (or accepts a set $X \subseteq\{0,1\}^{n}$ ) if $P(\vec{x})$ evaluates to $b_{f(x)}$ for every $\vec{x} \equiv b_{i_{1}}, \ldots, b_{i_{n}}$ corresponding to $x \equiv i_{1} \cdots i_{n} \in\{0,1\}^{n}$.

The Boolean proof net encoding of K. Terui [Ter04] of functions as negation, conditional, disjunction, composition, duplication, and so on, is trivially valid in the extended setting of $n M L L_{u}$. We give some of them in the Appendix.

### 4.3 Uniformity and complexity classes

The given notions are used for Boolean circuit families and for $n M L L_{u}$ proof net families. Both are denoted by $F=\left(F_{n}\right)_{n \in \mathbb{N}}$. From an algorithmic point of view the uniformity is an important issue because only a uniform family can be regarded as an implementation of an algorithm.

Definition 8 ([Ruz81]). A family $F=\left(F_{n}\right)_{n \in \mathbb{N}}$ is called L-uniform (respectively $P$-uniform) if there is a function which computes a description of $F_{n}$ from $1^{n}$ (unary numeral) in space $O\left(\log \left|F_{n}\right|\right)$ ) (respectively in time $\left.\left|F_{n}\right|^{O(1)}\right)$.

A description means all informations about elements like sort, predecessors, ... Usually the description is also chosen according to the uniformity notion used. Because we work with $N P$ the $P$-uniformity is sufficient [All89]. Nevertheless if we want to study a property in $N C$ then we use the $L$-uniformity with the following notion of description where links and sorts are identified by binary numbers. Let $W$ be the set of binary words and let $\bar{x}$ be the binary representation of the integer $x$.

Definition 9 ([Ruz81]). The direct connection language $L_{D C}(C)$ of a Boolean circuit family $C=\left(C_{n}\right)_{n \in \mathbb{N}}$ over basis $\mathcal{B}$ is the set of tuples $\langle\bar{y}, \bar{g}, \bar{w}, \bar{b}\rangle \in W^{4}$, where for $y=n$ we have $g$ is a gate in $C_{n}$ labeled by the function $b$ from $\mathcal{B}$ if $\bar{w}=\varepsilon$ else $b$ is the $w^{t h}$ predecessor gate of $g$. In case of input, $b$ is not a function but a sort (deterministic input or non-deterministic variable).

This definition adapted to $M L L_{u}$ proof nets [MR07] is here extended analogously to $n M L L_{u}$ proof nets:

Definition 10. The direct connection language $L_{D C}(P)$ of a $n M L L_{u}$ proof net family $P=\left(P_{n}\right)_{n \in \mathbb{N}}$ is the set of tuple $\langle\bar{y}, \bar{l}, \bar{w}, \bar{b}, \bar{s}\rangle \in W^{5}$ where for $y=n$ we have $l$ is a link in $P_{n}$ of sort $b$ if $\bar{w}=\varepsilon$ else the $w^{\text {th }}$ premise of $l$ is the link $b$. The link $l$ is also enclosed by the $s^{\text {th }}$ sum-box.

Remark that the length of all identifiers of such family $P$ is bounded by $\log |P|$.
We finish this section recalling the complexity classes defined with uniform families of Boolean circuits ([Coo85,All89,Vol99] and [Wol94,Par89] for nondeterministic Boolean circuits). All dimensions are defined w.r.t the length of the input, which we write everywhere $n$. We use abusively the words poly for $n^{O(1)}$ and polylog for $\log ^{O(1)}(n)$.

The classes $N C^{i}$ and $A C^{i}$ for $i \geqslant 0$ are the functions computable by uniform families of polynomial size, $O\left(\log ^{i} n\right)$ depth circuits over $\mathcal{B}_{0}$ and $\mathcal{B}_{1}$ respectively. We add the suffix $\left(s t C O N N_{2}\right)$ to classes if we extend basis with a $s t C O N N_{2}$ gate. The class $N N C^{i}(k(n))$ (resp. $\left.N A C^{i}(k(n))\right)$ are the functions computable by $N C^{i}$ (resp. $A C^{i}$ ) circuit families with $O(k(n))$ non-deterministic variables. We write $N C, N N C(k(n)), A C$ and $N A C(k(n))$ the respective unions over the exponents of the depth. Relationships between complexity classes follow: $\forall i \in \mathbb{N}$ and $\forall j \in \mathbb{N}^{*}$

$$
\begin{gathered}
A C^{0} \subsetneq N C^{1} \subseteq L \subseteq N L \subseteq A C^{1} \subseteq N C^{2} \subseteq \ldots \subseteq A C=N C \subseteq P \\
A C^{i} \subseteq A C^{i}\left(s t C O N N_{2}\right) \subseteq A C^{i+1} \\
N N C^{j}(\log n)=N C^{j}, \text { and then } N N C(\log n)=N C \\
N N C^{j}(\text { poly })=N N C(\text { poly })=N A C^{i}(\text { poly })=N A C(\text { poly })=N P
\end{gathered}
$$

## 5 Translation and simulation

### 5.1 Logspace translation

Let $C=\left(C_{n}\right)_{n \in \mathbb{N}}$ be a uniform Boolean circuit family over the basis $\mathcal{B}_{1}\left(s t C O N N_{2}\right)$ with non-deterministic variables. The intermediate proof nets we build are called modules.

First of all without distinguishing the inputs, we associate a uniform Boolean proof net family $P=\left(P_{n}\right)_{n \in \mathbb{N}}$ to $C$ using uniformity. After what we consider non-deterministic variables. Indeed the uniformity function of Boolean circuits builds the uniformity function of Boolean proof nets in the same way as in [MR07]. The main idea is already given in [Ter04]. Starting from $L_{D C}(C)$ :

- For each $n$-fan-in gate labeled $f(n)$ read in $L_{D C}\left(C_{n}\right)$ we give a polysize module computing $f(n)$. If the gate is a non-deterministic variable we cut the corresponding input with the proof net $b_{2} \equiv \operatorname{sum}\left(b_{0}, b_{1}\right)$,
- For each $n$-fan-out gate read in $L_{D C}\left(C_{n}\right)$ we make a polysize duplication,
- For each edge read in $L_{D C}\left(C_{n}\right)$ we joint modules and duplications.

Just parsing the $L_{D C}\left(C_{n}\right)$ for $i=0$ to $\left|C_{n}\right|$ we detail a Logspace translation into $L_{D C}\left(P_{n}\right)$ : everything is identified with a binary number

1. For each $\langle n, i, \varepsilon, b\rangle$ we build the module associated to the function $b$ of the basis of the family (or to the sort $b$ in case of inputs). It is a subset of $L_{D C}\left(P_{n}\right)$ where relationships between links and sorts are given.
2. If there are several $\langle n, i, k, j\rangle$ (i.e. $j$ is the $k$-th predecessor of $i$ ) for fixed $n$ and $j$ then the fan-out of $j$ is multiple. We build the corresponding duplication. It is again a subset of $L_{D C}\left(P_{n}\right)$.
3. For each $\langle n, i, k, j\rangle$ (i.e. $j$ is the $k$-th predecessor of $i$ ) we build $\langle n, a, b, c, 0\rangle$ (i.e. an edge from $(a, 0)$ to $(b, c))$ where $a$ is the link associated to the output of the module (step 1) corresponding to $j$ and $b$ is the link associated to the $c$-th input of the module corresponding to $i$, modulo added duplications.

The novelty is the translation of non-deterministic variables into cuts with $b_{2} \equiv$ $\operatorname{sum}\left(b_{0}, b_{1}\right)$. Only in this case the last bit of the description is not null.

Theorem 2. For every uniform family C of unbounded fan-in Boolean circuit of size $s$ and depth $c$ over the basis $\mathcal{B}_{1}\left(s t C O N N_{2}\right)$ and with $k(n)$ non-deterministic variables, there is a uniform family of Boolean proof nets of $n M L L_{u}$ of size $s^{O(1)}$ and logical depth $O(c)$ and with $k(n)$ sum-boxes, which accepts the same set as $C$ does.
Proof. Let $C_{n} \in C$ and $P_{n} \in P$ the Boolean proof net obtained by translation. By translation $b\left(P_{n}\right)=1$. Every gate is translated by a module of size $O\left(s^{4}\right)$ and constant depth, and only the composition of these modules increases linearly the depth [Ter04]. Let $x \in\{0,1\}^{n}$ an input of $C_{n}$ and $\vec{x}$ corresponding to $x$ as in definition 7. By theorem 1 proof we have: $\operatorname{sum}\left(P_{n}(\vec{x})\right) \Rightarrow{ }_{D 1}^{1} \Rightarrow_{M}^{1} \operatorname{sum}\left(\left\{P_{i}\right\}_{i \in I}\right) \Rightarrow^{c}$ $\operatorname{sum}\left(\left\{Q_{i}\right\}_{i \in I}\right)$ is an $O(c)$ steps reduction such that $\operatorname{sum}\left(\left\{Q_{i}\right\}_{i \in I}\right)$ is cut free and there is a witness $y \in\{0,1\}^{k(n)}$ such that $C_{n}(x, y)$ evaluates to 1 if and only if $P_{n}(\vec{x})$ evaluates to 1 (i.e. $\exists i \in I$ such that $Q_{i}=b_{1}$ ).
By translation $c=O(\underline{c})$, so we have the same result for logical depth $O(\underline{c})$.

### 5.2 Simulation of parallel reductions

Let $P=\left(P_{n}\right)_{n \in \mathbb{N}}$ be a uniform Boolean proof net family of $n M L L_{u}$ with $k(n)$ sum-boxes. We associate a uniform Boolean circuit family $C=\left(C_{n}\right)_{n \in \mathbb{N}}$ to $P$ in two big steps based on the reduction sequence of theorem 1:

- We both initialize the circuit descriptions and simulate all the parallel down reductions with a polysize and constant depth circuit, using uniformity,
- As in [Ter04] we simulate all the $\Rightarrow$ reductions of all summands using $s t C O N N_{2}$ gates for $\Rightarrow_{t}$ simulation and then we check the result of the last configuration.

In more detail, from the description of a proof net $P_{n} \in P$ we build $\Theta_{0}$ an initial set of Boolean values representing the proof net to simulate. A configuration $\Theta \in \operatorname{Conf}\left(P_{n}\right)$ is the set of the following Boolean values: alive $(l), \operatorname{sort}(l, s)$,
$b o x(l, i)$ and $\operatorname{edge}\left(l, 0, l^{\prime}, i^{\prime}\right)$. These values are initialized to 1 respectively iff a link $l \in L$ is in $P_{n}, l$ is of sort $s, l$ is enclosed by the sum-box numbered $i$, and the conclusion port of $l$ is in relation with the $i^{\prime t h}$ port of a link $l^{\prime} \in L$. In other terms our initial configuration $\Theta_{0}$ is the description itself extended to alive values. Every reduction step simulation is made by a small circuit which modifies a configuration in the other. All of this can be done in Logspace. The parallel reduction strategy is simulated as follows: this is the tricky part

Lemma 3. There is an unbounded fan-in circuit $C$ of size $O\left(\left|P_{n}\right|^{3}\right)$ and constant depth over $\mathcal{B}_{1}$ with non-deterministic variables, which computes in Logspace $\Theta \in$ $\operatorname{Conf}\left(P^{\prime}\right)$ from $\Theta_{0} \in \operatorname{Conf}\left(P_{n}\right)$ whenever $P_{n}\left(b_{i_{1}}, \ldots, b_{i_{n}}\right) \Rightarrow{ }_{D}^{b\left(P_{n}\right)} \Rightarrow{ }_{M}^{1} P^{\prime}$ from given inputs $i_{1}, \ldots, i_{n}$.
Proof. Like in the translation we parse the configuration of $P_{n}\left(b_{i_{1}}, \ldots, b_{i_{n}}\right)$ without taking care of sum-boxes to build partially $L_{D C}(C)$ in Logspace. From $\operatorname{Conf}\left(P_{n}\right)$, we complete $L_{D C}(C)$ in Logspace and compute $\operatorname{Conf}\left(P^{\prime}\right)$ as follows: The simulation of one $k$-ary sum-box $t$ which corresponds to $k$ summands/choices, uses $\log (k)$ non-deterministic variables $\left\{G^{t}\right\}$. Let $l$ be a link of box-depth $b(l)$, i.e. $l$ is enclosed in exactly $b(l)$ sum-boxes $\left\{t_{i}\right\}_{i \in I}$. Let $k_{i}$ be the arity of the sum-box $t_{i}$. So the link $l$ simulation depends on $\Sigma_{i \in I} \log \left(k_{i}\right)$ non-deterministic gates. We initialize the value of the corresponding edges with the conjunction of the values of these non-deterministic gates $\cup_{i \in I}\left\{G^{t_{i}}\right\}$ like in Fig. 6. Globally this constant depth initialization uses one conjunction gate by edge in $\operatorname{Conf}\left(P_{n}\right)$ and one negation gate by non-deterministic gates.


Fig. 6. $2^{2}$-ary sum-box simulation where $e d g e_{i}^{\prime}$ is in the $i^{t h}$ summand

Lemma 4. [Ter04] There is an unbounded fan-in circuit $C$ over $\mathcal{B}_{1}\left(s t C O N N_{2}\right)$ of size $O\left(|P|^{3}\right)$ and constant depth such that whenever a configuration $\Theta \in$ $\operatorname{Conf}(P)$ is given as input and $P \Rightarrow P^{\prime}, C$ outputs a $\Theta^{\prime} \in \operatorname{Conf}\left(P^{\prime}\right)$.

Theorem 3. For every uniform family $P$ of Boolean proof nets of $n M L L_{u}$ of size $s$ and logical depth $c$, with $k(n)$ sum-boxes of maximal arity $k$, there is a uniform family of unbounded fan-in Boolean circuit over the basis $\mathcal{B}_{1}\left(s_{C O N} N_{2}\right)$ of size $s^{O(1)}$ and depth $O(c)$ and with $O(k(n) \cdot \log (k))$ non-deterministic variables, which accepts the same set as $P$ does.

Proof. By theorem 1, $i_{1} \cdots i_{n}$ is accepted by $P$ if and only if $b_{1} \in\left\{Q_{i}\right\}_{i \in I}$ where $P_{n}\left(b_{i_{1}}, \ldots, b_{i_{n}}\right) \Rightarrow{ }_{D}^{b(P)} \Rightarrow{ }_{M}^{1} P^{\prime} \Rightarrow{ }^{c(P)} \operatorname{sum}\left(\left\{Q_{i}\right\}_{i \in I}\right)$. By lemma 3 we build from $P_{n}$ a uniform polysize constant depth circuit with $O(k(n) \cdot \log (k))$ nondeterministic variables. For each of the $\Rightarrow^{c(P)}$ reductions we apply the same construction as in Terui's lemma starting from our previous configuration. At the end we easily build a polysize constant depth circuit to check if the last configuration represents $b_{1}$ or not.

## 6 Proof net complexity

We define a hierarchy of complexity classes based on proof nets:
Definition 11. For $i \in \mathbb{N}$, the classes $U B P N^{i}(k(n))$ and $U B P N^{i}$ are functions computable by uniform families of polynomial size, $O\left(\log ^{i} n\right)$ depth Boolean proof nets of respectively $n M L L_{u}$ with $O(k(n))$ sum-boxes and $M L L_{u}$. We write $U B P N(k(n))$ and UBPN the respective unions over the exponents of the depth.

From theorem 2 and theorem 3 we have:

Theorem 4. For all $i \in \mathbb{N}$,
$N A C^{i}(k(n))\left(s t C O N N_{2}\right) \subseteq U B P N^{i}(k(n)) \subseteq N A C^{i}(k(n) \times \log n)\left(s t C O N N_{2}\right)$
Proof. For a Boolean proof net of size $s$ the arity $k$ of a sum-box is $O(s)$ in the worst case. By theorem 2 we have $O(s)=n^{O(1)}$. So $O(k(n) \cdot \log k)=$ $O(k(n) \times \log n)$.

Corollary 1. For all $i, j \in \mathbb{N}$,

1. $U B P N^{i}($ poly $)=N A C^{i}($ poly $)($ stCONN 2$)=N P$,
2. $N A C^{i}\left(l o g^{j} n\right)\left(s t C O N N_{2}\right) \subseteq U B P N^{i}\left(l o g^{j} n\right) \subseteq N A C^{i}\left(l o g^{j+1} n\right)\left(s t C O N N_{2}\right)$,
3. $\operatorname{UBPN}(1)=U B P N=N C$,
4. $U B P N(\log n) \supseteq N C$,
5. $U B P N($ polylog $)=N N C($ polylog $)$,
6. $U B P N($ poly $)=N N C($ poly $)=N P$.

Proof. Point 1. $O\left(n^{O(1)} \times \log n\right)=O\left(n^{O(1)}\right)$.
Point 3. NAC $(1)\left(s t C O N N_{2}\right)=N C=N N C(\log n)=N A C(\log n)\left(s t C O N N_{2}\right)$.
Point 5. by union over $i$ and $j$ from Point 2.
Point 6 . by union over $i$ from Point 1.
Remark that $U B P N(1)=U B P N$ is what we expect: a constant number of sum-boxes corresponds to $n^{O(1)}$ summands/choices in the worst case, and so it can be simulated with a disjunction of a polynomial number of circuits of the same depth. I.e. it corresponds to $N N C(\log n)=N C$.

## 7 Conclusion

Our study generalizes the work of K. Terui [Ter04] to non-deterministic extension as follows. We have defined new parallel reductions of proof nets of Nondeterministic Multiplicative Linear logic, $n M L L_{u}$. We have defined the uniform Boolean proof nets with an amount of explicit non-determinism analogously to the uniform Boolean circuits of $N N C()$, the non-deterministic $N C$ class. By the way we give a proof-as-programs correspondence between this model of parallel computation and the uniform Boolean proof nets, preserving both size and depth. We define in a standard way the classes of uniform Boolean proof nets families $\operatorname{UBPN}()$ and we establish the following results:

$$
\begin{array}{cc}
N C=\operatorname{UBPN}(1) \subseteq U B P N(\log n) \subseteq U B P N(\text { polylog }) \subseteq U B P N(\text { poly })=N P \\
\| \\
U B P N & N N C(\text { polylog })
\end{array} \quad N N C(\text { poly })
$$

Remark that the proofs of translation and simulation theorems could apply for Boolean circuits without depth constraint. Such a Boolean circuit is simply called a polynomial size circuit and the corresponding class equal $P$. So there is a chain from $P$ to $N P$ for families of uniform polynomial size Boolean proof nets.

There is a reduction which replaces a sequence of $\Rightarrow_{D}$ in our theorem in only one step: our circuit simulating parallel down reductions made it in lemma 3. The same thing could be done by parsing the sub-nets without reducing sumboxes (only $k(n) \cdot \log (n)$ bits are used) but with our theorem reduction we do fully parallel computation. Ad-hoc Boolean proof net classes can be given to have a more strictly correspondence with $N N C()$, using only binary $\square$-links. Then the encoding are no more constant depth but $O(k(n))$ depth: it corresponds to $N C$ with $O(\log n)$ sum-boxes. Remark that if we use a bigger uniformity notion then the descriptions are easier: e.g. for sum-boxes a relationship between the $\square$-links is sufficient.

Acknowledgments. I would like to thank the reviewers for their remarks and the improvements which they suggested.

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## A Appendix: functions in Boolean proof nets

The ports of the proof nets are labeled by letters rather than numbers, so that proof nets described only by the name of their links are more readable. For a given link $l$, we denote $l_{q}^{p_{1}, \ldots, p_{n}}$ if its conclusion port is $q$ and the other ports are respectively $p_{1}, \ldots, p_{n}$ in this order. Axiom link are more simply denoted $a x_{p}$.

The conditional (if-then-else) is the base of the Terui's gates translations: given two proof nets $P_{1}$ and $P_{2}$ of types $\vdash \Gamma$, $p_{1}: A$ and $\vdash \Delta, p_{2}: A$ resp., one can build a proof net $\operatorname{cond}_{r}^{p_{1}, p_{2}}\left[P_{1}, P_{2}\right](q)$ of type $\vdash \Gamma, \Delta, q: \mathbf{B}[A]^{\perp}, r: A \otimes A$ (Fig.7(b)). Given a cut between $b_{i}$ and $q$ we have:

```
\(\operatorname{cond}_{r}^{p_{1}, p_{2}}\left[P_{1}, P_{2}\right]\left(b_{1}\right) \rightarrow^{*}\) tensor \(_{r}^{p_{1}, p_{2}}\left(P_{1}, P_{2}\right)\)
cond \(_{r}^{p_{1}, p_{2}}\left[P_{1}, P_{2}\right]\left(b_{0}\right) \rightarrow^{*}\) tensor \(_{r}^{p_{2}, p_{1}}\left(P_{2}, P_{1}\right)\).
```



Fig. 7. (a) The Boolean $b_{0}, b_{1}$ and (b) The conditional Boolean proof net

Disjunction, conjunction and duplication are based on the conditional: let $n \geqslant 2$ be an integer and $C \equiv \otimes\left(\mathbf{B}\left[A_{1}\right], \ldots, \mathbf{B}\left[A_{n}\right]\right)$, we have:

$$
\begin{aligned}
& \operatorname{or}\left(p_{1}, p_{2}\right) \equiv \operatorname{cond}\left[b_{1}, a x_{p_{1}}\right]\left(p_{2}\right) \text { of type } \vdash p_{1}: \mathbf{B}^{\perp}, p_{2}: \mathbf{B}[\mathbf{B}]^{\perp}, q: \mathbf{B} \otimes \mathbf{B}, \\
& \operatorname{and}\left(p_{1}, p_{2}\right) \equiv \operatorname{cond}\left[a x_{p_{1}}, b_{0}\right]\left(p_{2}\right) \text { of type } \vdash p_{1}: \mathbf{B}^{\perp}, p_{2}: \mathbf{B}[\mathbf{B}]^{\perp}, q: \mathbf{B} \otimes \mathbf{B},
\end{aligned}
$$

$$
\operatorname{copy}^{n}(p) \equiv \operatorname{cond}\left[\text { tensor }\left(\overrightarrow{b_{1}}\right), \text { tensor }\left(\overrightarrow{b_{0}}\right)\right](p) \text { of type } \vdash p: \mathbf{B}^{\perp}[C], q: C \otimes C
$$

The composition of two translated circuits is defined by:
let $\Gamma \equiv p_{1}^{\prime}: A_{1}^{\prime}, \ldots, p_{n}^{\prime}: A_{n}^{\prime}$ and $\Delta \equiv q_{1}^{\prime}: B_{1}^{\prime}, \ldots, q_{n}^{\prime}: B_{m}^{\prime}$, let $P\left(\overrightarrow{p^{\prime}}\right)$ and $Q\left(\overrightarrow{q^{\prime}}\right)$ be proof nets of type $\vdash \Gamma, p: \otimes^{2}(\mathbf{B}, \vec{C})$ and $\vdash q: \mathbf{B}^{\perp}[A], \Delta, r: \otimes^{2}(\mathbf{B}, \vec{D})$, respectively. Then we have:

$$
\operatorname{comp}_{s}^{p, q, r}[P, Q]\left(\overrightarrow{p^{\prime}}, \overrightarrow{q^{\prime}}\right)
$$

is of type $\vdash \Gamma[A], \Delta, s: \otimes^{2}(\mathbf{B}, \vec{D}, \overrightarrow{C[A]})$. With this composition one can construct $n$-ary versions of conjunction and disjunction.


[^0]:    * Work partially supported by the french project Complice (ANR-08-BLANC-0211-01)

[^1]:    ${ }^{1}$ Up to the order when they are not drawn

