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Electrical properties of short period InAs/GaSb superlattice.

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Electrical properties in the temperature range between 80K and 300K of type-II short period InAs/GaSb superlattice (SL) photodiode are reported. Resistivity and Hall measurements have been carried out on a 300 periods unintentionally doped SL grown on semi-insulating GaAs substrate while capacitance-voltage and electrical-voltage measurements have been performed on the same SL structure elaborated on n-type GaSb substrate. Whatever the electrical investigations, the behaviour of the InAs/GaSb SL versus temperature exhibited a reproducible change in type of conductivity. The SL is n-type at high temperatures range with $n(300K) = 6x10^{16} \text{cm}^{-3}$ whereas it is p-type at low temperatures with $p(100K) = 2x10^{16} \text{cm}^{-3}$.

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1 Introduction

InAs/GaSb superlattice (SL) system is an attractive quantum system for infrared (IR) detection devices thanks to several theoretical advantages induced by its unique type II broken gap band alignment [1, 2]. Indeed, in this misaligned configuration, the effective band gap can be easily tailored by adjusting the layer thicknesses in order to reduced Auger recombination rate in comparison with bulk materials [3, 4] and to cover a wide range of wavelengths including the 3-5 μ m mid-wavelength infrared (MWIR) and the 8-12 μ m long wavelength infrared (LWIR) atmospheric windows. This last feature can be exploited for the realization of expected multicolor detection module. InAs/GaSb SL p-i-n photodiodes with noticeable detection performances were firstly reported for the LWIR domain [5-7] and more recently in the MWIR spectral range [8-10] with, in particular, a thermal imaging demonstration by using a focal plane array camera with 256x256 detector elements [11]. Nevertheless, improvement of device technology [12, 13] as well as better knowledge on fundamental properties of this binary/binary SL photodiodes [14-16] are still necessary to reach the criteria of third generation IR detectors [17].

In this paper, we report on results of electrical properties of short period InAs/GaSb SL adapted for detection in the MWIR domain. The objective of this work is to examine the behaviour of the InAs/GaSb SL as a function of temperature, exhibiting a reproducible change in type of conductivity.

2 InAs/GaSb superlattice samples description

Two kinds of samples were fabricated by molecular beam epitaxy (MBE). InAs/GaSb unintentionallydoped (nid) SL structures were grown on semi-insulating (001) GaAs substrate to perform hall measurement and on n-type (001) GaSb for electrical-voltage and capacitance-voltage investigations. Whatever the substrate, the SL's period is composed by equal number N=10 of InAs and GaSb monolayers

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(MLs) including one InSb ML at the InAs-GaSb interface. The aim of the inserted InSb layer is to eliminate the "GaAs-like" interface and to compensate the tensile effect of the InAs layers in order to achieve SL structure lattice matched to the GaSb substrate. This "GaSb" strain compensated SL structure exhibits room temperature cut-off energy near 220 meV (5.6 µm) [18].

For the sample on GaAs substrate, a 500 nm GaAs buffer layer was grown before the nid SL while a 500 nm Te-doped GaSb buffer layer was inserted in the case of n-type GaSb substrate. The nid short period SL is composed of 300 periods, corresponding to a total active zone thickness of 1.92µm, and the cap layer was already made by the last GaSb layer of the nid SL.

Further details of the epitaxial growth and GaSb(10MLs)/InAs(10MLs)/InSb(1ML) SL structural characterizations have been previously published [18]. Figures 1a and 1b report high-resolution X-ray diffraction spectra of 300 periods SL structure grown on GaAs and GaSb substrates, respectively.

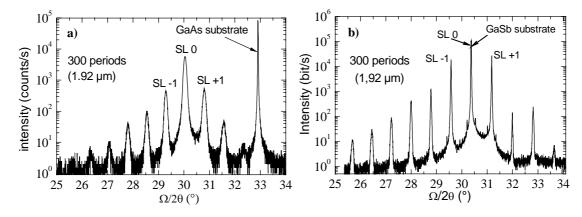


Fig. 1. High-resolution X-ray diffraction pattern of the GaSb(10MLs)/InAs(10MLs)/InSb(1ML) SL structures grown on GaAs substrate (a) ; on GaSb substrate (b).

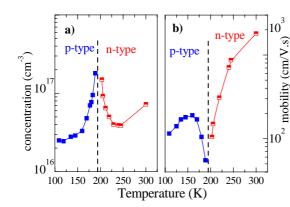
In the case of the SL structure grown on GaSb substrate (Fig. 1b), X-ray diffraction rocking curve reveals many intense satellite peaks attesting a good crystalline quality of the layers and the zeroth-order SL peak position indicates that the SL is practically lattice matched (-0.03%) to the GaSb substrate. Comparison with curve simulation leads to a measured period of 63.56 Å very close to the intended period of 64 Å. Because of the discrepancy between the average lattice constant of the SL and the lattice constant of the GaAs substrate (-7.2%), the X-ray diffraction peaks observed in Fig. 1a are less intense but numerous. Up to the fifth satellite peaks can be noted with linewidth comparable to those recently reported for InAs/GaSb SL grown on GaAs by metalorganic chemical vapor deposition [19].

InAs/GaSb superlattice electrical properties

Resistivity and Hall measurements have been performed on the nid SL grown on semi-insulating GaAs substrate to determine the number and type of conduction channels in the SL. Measurements have been done as a function of temperature (77-300K) for magnetic fields in the [0-1] Tesla range. Temperature dependence of apparent carrier concentration and Hall mobility is presented in Fig. 2. A reproducible change in type of conductivity of the nid SL was noted at around 190K. The SL is n-type at high temperatures range with $n(300K) = 6x10^{16} \text{ cm}^{-3}$ and a mobility of $1800 \text{ cm}^2/\text{V}$.s while it is p-type at low temperatures with $p(100K) = 1.5x10^{16} \text{ cm}^{-3}$ with a poor mobility value of $100 \text{ cm}^2/\text{V}$.s. This change in conductivity was already observed and reported but at lower temperatures 140K [3]. A simple two carri-ers analysis (electrons rather confined in InAs layer and holes in GaSb) cannot explain this versatile phenomena. To obtain the agreement with the experiments, it is necessary to consider a multi-carrier analysis approach including a third type of carriers with a density of 10¹³ cm⁻³ and a mobility of $10^5 \text{ cm}^2/\text{Vs}$ [3]. Undoubtedly, the presence of these carriers can be attributed to the influence of the in-serted InSb layer at the InAs-GaSb interface.

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Figure 3 displays resistivity results versus 1/kT. The data show the temperature dependence of the resistivity with two well-defined slopes below and above 190K. From these two regimes, we can extract two thermal activation processes characterized by the activation energies Ea=29meV and Ed=136meV at low and high temperatures, respectively. These two values strongly suggest the influence of deep levels or traps in such SL structure. At low temperature, the SL is p-type because acceptor traps dominate the carrier generation and recombination mechanisms through the traps. At higher temperature, deep donor traps can then be activated leading to an n-type SL structure, especially at room temperature.



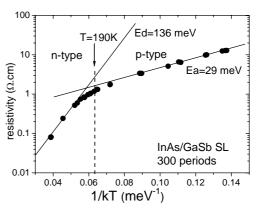


Fig.2: Measured carrier concentration (a) and Hall mobility (b) of the nid SL versus temperature. The Hall coefficient converts from p-type at low temperature to n-type at around 190 K.

Fig.3: Resistivity versus inverse temperature showing two regimes with two activation energies at high and low temperatures.

In order to validate such previous observations, another kind of electrical investigations were planned. Mesa devices were prepared with the nid SL structure grown on n-type GaSb substrate $(1x10^{18} \text{ cm}^{-3})$, by using standard photolithography and wet etching techniques. Au/Zn ohmic contacts have been realized on the cap layer structure made by the last GaSb layer of the nid SL. The diodes were not passivated.

Figure 4 presents the current-voltage (I-V) and capacitance-voltage (C-V) characteristics measured at several temperatures for a device of 350µm diameter. The device shows rectifying characteristics in the whole temperature range 300K-80K (Fig. 4a). However, we can easily note the change in slope of forward bias characteristics below 190K. In the same way, the C-V curves begin to exhibit anomalous behavior around the same temperature (Fig. 4b). These observed uncharacteristic electrical properties are signatures of reverse polarity induces by change in type of conductivity of the SL structure. A CHANGER

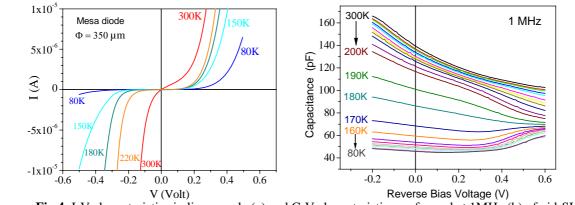


Fig.4: I-V characteristics in linear scale (a) and C-V characteristics performed at 1MHz (b) of nid-SL / n-GaSb heterojunction recorded at several temperatures

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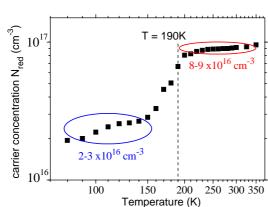


Fig.5 : Reduced carrier concentration N_{red} of the undoped SL versus temperature.

From linear C-V data and the theory of abrupt heterojunction, we can extract the reduced carrier concentration $1/N_{red}=1/N_A+1/N_D$ by using [16, 20] :

$$\left(\frac{A}{C(V)}\right)^2 = \frac{2V_d}{q\varepsilon_r\varepsilon_0 N_{red}} - \frac{2}{q\varepsilon_r\varepsilon_0 N_{red}} V$$

where A is the area and V_d is the built-in potential of the diode ; ε_r is the relative effective dielectric constant value of the multiplayer SL taken equal to 15.4. Figure 5 shows the temperature dependence of N_{red} . In the case of depletion at an np or pn junction, N_{red} represents the lower carrier concentration without precisions concerning the polarity of the carrier background of the nid SL region. However, these measurements confirm previous Hall results with a carrier concentration at around 2-3x10¹⁶cm⁻³ at low temperature and around 8-9x10¹⁶cm⁻³ above 200K.

In summary, we report electrical measurements performed on short-period InAs/GaSb SL in the temperature range between 80K and 300K. Whatever the electrical experiments (conventional hall effect, I-V and C-V), the nid InAs/GaSb SL reveals an unsettling change in type of conductivity. The SL is p-type at low temperature below 190K while is n-type at higher temperature. The accurate understanding of this versatile p or n-type behaviour of the SL active region is a key point on the way to improve InAs/GaSb system for mid-infrared detection.

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